

## Product Change Notice (PCN)

**Subject:** Add SFA, Korea as Alternate Wafer Sort Location (CP1)

**Publication Date:** 10/28/2024

**Effective Date:** 1/27/2025

**Revision Description:**

Initial Release

**Description of Change:**

Renesas is adding SFA, Korea as an alternate Wafer Sort Location (CP1) for the select part# that are currently wafer sorted at Carsem, Malaysia. Presently, SFA Korea is the qualified test location for CP2. This change will allow flexibility to test from either facility and will provide increased capacity, flexibility and shorter lead time to meet market demand. Test processing flows and test programs are the same at all the qualified facilities. Renesas has completed the electrical test correlation and based on the test results we do not anticipate any impact on device performance. The testing is fully compatible and transferrable between the two test facilities with no change to the test coverage.

**Affected Product List:** TS5111-Z2AHRI, TS5111-Z2AHR18

**Reason for Change:**

To provide increased capacity.

**Impact on Fit, Form, Function, Quality & Reliability:**

The change has no impact on the form, fit, function, quality, reliability and environmental compliance of the product.

**Product Identification:**

Traceability to the wafer sort location is available on request.

**Qualification Status:** Electrical characterization completed successfully. Refer Appendix A

**Sample Availability Date:** 12/1/2024

**Device Material Declaration:** Not applicable

Note:

1. Acknowledgement must be received by Renesas within 30 days or Renesas will consider the change as approved.
2. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN to make any objections to this PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved.
3. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

For additional information regarding this notice, please contact [idt-pcn@lm.renesas.com](mailto:idt-pcn@lm.renesas.com)

**Appendix A - Electrical Test Characterization Results**

**Qual Vehicle:** TS5111-Z2AHRI

**Sample size:** 1 Wafer with DPW of 25480

Descriptions	Existing Wafer Sort CP1 (Carsem, Malaysia)	Alternate Wafer Sort CP1 (SFA, Korea)
Prober	TSK UF2000	Opus3_SD from SEMICS
Probe Card	SBA2 CP1 64-sites Probe Card (same design)	SBA2 CP1 64-sites Probe Card (same design)
Docking Type	Pogo Tower with PIB (same design)	Pogo Tower with PIB (same design)
Tester Platform	Advantest V93K (PS1600)	Advantest V93K (PS1600)
Test Program	SBA2_WS_CP1_64S_REV01	SBA2_WS_CP1_64S_REV01
Test Site	64	64
Test Temperature	85 °C	85 °C
Wafer Sort Correlation Results	Passed	Passed
Parametric Test Correlation Results	Passed	Passed