

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32					
A	1	NOBALL	SD_DQ05	SD_CLK_N9	SD_CLK_N9	SD_CLK_P2	SD_DQ04	SD_DQ06	SD_DQ03	SD_DQ04	SD_DQ03	SD_DQ06	SD_DQ03	SD_CLK_N9	SD_CLK_P9	SD_CLK_P9	SD_DQ06	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ06	SD_DQ03	SD_DQ06	SD_DQ03	SD_DQ06	SD_DQ03	SD_CLK_N4	SD_CLK_N1	SD_DQ03	SD_DQ02	SD_DQ06	SD_DQ03	VSS_IO				
B	2	SD_DQ06_P16	SD_DQ07	VSS_IO	NC	SD_CLK_P9	VSS_IO	SD_DQ03	SD_DQ39	VSS_IO	SD_DQ04	SD_DQ05_N4	SD_DQ05_P14	VSS_IO	SD_DQ37	SD_CLK_P9	VSS_IO	SD_CLK_P9	SD_CLK_N9	VSS_IO	SD_DQ02	SD_DQ06	VSS_IO	SD_DQ07	SD_DQ03	VSS_IO	SD_CLK_P4	SD_CLK_P1	VSS_IO	SD_DQ07	SD_DQ06_P10	VSS_IO	SD_DQ06_N9				
C	3	VSS_IO	SD_DQ06_N9	SD_DQ08	VDD_SD	SD_DQ04	SD_DQ05_N9	VDD_SD	SD_DQ05	SD_DQ04	VDD_SD	SD_DQ05_N9	SD_DQ05_P14	VDD_SD	NC	NC	VDD_SD	NC	SD_DQ06	VDD_SD	SD_DQ05_N13	NC	VDD_SD	SD_DQ11	SD_DQ15	VDD_SD	NC	VDD_SD	NC	VDD_SD	NC	SD_DQ05_P9	SD_DQ11				
D	4	SD_DQ06_P17	SD_DQ06	SD_DQ06	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08	SD_DQ08				
E	5	SD_DQ06	SD_DQ06	VSS_IO	NC	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	VSS_IO	SD_DQ04	CG_P8_SELECT				
F	6	SD_DQ06	VSS_IO	NC	NC	NC	SD_DQ03	NC	SD_CLK_P1	VDD_SD	SD_DQ03	NC	VDD_SD	SD_CLK_P1	NC	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	VDD_SD	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	VSS_IO	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03	SD_DQ03				
G	7	HLP_AD0	HLP_AD1	SD_CLK_CLK	SD_CLK_CLK	HLP_AD0	HLP_AD0	VSS_IO	SD_DQ01	SD_CLK_P9	SD_A13	SD_DQ02	SD_CAS0	SD_WE0	SD_C09	SD_A10	SD_A0	SD_A0	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1	SD_A1				
H	8	HLP_AD2	HLP_AD3	HLP_AD4	VSS_IO	HLP_AD5	HLP_AD6	HLP_AD7	NC	VSS_IO	VDD_SD	VDD_SD	SD_VREF0	VSS_IO	SD_C09	SD_BA0	SD_BA1	VSS_IO	SD_A0	SD_A0	VSS_IO	SD_A0	VSS_IO	SD_A0	VSS_IO	SD_A0	VSS_IO	PB_A0	VDD_SD	CG_SELECT	CG_SELECT	VDD_SD	CG_P8_SELECT				
J	9	KEN_PLL_AVG	KEN_PLL_AVG	HLP_AD10	HLP_AD11	HLP_AD12	HLP_AD13	HLP_AD14	HLP_AD15	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	PB_A0	PB_A0	PB_A0	PB_A0	PB_A0	PB_A0	NC	CG_P8_SELECT	NC	
K	10	HLP_AD16	VSS_IO	HLP_AD17	HLP_AD18	HLP_AD19	HLP_AD20	VSS_IO	HLP_AD21	VDD_PC	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	VDD_PC	VSS_IO	PB_T10	PB_A0	PB_A0	VSS_IO	KEN_PLL_AVG	KEN_PLL_AVG	VSS_IO	PB_A1	
L	11	HLP_AD22	HLP_AD23	HLP_AD24	HLP_AD25	VSS_IO	HLP_AD26	HLP_AD27	HLP_AD28	VDD_PC	VDD_PC	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO
M	12	HLP_WE0	HLP_C09	HLP_C09	HLP_C09	HLP_LE	HLP_AD09	HLP_AD09	HLP_AD09	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_INT0	PB_INT0	PB_INT0	PB_A0	PB_CLK0	PB_CLK0	PB_CLK0	SD_CLK0		
N	13	HLP_C09	VSS_IO	HLP_RD0	HLP_DE	GPI0	GPI0	VSS_IO	U_1_RX	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_B0	RESERVED0	NC	VSS_IO	PB_B0	RESERVED0	VSS_IO	RESERVED0		
P	14	JTAG_TDO	JTAG_TDI	GPI0	VSS_IO	GPI0	GPI0	GPI0	U_0_RX	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_T10	PB_A0	PB_A0	VSS_IO	KEN_PLL_AVG	KEN_PLL_AVG	VSS_IO	PB_A1		
R	15	TEST_TM1	JTAG_TCK	GPI0	INT0	GPI0	GPI0	GPI0	U_1_TX	VDD_PC	VDD_PC	VSS_IO	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_Q0	PB_A1	PB_A2	PB_A0	PB_A0	PB_A0	RESERVED0	RESERVED0			
T	16	TEST_TM3	VSS_IO	JTAG_TRST	GPI0	GPI0	INT0	VSS_IO	U_0_TX	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_A2	PB_A1	PB_A0	VSS_IO	PB_A1	PB_A1	VSS_IO	PB_INT0		
U	17	TEST_TM2	JTAG_TMS	TEST_ON	INT1	VSS_IO	GPI0	GPI0	DC_SCLK	VDD_PC	VDD_PC	VSS_IO	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VSS	PB_T0	VDD_PC	PB_A2	PB_D0	VDD_PC	PB_A2	PB_TA			
V	18	TEST_TM0	TEST_BDR_C1	DCN_RST0	GPI0	GPI0	INT0	NC	DC_SD	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_T0	PB_A2	PB_A1	NC	PB_B0	PB_A0	PB_A1	PB_A1		
W	19	E_MDO	VSS_IO	E_MDC	E_1_TC09	E_0_TC09	E_0_TC09	VSS_IO	E_0_TC07	VDD_PC	VDD_PC	VSS_IO	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_A1	PB_A0	RESERVED0	VSS_IO	RESERVED0	NC	VSS_IO	PB_A1			
Y	20	E_REF125	E_1_TC07	E_1_TC08	VSS_IO	E_0_TC08	E_0_TC08	E_0_TC08	E_0_TC08	VDD_PC	VDD_PC	VSS_IO	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	PB_A2	PB_A0	PB_A0	PB_D0	NC	PB_A1	PB_A0	NC		
AA	21	E_1_TC08	E_1_TC08	E_1_TC04	E_1_TC03	E_0_TC04	E_0_TC04	E_0_TC04	E_0_TC04	VDD_PC	VDD_PC	VSS_IO	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VDD	VSS	VSS	PB_A0	VDD_PC	NC	PB_D0	VDD_PC	PB_A1	PB_A0			
AB	22	E_1_TXCLK	VSS_IO	E_1_TC01	E_1_TC01	E_0_P0L_R0C0_P0R0_P0S0	VSS_IO	E_0_E0M0	VDD_PC	VDD_PC	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	VSS_IO	PB_D0	RESERVED0	RESERVED0	VSS_IO	RESERVED0	VSS_IO	PB_A0		
AC	23	E_1_TC02	E_1_P0R0_S0R0_P0L0_R0C0_P0S0	E_0_P0R0_S0R0_P0L0_R0C0_P0S0	VSS_IO	E_0_P0R0_S0R0_P0L0_R0C0_P0S0	VSS_IO	E_0_P0R0_S0R0_P0L0_R0C0_P0S0	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VSS_IO	VDD_PC	PB_D3	PB_D7	PB_D19	PB_D3	PB_D3	PB_D3	RESERVED0	RESERVED0	
AD	24	E_1_TXCLK	E_1_WRAP	VSS_IO	E_0_P0R0_S0R0_P0L0_R0C0_P0S0	E_0_R0C0	E_0_R0C0	VSS_IO	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VDD_PC	VSS_IO	VDD_PC	PB_D4	RESERVED0	VDD_PC	PB_D2	PB_D2	VDD_PC	PB_D2	PB_D4		
AE	25	E_1_EC0M0	E_1_R0C0	E_1_P0R0_S0R0_P0L0_R0C0_P0S0	E_0_WRAP	E_0_R0C0	E_0_R0C0	E_0_R0C0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	VSS_IO	PB_D5	PB_D7	RESERVED0	VSS_IO	PB_D6	RESERVED0	VSS_IO	PB_D6		
AF	26	E_1_TXCLK	VSS_IO	E_1_R0C0	VSS_IO	E_0_R0C0	E_0_R0C0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_STOP	P0L_A0	VSS_IO	P0L_CBE0	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	NC	VSS_IO	RIU	PB_D31	VSS_IO	PB_D30	PB_D7	PB_D15	PB_D14	PB_D10	PB_D29	PB_D16
AG	27	E_1_R0C0	E_1_R0C0	E_1_R0C0	E_0_R0C0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0
AH	28	E_1_TXCLK	E_1_R0C0	E_1_R0C0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0
AJ	29	E_1_R0C0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0
AK	30	E_1_R0C0	P0L_RST0	P0L_P0C0_C0P0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0
AL	31	P0L_RST0	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	P0L_A0	VSS_IO	VSS_IO	VSS_IO	P0L_A0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0	VSS_IO	P0L_A0	P0L_A0
AM	32	VSS_IO	P0L_HEALTH	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	P0L_A0	VSS_IO	VSS_IO

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Revision History

80E5000_PN002_04, Formal, October 2009. This version was rebranded as IDT. It does not include any technical changes.