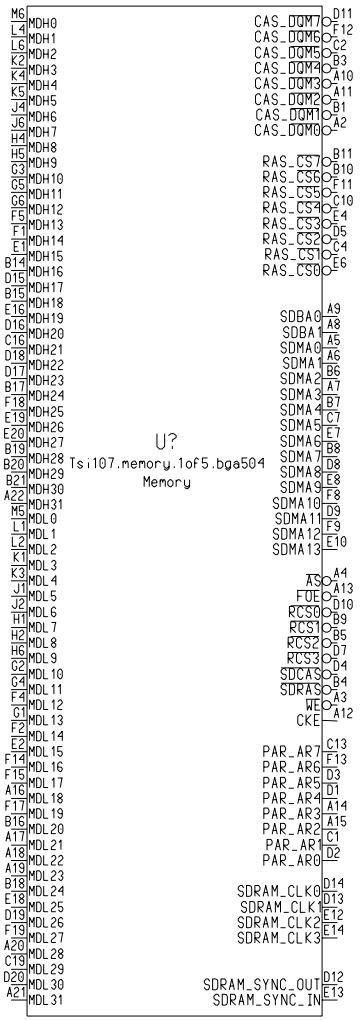

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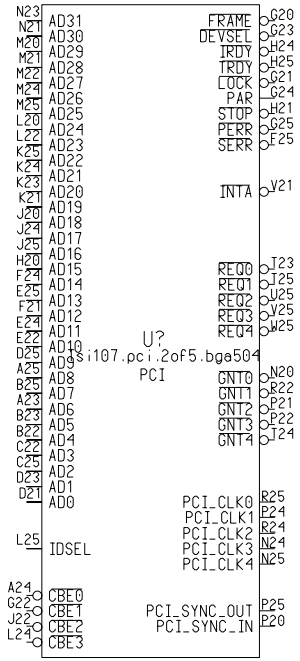
Engineer: Luc Picknell
Drawn By: Luc Picknell
R&D CHK
DOC CTRL CHK
MFG CTRL CHK

TI

Tsi107

Memory Interface

D



Engineer	Luc Picknell
Drawn By	Luc Picknell
R&D CHK	
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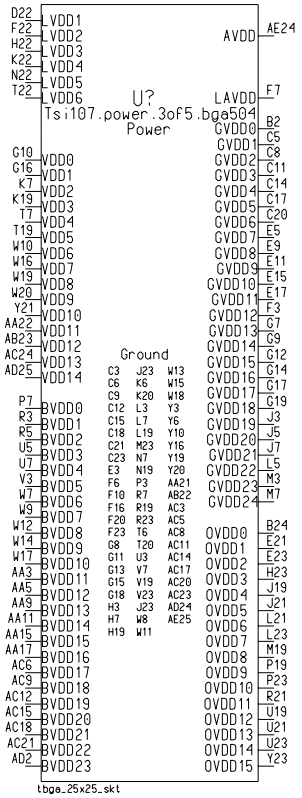
TITLE
Tsi107
PCI Interface

Size D

d By Librarian	Date Changed Wednesday, December 10, 2003	Time 2:59:50 pm		REV 01	Drawing Number 60C2000_SS001.01	Sheet 2 of 5



Engineer	Luc Picknell
Drawn By	Luc Picknell
R&D CHK	
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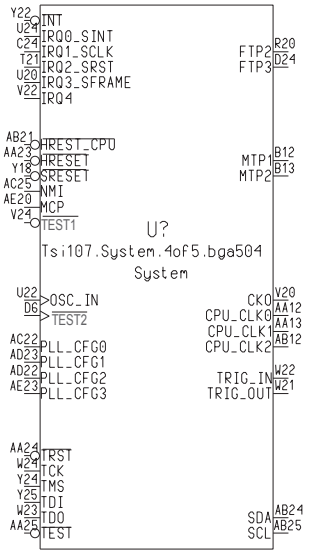
Tsi107
Power Interface

D

Changed By	Date Changed	Time	QA CHK	REV	Drawing Number	Sheet 3 of 5
Librarian	Wednesday, December 10, 2003	3:01:59 pm		01	60C2000_SS001_01	



Engineer	Luc Picknell
Drawn By	Luc Picknell
R&D CHK	
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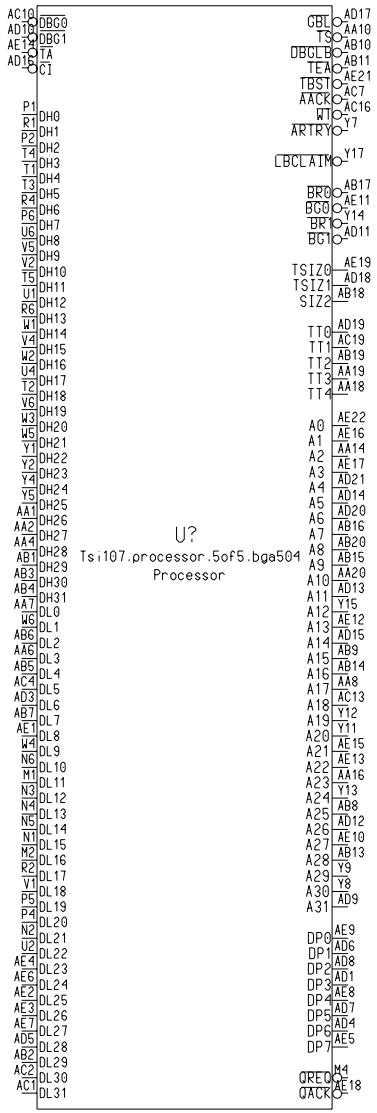
TITLE
Tsi107
System

Size D

Changed By	Date Changed	Time	QA CHK	REV	Drawing Number	Sheet 4 of 5
Librarian	Wednesday, December 10, 2003	3:03:00 pm		01	60C2000_SS001.01	



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Engineer
Luc Picknell
Drawn by
Luc Picknell
R&D CHK
DOC CTRL CHK
MFG CTRL CHK

D

Tsi107
Processor Interface