

LET Threshold for SEU of the HS-26Cx32RH Operating in Failsafe Mode

Description

In failsafe mode, the inputs to the HS-26Cx32RH quad differential line receiver are open circuited and not driven. A designed imbalance in the input resistor structure generates an inherent error voltage, which forces the outputs high. The magnitude of the error voltage is a function of the differential impedance between the input lines. If this device is to be operated in failsafe mode, Renesas recommends testing it to establish the LET threshold for SEU.

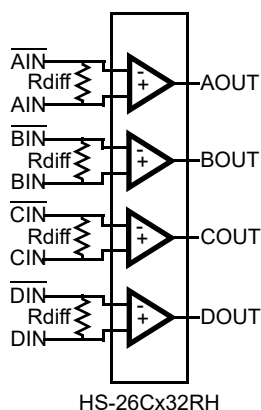


Figure 1. Schematic

Revision History

Revision	Date	Description
1.00	Aug 11, 2026	- Placed in the latest template. - Updated Description.
0.00	Nov 25, 2002	initial release

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