RENESAS ELECTRONICS SINGLE-CHIP 16-BIT MICROCOMPUTER R5F2L3A7CNXXXFA

ROM number

	Date:	
pt	Section mgr signature	PIC signature
Receipt		
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ROM PROGRAMMING CONFIRMATION FORM

Note: Please fill in all items marked *.

*	Company Name	TEL ()	ant ignature	Submitted by
	 Date issued	Year / Month / Date Date:	Applica s	

* 1. Mask file

Please kindly verify and confirm the mask file in the submitted CD-R prior to submission. Please submit mask files on CD-R. And the number of the mask file must be 1 mask file per one CD-R.

Part Number] R5F2L	.3A7CNXXX	XFA		
File Code					(hexadecimal notation)
Mask file name					.MSK (no more than 8 characters)
★ 2. Mask option Set the mask option	n in the ma	ask file gene	erating utility	as follows:	
Address : 10h	<u>1</u>		<u>Data : 01h</u>		
 ★ 3. ROM data which r Check the option fur values as ROM data □ OFS register 	unction sele		s (OFS, OFS OFS2 regis	·	ode areas to be set for appropriate
generating util product differs <u>There is no Er</u> <u>Initial product</u> <u>Should you fin</u>	r of this pro confirmation mming will ility. Only in s from that ngineering delivery. nd any pro ards RENE	on request be process n case whe t of above m <u>3 Sample, th</u> <u>blem, pleas</u> <u>ESAS will a</u>	sed based or en ROM data nentioned ma thus please o se return imm automatically	n the mask f a programme ask file, REN confirm the F mediately. 2 be regarded R5F2L3A	a. file generated by the mask file ed in the actual mass produced NESAS takes the responsibility. ROM data at the receipt of the 2 weeks without technical error d as acceptance of products. A7CNyyyFA XXXXXXX

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Usage conditions

For our reference of new products, please reply to the following questions about the usage of the products you ordered.

(1) What is the voltage of power supply Typ. = V		V	Max. =[V	
(2) What is the ambient temperature yo Typ. = °C		°C	Max. =	°℃	
 (3) On which condition will you use Res □ Hardware Reset □ Watchdog timer Reset 	set? (Plural answers are Power-on reset Software Reset	Reset	□ Volt	age monitor 0 Reset	
(4) On which condition will you use Vol	tage monitor 0 Circuit? □ Use			□ Not use	
Voltage Detection 0 Level Select	□ 3.80∨	□ 2.85∨		□ 2.35V	□ 1.90V
(5) On which condition will you use Vol Voltage Detection 1 Level Select	tage monitor 1 Circuit? Use 2.20V 2.80V 3.40V 4.00V	□ 2.35V □ 2.95V □ 3.55V □ 4.15V		 □ Not use □ 2.50V □ 3.10V □ 3.70V □ 4.30V 	□ 2.65V □ 3.25V □ 3.85V □ 4.45V
(6) Will you use Voltage monitor 2 Circ	uit? □ Use			□ Not use	
 (7) On which condition will you use Hig High-Speed On-Chip Oscillator Frequency Division ratio XIN-XOUT Oscillates Oscillator type Frequency 	Use 40MHz	□ 36.864MHz node □ Not use □ Ceramic resonat Hz	cor	 □ Not use □ 32MHz □ External clock in □ Others (nput
Load capacity Internal feedback resistance Oscillation stop detection	XIN side = □ Use □ Use	ρF		XOUT side = □ Not use □ Not use	pF
(8) On which condition will you use Low					
Frequency f(XCIN Load capacity XCIN	vstal Oscillator □ Oth I) = kHz		Low XCOUT		

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(9) On which condition will you	-				_		
	☐ No divis ☐ Divide-b		☐ Divide-t ☐ Divide-t	by-2 mode by-16 mode		Divide-by-	4 mode
(10) Which Power control mode	will you use' □ Wait mo		wers are pos Stop mo		D F	Power-off	mode
 (11) Will you use Flash memory CPU rewrite mode ROM code protect Low-Current-Consumption Erase-suspend BGO function 		□ Use □ Use □ Use □ Use □ Use	CPU clock	frequency =		kΩ	 Not use Not use Not use Not use Not use
(12) Which timer mode will you	use?						
Timer RA	🛛 Use				🗆 Not ı	use	
Operation mode	🛛 Timer m	ode	🗆 Pulse ou	utput mode	🗆 Even	t counter	mode
	🛛 Pulse wi	dth measure	ement mode		□ Pulse	e period m	easurement mode
Count source	🗆 f1	□ f2	□ f8	☐ fOCO	□ fC32	□ fC	
Timer RB	🗆 Use				🗆 Not ı		
Operation mode	🛛 Timer m						one-shot generation mode
	🛛 Program	mable wavef	form generat	ion n 🗆 Prog	grammabl	e wait one	-shot generation mode
Count source	□ f1	□ f2	□ f8	□ Timer R	A under f	flow	
Timer RC	🗆 Use				🗆 Not u	use	
Operation mode	🛛 Timer m	ode	🗆 Input ca	pture functio	n	🗆 Ou	Itput compare function
	D PWM mo	ode	□ PWM2 m	node			
Count source	□ f1 □ f0C0-F	□ f2		□ f8 <	□ f32	☐ fO	CO40M
Timer RD	🗆 Use				🗆 Not u	use	
Count	🗆 Up coun	t			🗆 Dowr	n count	
Operation mode	🛛 Timer m	ode	🛛 Input ca	pture functio	on		tput compare function
	□ PWM mo □ PWM3 m		□ Reset s	ynchronous I	PWM mod	le 🗆 Co	mplimentary PWM mode
Operation Clock	□ f1	□ f2	□ f4	□ f8	□ f32	□ fC	2
	☐ fOCO40	М	☐ fOCO-F			CLK	
Timer RE	🗆 Use				🗆 Not ı	use	
Operation mode	Output of the	compare mod	le		🛛 Real	-time cloc	k mode
Operation Clock	□ f4	□ f8	□ f32	☐ fC4			
Timer RG	🗆 Use				🗆 Not u	use	
Count	🗆 Up coun	t			🗆 Dowr	n count	
Operation mode	□ Timer m □ PWM mo		🛛 Input ca	pture functio	on	🗆 Ou	tput compare function
Operation Clock	□ f1	□ f2	□ f4	□ f8	□ f32	□ fC	2
	foco40	М		KA		CLKB	

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(13) On which condition will you	use UART?				
UART0	🗆 Use		□ Not use		
Operation mode	□ Clock synchronous serial I/O mode □ Clock non-synchronous serial I/O				
UART1	🗆 Use		□ Not use		
Operation mode	Clock synchronous s	serial I/O mode	Clock non-synchro	nous serial I/O mode	
UART2	🗆 Use		🗆 Not use		
Operation mode	□ Clock synchronous s □ I2C mode	serial I/O mode	□ Clock non-synchro □ Multiprocessor con		
Synchronous Serial Commu	nication Unit (SSU)		🗆 Use	□ Not use	
Operation mode	Clock synchronous of	communication mode	☐ 4 lines bus commu	nication mode	
I2C bus Interface	🗆 Use		🗆 Not use		
Operation mode	☐ I2C bus interface mo	ode	Clock synchronous	serial mode	
LIN Module	🗆 Use		🗆 Not use		
	☐ Master mode		☐ Slave mode		
(14) On which condition will you	use DTC?				
	🗆 Use		🗆 Not use		
Transfer mode Transfer times =	□ Normal mode		☐ Repeat mode		
(15) On which condition will you	use A/D converter?				
(,	Use		□ Not use		
A/D input pin	Number of A/D input pi	ins used = pir			
Conversion mode	□ 8bit A/D	i			
A/D clock source	□ f1		☐ f0C0-F		
Division ratio	□ No division	☐ In frequency/2	☐ In frequency/4	□ In frequency/8	
A/D Trigger	□ Software □ Not use	☐ Timer RD	☐ Timer RC	External Trigger	
A/D Operation mode	□ Single mode	🛛 Repeat mode0	□ Repeat mode1		
	□ Single sweep mode	🛛 Repeat sweep mode	Sweep pin =	pins	
Disconnection-detection as	s 🗆 Use		□ Not use		
(16) Will you use D/A converter	?				
,,	Use		□ Not use		
(17) On which condition will you	use ComparatorB?				
Comparator B1	Use .		□ Not use		
Digital Filter	□ Use		□ Not use		
Comparator B3	□ Use		□ Not use		
Digital Filter	□ Use		□ Not use		

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		ol Circuit?		
	🗆 Use		🗆 Not use	
Usage of LCD pins	Number of common p	ins used =pins	Number of segment p	pins used = <u>pins</u>
Bias	□ 1/2	□ 1/3	□ 1/4	
LCD panel waveform	Segment panel wa	veform	🛛 Dot matrix panel v	waveform
Usage of LCD panel	□ 5V faction	□ 3V faction	□ Others()
LCD Clock Source	🗆 f32		☐ fC-LCD	
Division ratio	☐ In frequency/2	☐ In frequency/4	☐ In frequency/8	□ In frequency/2
	☐ In frequency/2	☐ In frequency/2	☐ In frequency/2	
Use External division	resister			
Range of LCD power	supply voltage(VL4)	<i>l</i> lin. = <u> </u>	Max. =	V
Division resistance	One Res	ster Value = <u> </u>	Ω	
🗌 Use Internal voltage n	nultiplier circuit			
Voltage of VL1	External input volt	age =V	🛛 Internally-generat	ed voltage accuracy
Capaciter for voltage	e mi CL1-CL2 =	F VL4,VI	_3,VL2,VL1=	_F
		🛛 Reversing display		
Frame frequency =	Hz			
(19) On which condition will y	you use Watchdog Timer? □ Use		□ Not use	
Count Source	CPU clock	□ Low-speed on-chip	oscillator clock for the	e watchdog timer clock
Division ratio of the pres	caler			
	□ 1/2	□ 1/16	□ 1/128	
Watchdog timer underflov	v period set bit			
	03FFh	🗆 0FFFh	🗆 1FFFh	🗆 3FFFh
Watchdog timer refresh a	03FFh		🗆 1FFFh	□ 3FFFh
Watchdog timer refresh a	03FFh		□ 1FFFh □ 75%	□ 3FFFh □ 100%
Watchdog timer refresh a Watchdog timer start sele	□ 03FFh acknowledgement period s □ 25%	et bit	_	_
	□ 03FFh acknowledgement period s □ 25% ect bit	et bit	□ 75%	_
	□ 03FFh acknowledgement period s □ 25% ect bit	tet bit □ 50% ntomatically starts after re	□ 75%	_
	☐ 03FFh icknowledgement period s ☐ 25% ect bit ☐ Watchdog timer au ☐ Watchdog timer is	tet bit □ 50% ntomatically starts after re stopped after reset	□ 75%	_
Watchdog timer start sele	 □ 03FFh □ cknowledgement period s □ 25% ect bit □ Watchdog timer au □ Watchdog timer is mode after reset select b 	tet bit □ 50% ntomatically starts after re stopped after reset	□ 75% set	_
Watchdog timer start sele	□ 03FFh acknowledgement period s □ 25% ect bit □ Watchdog timer au □ Watchdog timer is mode after reset select b □ Count source prot	et bit □ 50% itomatically starts after re stopped after reset it	□ 75% set reset	_

Thank you for your cooperation.