RENESAS ELECTRONICS SINGLE-CHIP 16-BIT MICROCOMPUTER R5F2L36CCDXXXFA

ROM PROGRAMMING CONFIRMATION FORM

ROM	number	
	Date:	
ta.	Section mgr signature	PIC signature
Receipt		

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	Company							Т	EL			ture	Su	ubmitted by
Applicant								()			ant igna		
	Date	_		Yea	ar / Mon	th / Date						s		
	issued	Date:										¥		
Pleas	e kindly ve													one CD-R.
Part N	Number	□R	35F2L	36CC	DXXX	(FA								
File C	ode									(hexa	deci	mal no	tation)	
Mask	file name									.MSK	(no	o more	than 8 d	characters)
Set th 3. RON Check values	e mask op Address: M data which the option as ROM of	10h ich must n functio data.	: be se	et by u	□	Data :	: <u>01h</u> S, OFS	52) and		ode area				opriate
	OFS regis	ter				OFS2	: regis	ter			ID	code a	areas	
Note 2	1: ROM of 2: ROM do ROM proof generating product difted the second proof should you feedback to 3: Mark sp	rder of the ata configramming utility. Offers from the Engine duct deliver find and towards	irmation irmation irmation in that eering very. In probable RENE	on request be properties of about the Sampolem, ESAS	juest ocess when ove mole, the pleas will au	eed bas n ROM nentior nus ple e retur utoma	sed or detail de	n the r progr ask file confirm mediat be ree	mask framme e, REI n the I ely. 2 garde	file gener ed in the NESAS to ROM data weeks vond as accessives	actua akes a at t vitho eptar CD vFA	al mas the rec the rec ut tech nce of p	s products sponsibi eipt of th unical err products DM numbe	ced lity. ne or -
	* 1. Mass Please	* 1. Mask file Please kindly verblease submit in Part Number File Code Mask file name * 2. Mask option Set the mask option Set the m	Applicant Date issued Date:	Applicant Date issued Date: The standard product delivery. Should you find any product delivery. Should you find you fi	Applicant Date issued Date:	Applicant Date issued Date:	Applicant Date issued Date:	Applicant Date issued Date:	Applicant Date issued Date:	Applicant Name Pate Date Susued Date Pate Date Date Susued Date Pate Date Date	Applicant Company Name	Applicant Company Name	Applicant Company Name Year / Month / Date () Year / Month / Date Subset Date Date Subset Date Date Subset Date Date	# 1. Mask file Please kindly verify and confirm the mask file in the submitted CD-R prior to submission. Please submit mask files on CD-R. And the number of the mask file must be 1 mask file per Part Number

Fig. 1

Usage conditions

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For our reference of new products, plea ordered.	se reply to the following	questions about the	usage of the products you		
(1) What is the voltage of power supply Typ. = V		V	Max. = V		
(2) What is the ambient temperature yo	u use?				
Typ. = °C	Min. =	°C	Max. = C		
(3) On which condition will you use Res	set? (Plural answers are	possible.)			
☐ Hardware Reset	☐ Power-on reset	Reset	☐ Voltage monitor 0 Reset		
☐ Watchdog timer Reset	☐ Software Reset				
(4) On which condition will you use Vol	tage monitor 0 Circuit?				
	□ Use		☐ Not use		
Voltage Detection 0 Level Select	□ 3.80V	□ 2.85V	□ 2.35V	□ 1.90V	
(5) On which condition will you use Vol	_		□ Nat was		
Voltage Detection 1 Level Select	☐ Use ☐ 2.20V	□ 2.35V	☐ Not use ☐ 2.50V	□ 2.65V	
Voltage Detection 1 Level Select	□ 2.80V	□ 2.95V	□ 2.30V	☐ 2.05V	
	□ 3.40V	☐ 3.55V	□ 3.70V	□ 3.85V	
	□ 4.00V	☐ 4.15V	□ 4.30V	☐ 4.45V	
(6) Will you use Voltage monitor 2 Circ	uit?				
,,, ,	□ Use		☐ Not use		
(7) On which condition will you use Hig	h-speed clock?				
High-Speed On-Chip Oscillator	□ Use		☐ Not use		
Frequency	☐ 40MHz	☐ 36.864MHz	☐ 32MHz		
Division ratio	Divide-byr	node			
XIN-XOUT Oscillates	□ Use	☐ Not use	☐ External clock	input	
Oscillator type	☐ Crystal Oscillator	☐ Ceramic resonat	tor 🗆 Others ()	
Frequency	f(XIN) = MI	Hz			
Load capacity	XIN side =	pF	XOUT side =	pF	
Internal feedback resistance	□ Use		☐ Not use		
Oscillation stop detection	□ Use		☐ Not use		
(8) On which condition will you use Low	•	t use	☐ Low-speed on-chip osc	illator	
Oscillator type	stal Oscillator 🔲 Oth	ners ()		
Frequency f(XCIN	<u> </u>				
Load capacity XCIN s				ρF	
Internal feedback resistance	Use		☐ Not use		

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(9) On which condition will you u	use System	clock divisio	n ratio?			
	☐ No divis		☐ Divide-b	-	☐ Divi	de−by−4 mode
(10) Which Power control mode	will you use' □ Wait mo		wers are pos		□ Pow	er-off mode
(11) Will you use Flash memory CPU rewrite mode ROM code protect Low-Current-Consumption R Erase-suspend BGO function		☐ Use ☐ Use ☐ Use ☐ Use ☐ Use ☐ Use	CPU clock	frequency =		☐ Not use ☐ Not use kΩ ☐ Not use ☐ Not use ☐ Not use
(12) Which timer mode will you						
Timer RA	☐ Use				☐ Not use	
Operation mode	☐ Timer m	ode dth measure	Pulse ou mant mada	itput mode		ounter mode eriod measurement mode
Count source	☐ f1	f2	f8	□ fOCO	☐ fC32	fC
Timer RB	□ Use				☐ Not use	
Operation mode	☐ Timer m	ode			☐ Program	mable one-shot generation mod
	☐ Program	mable wavef	orm generati	ion n 🗆 Prog	grammable w	ait one-shot generation mode
Count source	☐ f1	☐ f2	☐ f8	☐ Timer R	A under flow	•
Timer RC	□ Use				☐ Not use	
Operation mode	☐ Use	ode	☐ Input car	pture functio		☐ Output compare function
operation meas	□ PWM mo		☐ PWM2 m			_ cuput compare ranction
Count source	☐ f1 ☐ fOCO-F	□ f2	☐ f4 ☐ TRCCLK	□ f8 <	☐ f32	☐ fOCO40M
Timer RD	□ Use				☐ Not use	
Count	☐ Up coun	t			☐ Down co	ount
Operation mode	☐ Timer m	ode	☐ Input ca	pture functio	on	☐ Output compare function
	☐ PWM mo		☐ Reset sy	ynchronous I	PWM mode	☐ Complimentary PWM mode
Operation Clock	☐ f1 ☐ f0CO40	☐ f2 M	☐ f4 ☐ fOCO-F	□ f8	☐ f32 ☐ TRDCLK	G fC2
Timer RE	□ Use				☐ Not use	
Operation mode		compare mod	le			ne clock mode
Operation Clock	☐ f4	☐ f8	☐ f32	☐ fC4		
Timer RG	□ Use				☐ Not use	
Count	☐ Up coun	t			□ Down co	ount
Operation mode	☐ Timer m	ode	☐ Input ca	pture functio		Output compare function
Operation Clock	☐ f1 ☐ fOCO40	☐ f2	☐ f4 ☐ TRGCLk	□ f8 《A	☐ f32 ☐ TRGCLk	☐ fC2 (B

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(13) On which condition will yo	ou use UART?			
	UART0	□ Use		☐ Not use	
	Operation mode	☐ Clock synchronous	serial I/O mode	☐ Clock non-synchro	onous serial I/O mode
	UART1	□ Use		☐ Not use	
	Operation mode	☐ Clock synchronous	serial I/O mode	☐ Clock non-synchro	onous serial I/O mode
	UART2	□ Use		☐ Not use	
	Operation mode	☐ Clock synchronous	serial I/O mode	☐ Clock non-synchro	onous serial I/O mode
		☐ I2C mode		☐ Multiprocessor cor	mmunication function
	Synchronous Serial Comm	unication Unit (SSU)		□ Use	☐ Not use
	Operation mode	☐ Clock synchronous of	communication mode	☐ 4 lines bus commu	unication mode
	I2C bus Interface	□ Use		☐ Not use	
	Operation mode	☐ I2C bus interface me	ode	☐ Clock synchronous	s serial mode
	LIN Module	□ Use		☐ Not use	
		☐ Master mode		☐ Slave mode	
, , ,	\	DTOO			
(14) On which condition will yo				
	T	□ Use		☐ Not use	
	Transfer mode	□ Normal mode		☐ Repeat mode	
	Transfer times =				
(15) On which condition will yo	ou use A/D converter?			
	•	□ Use		☐ Not use	
	A/D input pin	Number of A/D input p	ins used = <u>pir</u>	<u>ns</u>	
	Conversion mode	☐ 8bit A/D	-	☐ 10bit A/D	
	A/D clock source	☐ f1		☐ fOCO-F	
	Division ratio	☐ No division	☐ In frequency/2	☐ In frequency/4	☐ In frequency/8
	A/D Trigger	☐ Software	☐ Timer RD	☐ Timer RC	☐ External Trigger
		☐ Not use	_	_	_
	A/D Operation mode	☐ Single mode	☐ Repeat mode0	☐ Repeat mode1	
	•		☐ Repeat sweep mode	Sweep pin =	pins
	Disconnection-detection a	= :	,	□ Not use	
(16) Will you use D/A converte	er? □ Use		☐ Not use	
		_ 036		1400 use	
(17) On which condition will yo	ou use ComparatorB?			
	Comparator B1	□ Use		☐ Not use	
	Digital Filter	□ Use		☐ Not use	
	Comparator B3	□ Use		☐ Not use	
	Digital Filter	□ Use		☐ Not use	

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(18) On which condition will y	ou use LCD Drive Contr	ol Circuit?		
	☐ Use		☐ Not use	
Usage of LCD pins	Number of common p	ins used = <u>pins</u>	Number of segment	pins used = <u>pins</u>
Bias	□ 1/2	□ 1/3	□ 1/4	
LCD panel waveform	☐ Segment panel wa	veform	☐ Dot matrix panel	waveform
Usage of LCD panel	☐ 5V faction	☐ 3V faction	☐ Others()
LCD Clock Source	☐ f32		☐ fC-LCD	
Division ratio	☐ In frequency/2	☐ In frequency/4	☐ In frequency/8	☐ In frequency/2
	☐ In frequency/2	☐ In frequency/2	☐ In frequency/2	
☐ Use External division i	resister			
Range of LCD power	supply voltage(VL4)	Min. =V	Max. =	V
Division resistance	One Res	ister Value =k	Ω	
☐ Use Internal voltage m	ultiplier circuit			
Voltage of VL1	☐ External input vol	tage =V	☐ Internally-generat	ed voltage accuracy
Capaciter for voltage	mı CL1-CL2 =	F VL4,VL	_3,VL2,VL1=	_F
Data display mode	☐ Blinking display	☐ Reversing display	☐ Not use	
Frame frequency =	Hz			
(19) On which condition will y	ou use Watchdog Timer?	•	☐ Not use	
Count Source	☐ CPU clock	☐ Low-speed on-chip	oscillator clock for the	watchdog timer clock
Division ratio of the preso	caler			
	□ 1/2	□ 1/16	□ 1/128	
Watchdog timer underflow	period set bit			
	□ 03FFh	☐ 0FFFh	☐ 1FFFh	☐ 3FFFh
Watchdog timer refresh ac	cknowledgement period s	set bit		
	□ 25%	□ 50%	□ 75%	□ 100%
Watchdog timer start sele	ct bit			
	☐ Watchdog timer au	utomatically starts after re	set	
	☐ Watchdog timer is	stopped after reset		
Count source protection n	node after reset select b	oit		
	☐ Count source prot	ection mode enabled after	reset	
	☐ Count source prot	ection mode disabled after	reset	

Thank you for your cooperation.