To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

REJ19B0006-0101Z
Rev.1.01

Mask ROM number	

740 FAMILY MASK ROM CONFIRMATION FORM
SINGLE-CHIP MICROCOMPUTER M37542M4T-XXXFP/GP
RENESAS TECHNOLOGY

Receipt	Date :	
	Section head signature	Supervisor signature

Note : Please fill in all items marked※.

※						
Customer	Company name	TEL ( )	Issuance signature	Submitted by	Supervisor
	Date issued	Date:			

※1. Confirmation

Specify the name of the product being ordered.
Three EPROMs are required for each pattern if this order is performed by EPROMs.
One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name: ☐ M37542M4T-XXXFP ☐ M37542M4T-XXXGP

☐ Ordering by EPROMs

Specify the type of EPROMs submitted.
If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM | | | | | (hexadecimal notation)

EPROM type (indicate the type used)

☐ 27C256	☐ 27C512
EPROM address	EPROM address
0000_{16} Area for ASCII codes of the name of the product 'M37542M4T-' $000F_{16}$	0000_{16} Area for ASCII codes of the name of the product 'M37542M4T-' $000F_{16}$
0010_{16} $407F_{16}$	0010_{16} $C07F_{16}$
4080_{16} Data ROM (16K-130) bytes $7FFD_{16}$	$C080_{16}$ Data ROM (16K-130) bytes $FFFD_{16}$
$7FFE_{16}$ $7FFF_{16}$	$FFFE_{16}$ $FFFF_{16}$

In the address space of the microcomputer, the internal ROM area is from address $C080_{16}$ to $FFFD_{16}$. The reset vector is stored in addresses $FFFC_{16}$ and $FFFD_{16}$.

(1) Set the data in the unused area (the shaded area of the diagram) to "FF_{16}".
(2) The ASCII codes of the product name "M37542M4T-" must be entered in addresses 0000_{16} to 0008_{16}. And set the data "FF_{16}" in addresses 0009_{16} to $000F_{16}$.
The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address	
0000_{16}	'M'=$4D_{16}$
0001_{16}	'3'=33_{16}
0002_{16}	'7'=37_{16}
0003_{16}	'5'=35_{16}
0004_{16}	'4'=34_{16}
0005_{16}	'2'=32_{16}
0006_{16}	'M'=$4D_{16}$
0007_{16}	'4'=34_{16}

Address	
0008_{16}	'T' = 54_{16}
0009_{16}	'-' = $2D_{16}$
$000A_{16}$	FF_{16}
$000B_{16}$	FF_{16}
$000C_{16}$	FF_{16}
$000D_{16}$	FF_{16}
$000E_{16}$	FF_{16}
$000F_{16}$	FF_{16}

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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 0000_{16} to 0008_{16} of EPROM.

EPROM type	27C256	27C512
The pseudo-command	△∗=△$8000 △.BYTE△ ‘M37542M4T-’	△∗=△$0000 △.BYTE△ ‘M37542M4T-’

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

☐ Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

Microcomputer name: ☐ M37542M4T-XXXFP ☐ M37542M4T-XXXGP

File code ☐☐☐☐☐☐☐☐ (hexadecimal notation)

Mask file name ☐☐☐☐☐☐☐☐ .MSK (equal or less than eight characters)

Note: When submitting data by floppy disk, do not write data to the product name area (addresses 0000_{16} to $000F_{16}$).
Write data to only ROM data area (addresses $C080_{16}$ to $FFFD_{16}$).

※2. Mark specification

Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (36P2R-A for M37542M4T-XXXFP, 32P6U-A for M37542M4T-XXXGP) and attach it to the mask ROM confirmation form.

Mask ROM number	

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※3. Usage conditions

For our reference of new products, please reply to the following questions about the usage of the products you ordered.

(1) Which operation source clock you use?

☐ Ceramic resonator
☐ RC oscillation
☐ External clock input
☐ Quartz-crystal oscillation

What frequency do you use?
f(XIN) = ☐ MHz

☐ Other( )
☐ On-chip oscillator

(2) What is the voltage of power supply (VDD) you use?

Typ.= ☐ V Min.= ☐ V Max.= ☐ V

(3) What is the ambient temperature you use?

Typ.= ☐ °C Min.= ☐ °C Max.= ☐ °C

(4) Which clock division ratio you use?

☐ Double-speed mode (f(φ)=F(XIN)) ☐ High-speed mode (f(φ)=F(XIN)/2)
☐ Middle-speed mode (f(φ)=F(XIN)/8) ☐ Applied from on-chip oscillator

(5) Please reply to the following questions about timer function.

(i) Which timer you use?

☐ Timer1 ☐ TimerX ☐ TimerA ☐ TimerB

(ii) Which count source of timer you use?

- Timer X ☐ f(XIN) ☐ f(XIN)/2 ☐ f(XIN)/16
- Timer A ☐ f(XIN)/2 ☐ f(XIN)/16 ☐ f(XIN)/32 ☐ f(XIN)/64 ☐ f(XIN)/128 ☐ f(XIN)/256 ☐ On-chip oscillator output
- Timer B ☐ f(XIN)/2 ☐ f(XIN)/16 ☐ f(XIN)/32 ☐ f(XIN)/64 ☐ f(XIN)/128 ☐ f(XIN)/256 ☐ Timer A underflow

(iii) Which operating mode you use?

- Timer X ☐ Timer mode ☐ Pulse output mode ☐ Event counter mode ☐ Pulse width measurement mode

(iv) Do you use the Output compare?

☐ Use ( )channel ☐ Not use

(v) Do you use the Input capture?

☐ Use ( )channel ☐ Not use

(6) Do you use the Serial I/O?

☐ Use ☐ Not use

Serial I/O1 (☐ Clock synchronous Serial I/O1 mode ☐ Asynchronous Serial I/O1(UART1) mode)
Serial I/O2 (☐ Clock synchronous Serial I/O2 mode ☐ Asynchronous Serial I/O2(UART2) mode)

(7) Do you use the A/D converter?

☐ Use ☐ Not use

(8) Do you use the Watchdog timer?

☐ Use ☐ Not use

(9) Do you use the oscillation stop detection circuit?

☐ Use ☐ Not use

Thank you cooperation.

※4. Comments