To our customers,

Old Company Name in Catalogs and Other Documents

   On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

RENESAS

REJ19B0686-0100

Mask ROM number	

740 FAMILY MASK ROM CONFIRMATION FORM
SINGLE-CHIP MICROCOMPUTER M37516M6-XXXHP
RENESAS TECHNOLOGY

Receipt	Date:	
	Section head signature	Supervisor signature

Note : Please fill in all items marked ※.

※ Customer	Company name		TEL ( )	Issuance signature	Submitted by	Supervisor
	Date issued	Date:				

※ 1. Confirmation

Three EPROMs are required for each pattern if this order is performed by EPROMs.
One floppy disk is required for each pattern if this order is performed by a floppy disk.

☐ Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM ☐☐☐☐ (hexadecimal notation)

EPROM type (indicate the type used)

☐ **27512**

EPROM address

000_{16}
$000F_{16}$ — Product name ASCII code : 'M37516M6-'
0010_{16}
$A07F_{16}$
$A080_{16}$ — data ROM (24K-130) bytes
$FFFD_{16}$
$FFFE_{16}$
$FFFF_{16}$
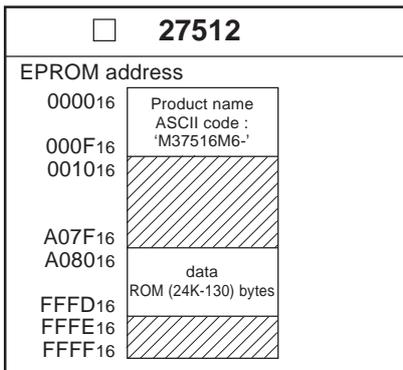
In the address space of the microcomputer, the internal ROM area is from address $A080_{16}$ to $FFFD_{16}$. The reset vector is stored in addresses $FFFC_{16}$ and $FFFD_{16}$.

(1) Set the data in the unused area (the shaded area of the diagram) to "FF_{16}".
(2) The ASCII codes of the product name "M37516M6–" must be entered in addresses 000_{16} to 0008_{16}. And set the data "FF_{16}" in addresses 0009_{16} to $000F_{16}$. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address			Address	
0000_{16}	'M' = $4D_{16}$		0008_{16}	'–' = $2D_{16}$
0001_{16}	'3' = 33_{16}		0009_{16}	FF_{16}
0002_{16}	'7' = 37_{16}		$000A_{16}$	FF_{16}
0003_{16}	'5' = 35_{16}		$000B_{16}$	FF_{16}
0004_{16}	'1' = 31_{16}		$000C_{16}$	FF_{16}
0005_{16}	'6' = 36_{16}		$000D_{16}$	FF_{16}
0006_{16}	'M' = $4D_{16}$		$000E_{16}$	FF_{16}
0007_{16}	'6' = 36_{16}		$000F_{16}$	FF_{16}

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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 0000_{16} to 0008_{16} of EPROM.

EPROM type	27512
The pseudo-command	*= $\triangle$ $0000 .BYTE $\triangle$ 'M37516M6–'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

☐ Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

File code ☐☐☐☐☐☐☐☐ (hexadecimal notation)

Mask file name ☐☐☐☐☐☐☐☐ .MSK (equal or less than eight characters)

※ 2. Mark specification

M37516M6-XXXHP is specified to the standard mark.

※ 3. Usage conditions

Please answer the following questions about usage for use in our product inspection :

(1) How will you use the X_{IN}-X_{OUT} oscillator?

☐ Ceramic resonator   ☐ Quartz crystal

☐ External clock input   ☐ Other (   )

At what frequency? $f(X_{IN})$ = ☐☐☐☐ MHz

(2) Which function will you use the pins $P2_1/X_{CIN}$ and $P2_0/X_{COUT}$ as $P2_1$ and $P2_0$, or X_{CIN} and X_{COUT} ?

☐ Ports $P2_1$ and $P2_0$ function   ☐ X_{CIN} and X_{COUT} function (external resonator)

(3) Will you use the I^2C-BUS function or the SM-BUS function ?

☐ I^2C-BUS function used   ☐ SM-BUS function used

☐ Not used

※ 4. Comments

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