

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

REJ19B0761-0100

**RENESAS TECHNOLOGY SINGLE-CHIP 16-BIT
 MICROCOMPUTER M30621M8-XXXGP
 MASK ROM CONFIRMATION FORM**

Mask ROM number	
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The ASCII code for the type No. can be written to EPROM addresses 00000₁₆ to 0000F₁₆ by specifying the pseudo-instructions for the respective EPROM type shown in the following table at the beginning of the assembler source program.

EPROM type	27C201	27C401
Code entered in source program	△ .SECTION△ ASCII CODE, ROM DATA △ .ORG △ 0C0000H △ .BYTE △ ' M30621M8- '	△ .SECTION△ ASCII CODE, ROM DATA △ .ORG △ 080000H △ .BYTE △ ' M30621M8- 'R

Note: The ROM cannot be processed if the type No. written to the EPROM does not match the type No. in the check sheet.

In the case of floppy disks

Renesas processes the mask files generated by the mask file generation utilities out of those held on the floppy disks you give in to us, and forms them into masks. Hence, we assume liability provided that there is any discrepancy between the contents of these mask files and the ROM data to be burned into products we produce. Check thoroughly the contents of the mask files you give in. Prepare 3.5 inches 2HD (IBM format) floppy disks. And store only one mask file in a floppy disk.

Microcomputer type No. : M30621M8-XXXGP

File code :

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 (hex)

Mask file name :

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 .MSK (alpha-numeric 8-digit)

※2. Mark specification

The mark specification differs according to the type of package. After entering the mark specification on the separate mark specification sheet (for each package), attach that sheet to this masking check sheet for submission to Renesas.

For the M30621M8-XXXGP, submit the 80P6S mark specification sheet.

※3. Usage Conditions

For our reference when of testing our products, please reply to the following questions about the usage of the products you ordered.

(1) Which kind of X_{IN}-X_{OUT} oscillation circuit is used?

- | | |
|---|---|
| <input type="checkbox"/> Ceramic resonator | <input type="checkbox"/> Quartz-crystal oscillator |
| <input type="checkbox"/> External clock input | <input type="checkbox"/> Other () |

What frequency do you use?

f(X_{IN}) = MHz

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Mask ROM number	
-----------------	--

- (2) Which kind of XCIN-XCOUT oscillation circuit is used?
- Ceramic resonator Quartz-crystal oscillator
 External clock input Other ()

What frequency do you use?

f(XCIN) = kHz

- (3) Which operation mode do you use?
- Single-chip mode Memory expansion mode
 Microprocessor mode

- (4) Which operating ambient temperature do you use?
- 10 °C to 75 °C -20 °C to 75 °C -40 °C to 75 °C
 -10 °C to 85 °C -20 °C to 85 °C -40 °C to 85 °C

- (5) Which operating supply voltage do you use?
- 2.7V to 3.2V 3.2V to 3.7V 3.7V to 4.2V
 4.2V to 4.7V 4.7V to 5.2V 5.2V to 5.5V

Thank you cooperation.

※ 4. Special item (Indicate none if there is no specified item)