

## Notes

### Using the PMC Card in A Backplane Environment

The 79PMC438 reference board can also be used with a CPCI adapter card in a backplane environment. When using the PMC card along with the CPCI adapter (79CP438), and a 8 slot backplane, the interrupt assignment changes to the following:

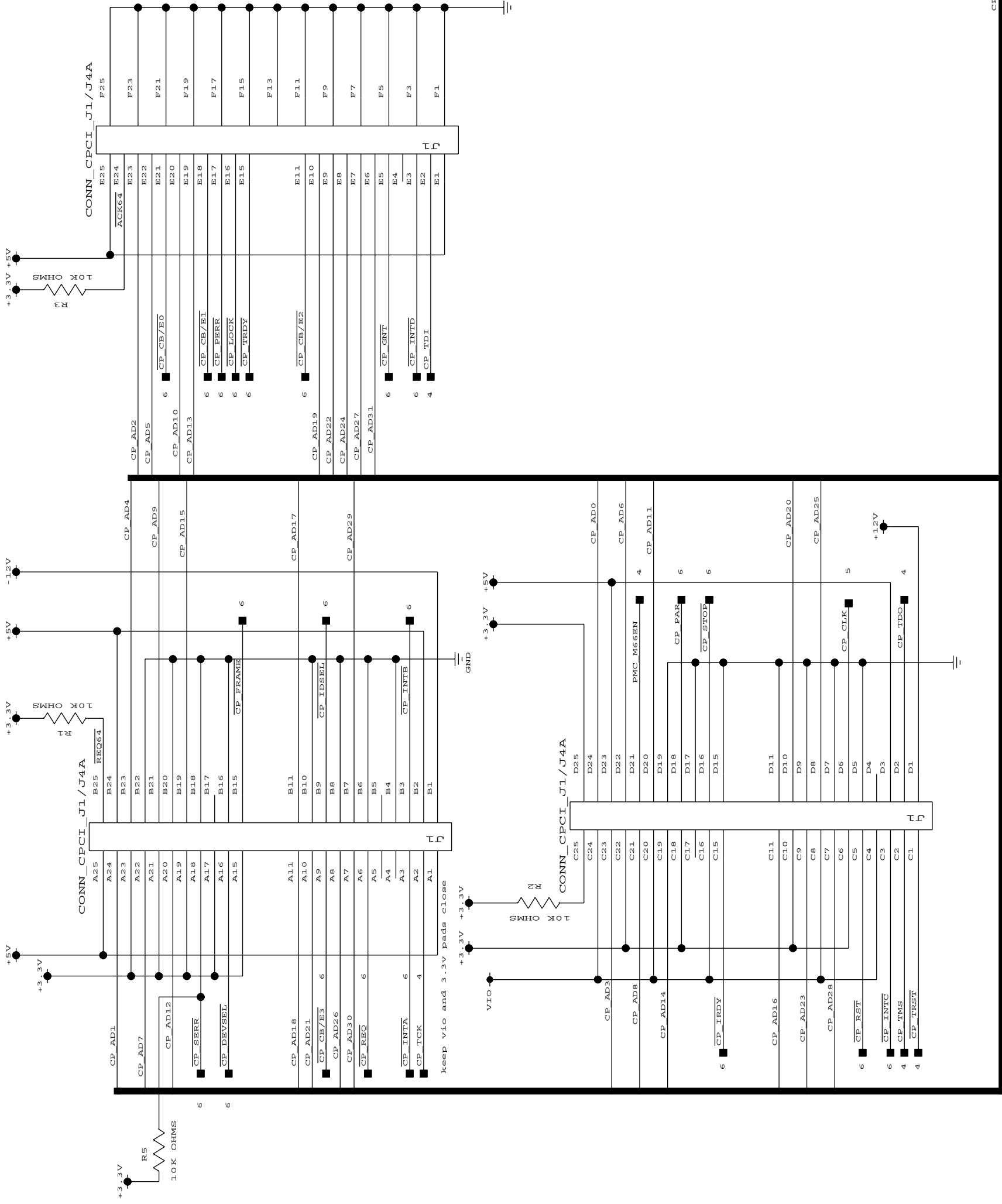
Device #	ID_SEL	Config. Address	CPCI_SLOT #	Assigned Interrupt	GPIO Pin #
15	PCI_AD25	0x8000_7800	SLOT_8	D	11
16	PCI_AD26	0x8000_8000	SLOT_7	C	10
17	PCI_AD27	0x8000_8800	SLOT_6	B	9
18	PCI_AD28	0x8000_9000	SLOT_5	A	8
19	PCI_AD29	0x8000_9800	SLOT_4	D	11
20	PCI_AD30	0x8000_A000	SLOT_3	C	10
21	PCI_AD31	0x8000_A800	SLOT_2	B	9

Figure 1 Interrupt Assignments Used with CPCI Adapter Card

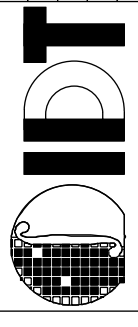
### CPCI Adapter Card Schematics

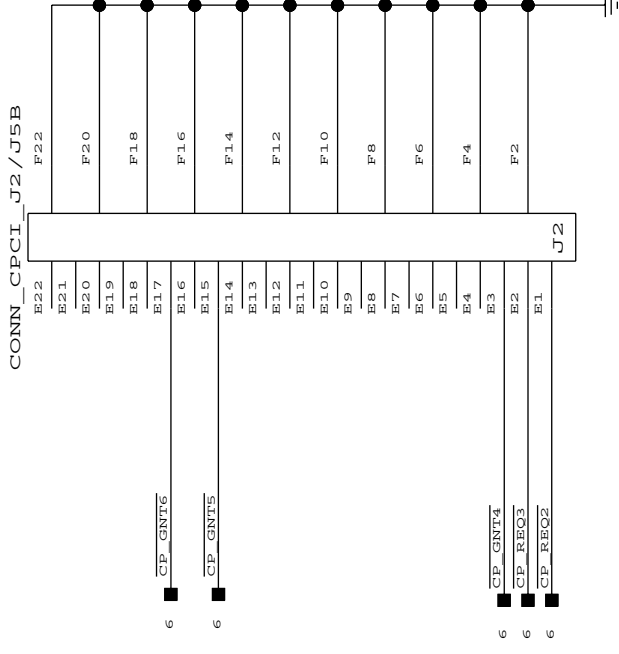
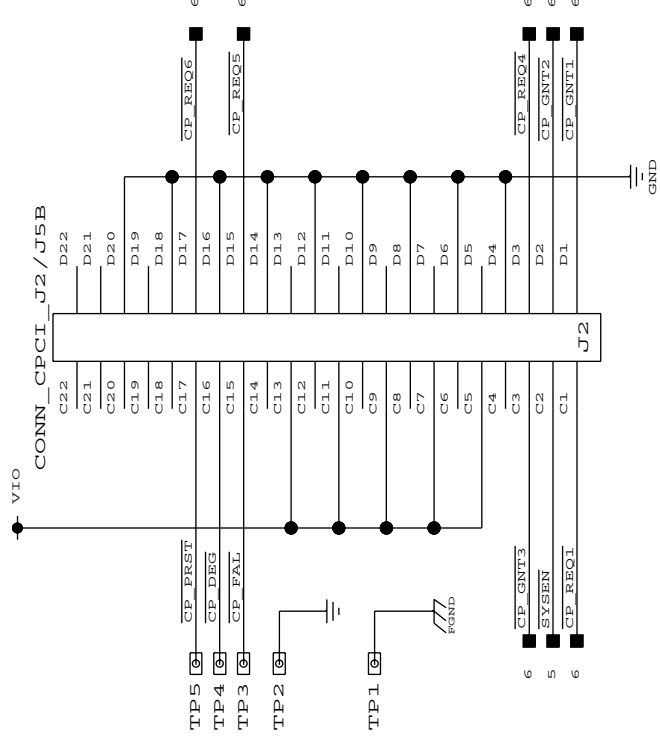
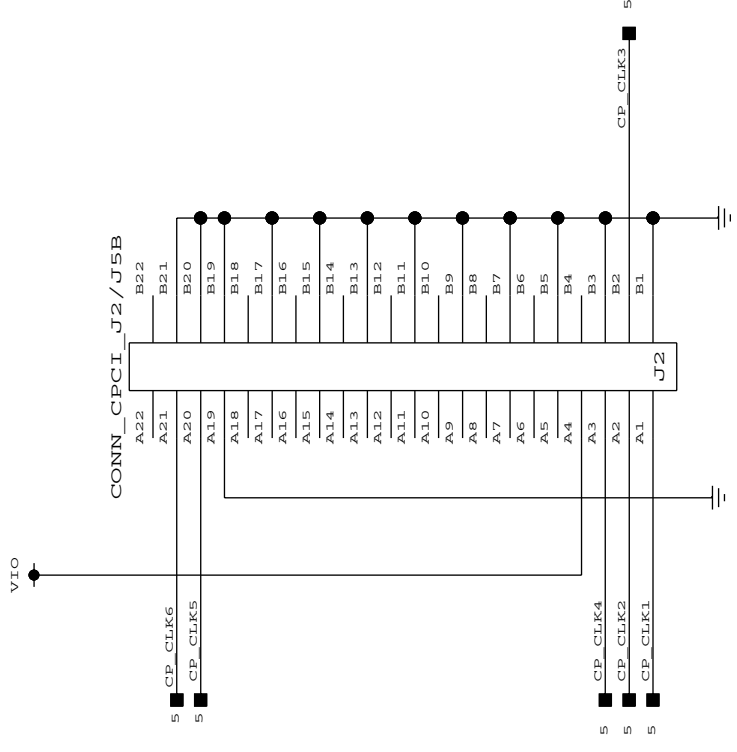
DCN		REV		DESCRIPTION		DATE		CHG BY	
SFTGC-000901		1.0		INITIAL RELEASE		11-20-02		G.S.	

SHEET	DESCRIPTION
1	TABLE OF CONTENTS
2	CPCI CONNECTOR 1
3	CPCI CONNECTOR 2
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6	CPCI TERMINATIONS (STD.)

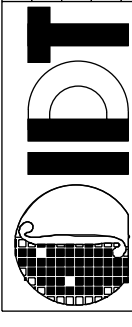


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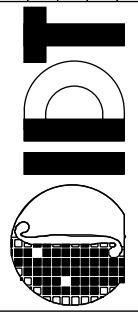
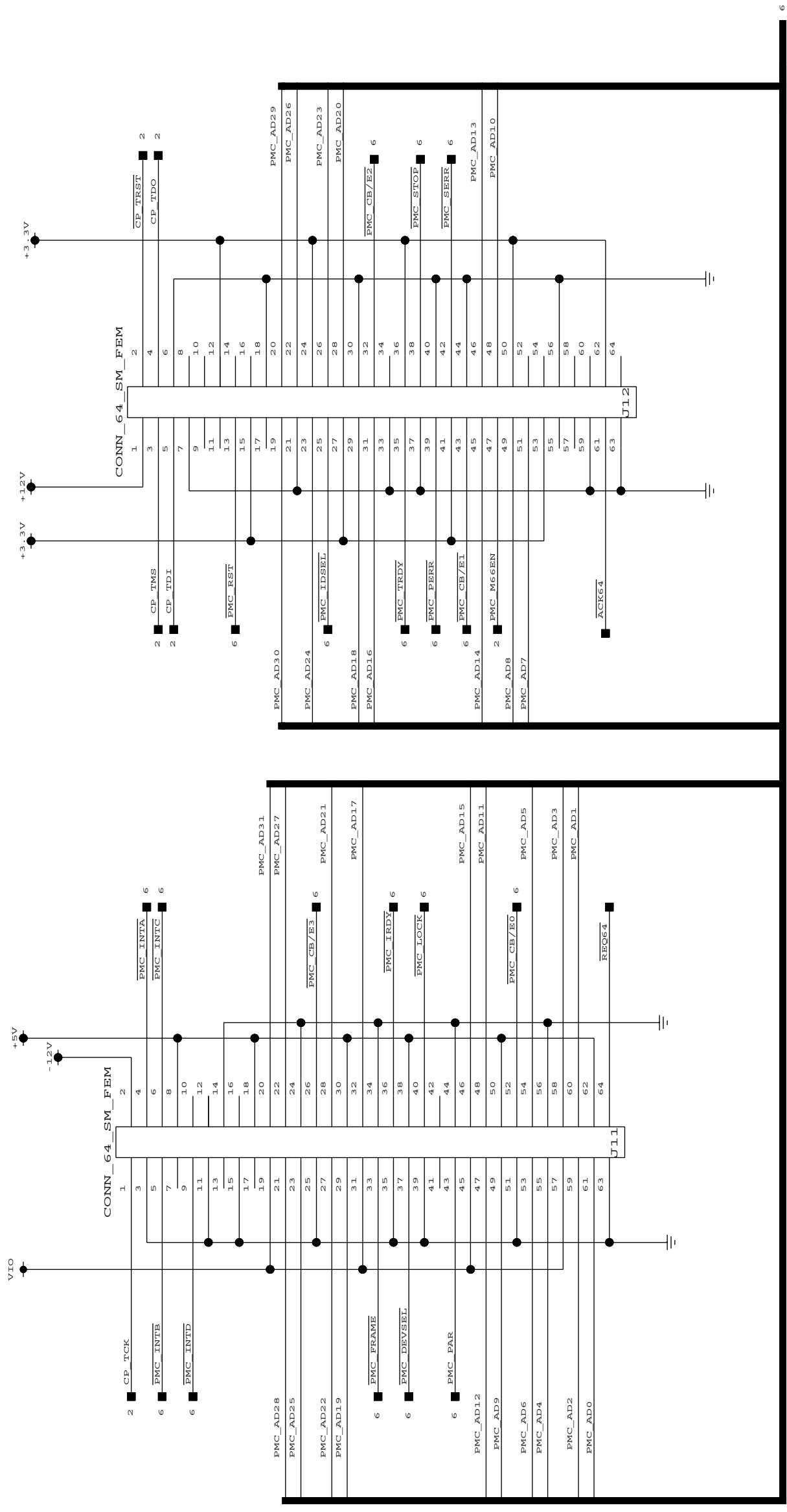




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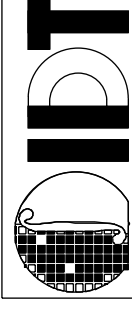
SH. TITLE: CPCI connector 2  
 PROJECT: 79CPC438 ACACIA CPCI ADAPTER CARD  
 DOC NO: STGSCH-00011 REV: 1.0  
 FAB P/N: 18-564-000 PG NO: 3 OF 6  
 DWG BY: RAKESH BHATIA ENG: RAKESH BHATIA



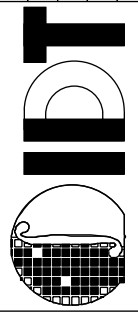
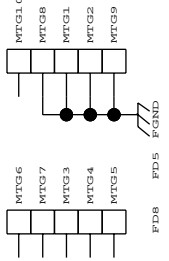
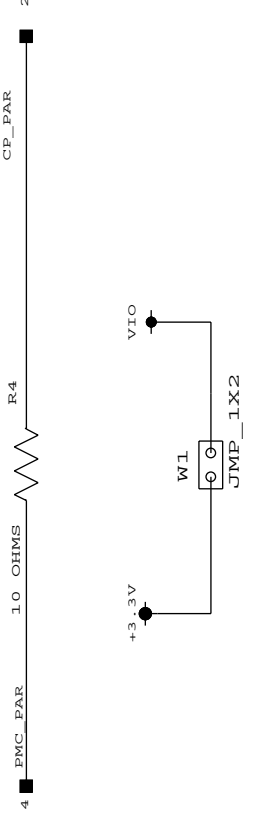
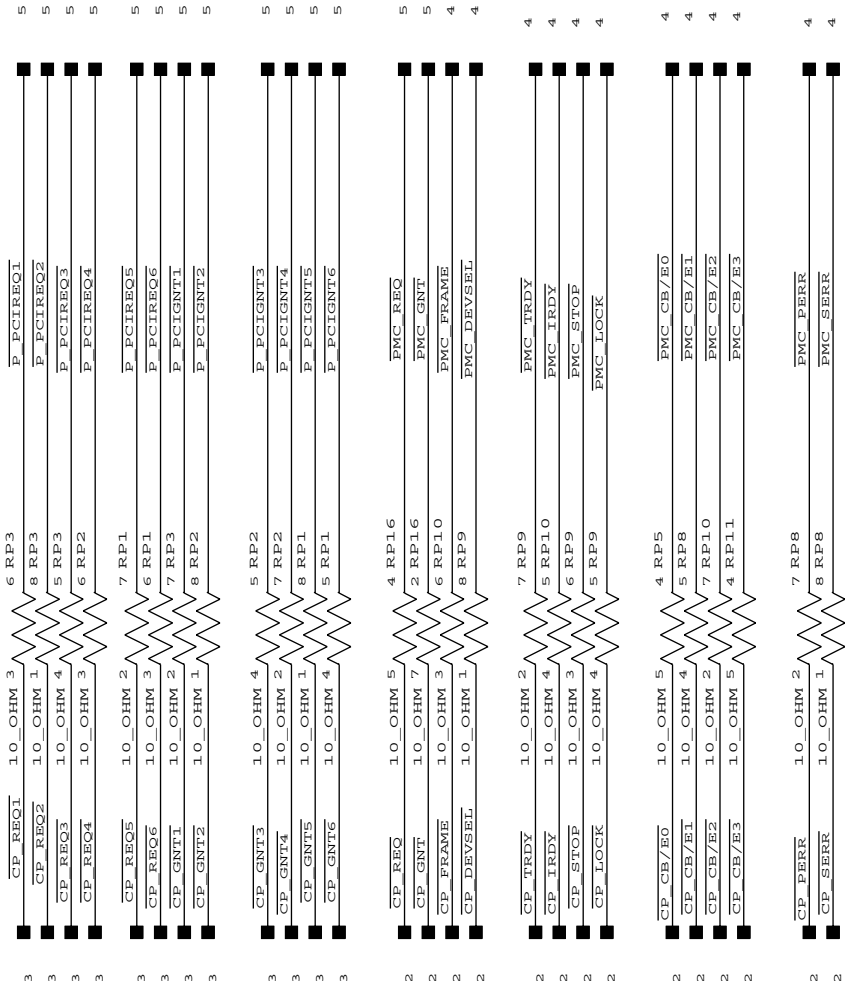
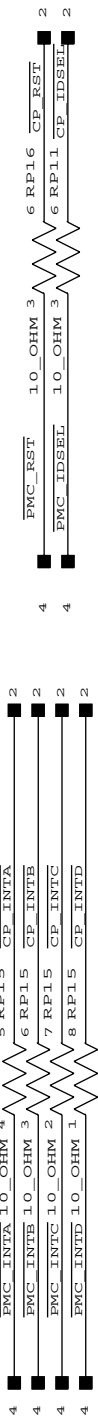
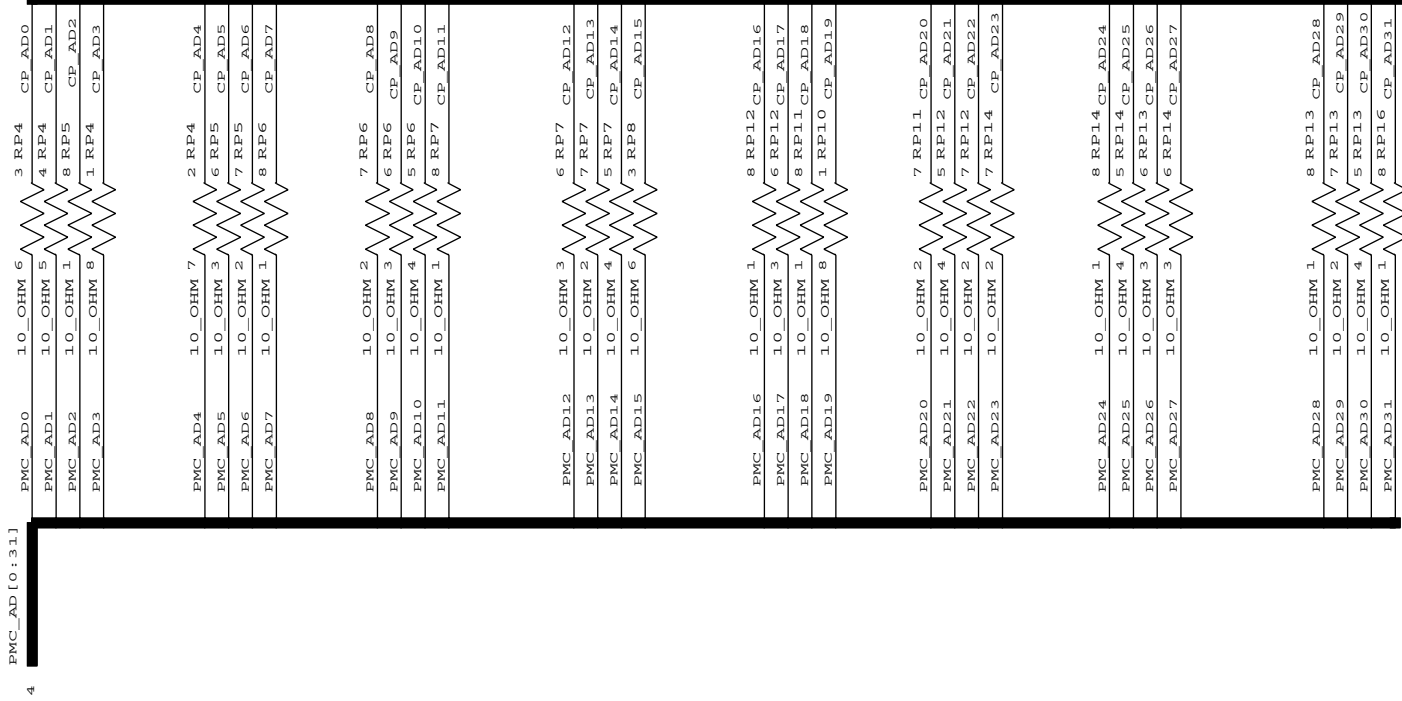
CONN\_64\_SM\_FEM

3	CP_CLK1	1	2
6	P_FCIREQ1	3	4
6	P_FCIGNT1	5	6
3	CP_CLK2	7	8
6	P_FCIREQ2	9	10
6	P_FCIGNT2	11	12
3	CP_CLK3	13	14
6	P_FCIREQ3	15	16
6	P_FCIGNT3	17	18
3	CP_CLK4	19	20
6	P_FCIREQ4	21	22
6	P_FCIGNT4	23	24
3	CP_CLK5	25	26
6	P_FCIREQ5	27	28
6	P_FCIGNT5	29	30
3	CP_CLK6	31	32
6	P_FCIREQ6	33	34
6	P_FCIGNT6	35	36
3	SYSEN	37	38
2	CP_CLK	39	40
6	PMC_REQ	41	42
6	PMC_GNT	43	44
		45	46
		47	48
		49	50
		51	52
		53	54
		55	56
		57	58
		59	60
		61	62
		63	64

J14



10 ohm resistors need to be close to cpci conn



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