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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Evaluation Board Information

μ PC8178TB

1.9 GHz Silicon MMIC Evaluation Board

- Evaluation Board Pattern Layout
- Circuit Description
- Circuit Current and Power Gain Data
- 1 dB Gain Compression Output Power Data
- Isolation Data
- Input and Output Return Loss Data

For the purposes of maintaining up-to-date information, the contents of this document are subject to change without notice.

This document outlines general applications for this product. The application circuits and circuit constants provided in this document are simply examples and should not be used for mass production design. Be aware also that there is no intention to standardize the restrictions and characteristics of these application circuits.

The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

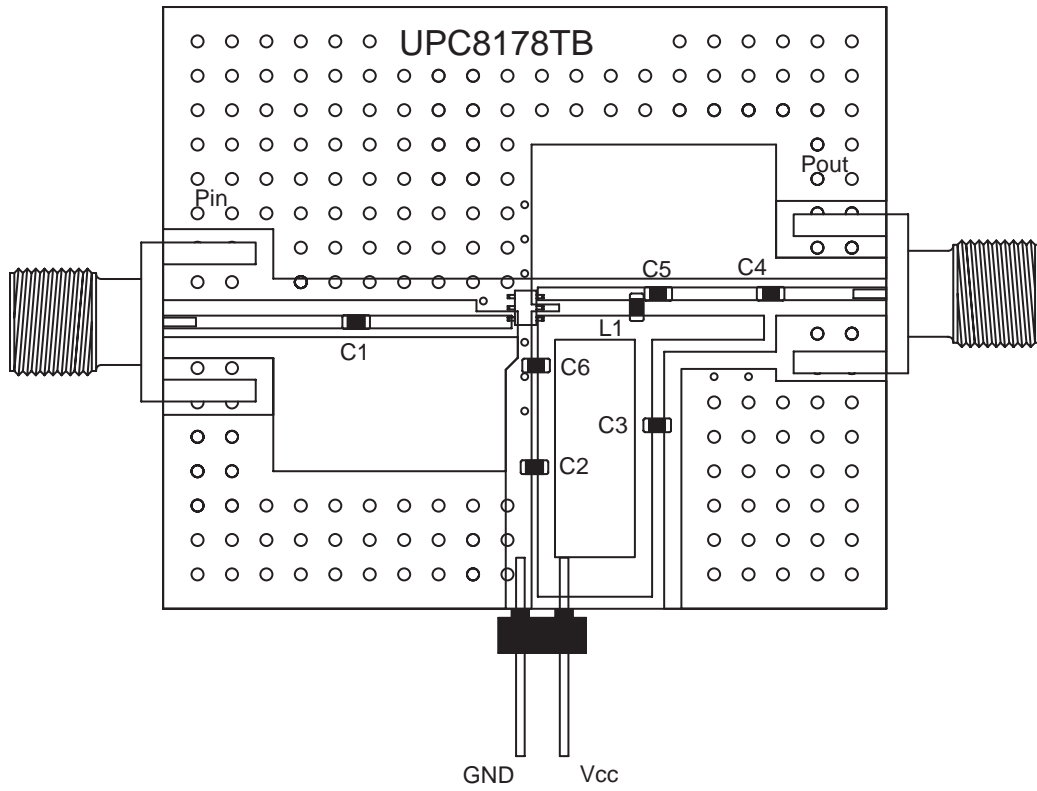
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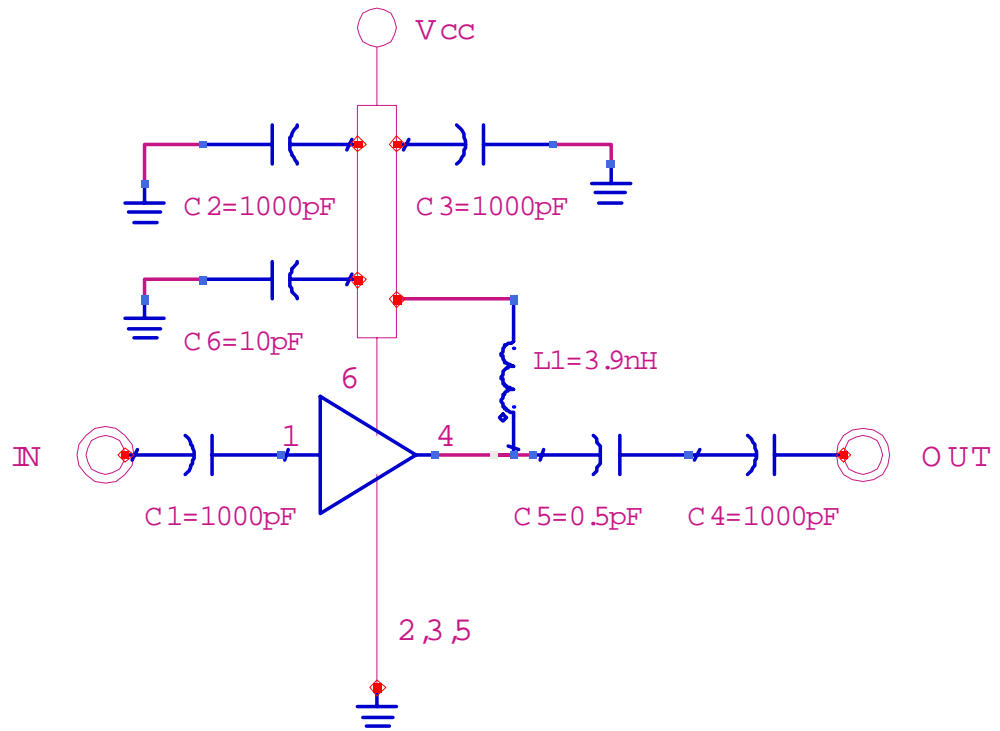
Evaluation Board Pattern Layout

uPC8178TB $f = 1.9 \text{ GHz}$



Circuit Description

uPC8178TB $f=1.9\text{GHz}$



KC-8178TB**@ f = 1.9 GHz, V_{CC} = 3.0 V**

Symbol	I _{CC}	G _P	P _{O(1 dB)}	ISL	RL _{in}	RL _{out}
Unit	mA	dB	dBm	dB	dB	dB
Conditions	No signal	P _{in} = -30 dBm	—	P _{in} = -30 dBm	P _{in} = -30 dBm	P _{in} = -30 dBm
TYP.	1.90	11.5	-7.0	40.0	8.0	—
1	1.76	10.9	-8.1	42.5	7.8	17.2
2	1.77	10.7	-7.5	42.2	7.5	18.5
3	1.78	10.9	-8.1	43.0	7.9	20.2
4	1.76	10.9	-8.3	42.8	7.8	14.0
5	1.74	10.7	-7.5	44.4	7.8	18.2
AVE.	1.76	10.8	-7.9	43.0	7.8	17.6

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