User's Manual



RX850V4 Ver. 4.41

Real-Time Operating System

Debug for CubeSuite Ver.1.20

Target Tool RX850V4 Ver.4.41

Document No. U20045EJ1V0UM00 (1st edition) Date Published February 2010

© NEC Electronics Corporation 2010 Printed in Japan [MEMO]

SUMMARY OF CONTENTS

CHAPTER 1 GENERAL ... 12

CHAPTER 2 FUNCTIONS ... 13

APPENDIX A WINDOW REFERENCE ... 19

APPENDIX B INDEX ... 64

Windows, and Windows Vista are either registered trademarks or trademarks of Microsoft Corporation in the United States and/or other countries.

TRON is the abbreviation of "The Real-time Operating system Nucleus."

ITRON is the abbreviation of "Industrial TRON."

 μ ITRON is the abbreviation of "Micro Industrial TRON."

TRON, ITRON, and μ ITRON do not refer to any specific product or products.

The μ ITRON4.0 Specification is an open real-time kernel specification developed by TRON Association.

The μ ITRON4.0 Specification document can be obtained from the TRON Association web site (http://www.assoc.tron.org/).

The copyright of the μ ITRON4.0 Specification document belongs to TRON Association.

- The information in this document is current as of February, 2010. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets or data books, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Electronics. NEC Electronics assumes no responsibility for any errors that may appear in this document.
- NEC Electronics does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from the use of NEC Electronics products listed in this document or any other liability arising from the use of such products. No license, express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Electronics or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software and information in the design of a customer's equipment shall be done under the full responsibility of the customer. NEC Electronics assumes no responsibility for any losses incurred by customers or third parties arising from the use of these circuits, software and information.
- While NEC Electronics endeavors to enhance the quality, reliability and safety of NEC Electronics products, customers agree
 and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property
 or injury (including death) to persons arising from defects in NEC Electronics products, customers must incorporate sufficient
 safety measures in their design, such as redundancy, fire-containment and anti-failure features.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific". The "Specific" quality grade applies only to NEC Electronics products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.
 - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.
 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anticrime systems, safety equipment and medical equipment (not specifically designed for life support).
 - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

- (Note 1) "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (Note 2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).

(M8E0909E)

[MEMO]

INTRODUCTION

Readers	This manual is intended f V850 microcontrollers pro		o design and develop application systems using
Purpose	This manual is intended described the organizatio		to understand the functions of the RX850V4
Organization	This manual consists of the	he following	major sections.
	• GENERAL • FUNCTIONS		
How to read this manual			manual have general knowledge in the fields of icrocontrollers, C language, and assemblers.
	To understand the hardw → Refer to the User's Ma		of the V850 microcontrollers ware of each product.
	To understand the instruc	ction function	s of the V850 microcontrollers
	ightarrow Refer to the V850ES A	Architecture	User's Manual (U15943E) or
	V850E1 Architecture	User's Man	ual (U14559E).
Conventions	Data significance:	Hiaher diai	ts on the left and lower digits on the right
	Note:	0 0	or item marked with Note in the text
	Caution:	Informatior	requiring particular attention
	Remark:	Supplemer	ntary information
	Numerical representation	: BinaryXX	XX or XXXXB
		Decimal>	XXX
			al0xXXXX
	Prefixes indicating power	-	s space and memory capacity):
		K (kilo)	2 ¹⁰ = 1024
		M (mega)	$2^{20} = 1024^{2}$

Related Documents

Read this manual together with the following documents.

The related documents indicated in this publication may include preliminary versions. However, preliminary versions are not marked as such.

Documents related to development tools (user's manuals)

Docur	nent Name	Document No.
RX Series	Start for CubeSuite for Ver.1.20	U20041E
	Message for CubeSuite for Ver.1.20	U20042E
RX850V4 Ver.4.41	Coding for CubeSuite for Ver.1.20	U20044E
	Debug for CubeSuite for Ver.1.20	This document
	Analysis for CubeSuite	U19439E
	Internal Structure for CubeSuite for Ver.1.20	U20046E
CubeSuite	Start	U19809E
Integrated Development Environment	Analysis	U19816E
	Programming	U19390E
	Message	U19810E
	Coding for CX compiler	U19811E
	Build for CX compiler	U19812E
	V850 Coding	U19383E
	V850 Build	U19386E
	V850 Debug	U19815E
	V850 Design	U20184E

Caution The related documents listed above are subject to change without notice. Be sure to use the latest edition of each document when designing.

TABLE OF CONTENTS

CHAPTER 1 GENERAL ... 12

- 1.1 Overview ... 12
- 1.2 Features ... 12

CHAPTER 2 FUNCTIONS ... 13

- 2.1 Overview ... 13
- 2.2 Open Realtime OS Resource Information Panel ... 142.2.1 Select item ... 142.2.2 Change display order ... 15
- 2.3 Confirm Resource Information ... 16
- 2.4 Change Resource Information ... 17 2.4.1 Issue service call ... 18

APPENDIX A WINDOW REFERENCE ... 19

A.1 Description ... 19

APPENDIX B INDEX ... 64

LIST OF FIGURES

Figure No.	Title, Page
2-1 Select Item 14	
2-2 Change Display Or	der (Move "ID" Column) 15
2-3 Issue Service Call (Context Menu) 18
2-4 Issue Service Call (ServiceCall Dialog Box) 18
A-1 Realtime OS Reso	urce Information Panel 20
A-2 [System] Tab 2	2
A-3 [Memory Area] Tab	9 25
A-4 [Task] Tab 27	
A-5 [Task] Tab (Locking	g Mutex Information) 30
A-6 [Semaphore] Tab .	33
A-7 [Semaphore] Tab (Waiting Task Information) 34
A-8 [Eventflag] Tab	35
A-9 [Eventflag] Tab (Wa	aiting Task Information) 36
A-10 [Data Queue] Tab	· 38
A-11 [Data Queue] Tab	(Sending Waiting Task Information) 39
A-12 [Data Queue] Tab	(Receiving Waiting Task Information) 39
A-13 [Data Queue] Tab	(Receiving Waiting Data Information) 40
A-14 [Mailbox] Tab	41
A-15 [Mailbox] Tab (Wa	aiting Task Information) 42
A-16 [Mailbox] Tab (Wa	aiting Message Information) 42
A-17 [Mutex] Tab 4	4
A-18 [Mutex] Tab (Wait	ing Task Information) 45
A-19 [Fixed-Sized Mem	lory Pool] Tab 46
A-20 [Fixed-Sized Merr	nory Pool] Tab (Waiting Task Information) 47
A-21 [Variable-Sized M	emory Pool] Tab 48
A-22 [Variable-Sized M	emory Pool] Tab (Waiting Task Information) 49
A-23 [Cyclic Handler] T	ab 50
A-24 [Interrupt Handler]] Tab 52
A-25 [Initialize Routine]	Tab 54
A-26 [Extended Service	e Call] Tab 56
A-27 [Ready Queue] Ta	ab 58
A-28 [Ready Queue] Ta	ab (Executing Task Information) 59
A-29 [Timer Queue] Ta	b 60
A-30 [Timer Queue] Ta	b (Waiting Task Information) 61
A-31 [Timer Queue] Ta	b (Cyclic Handler Information) 61
A 22 Comise Coll Dieles	

A-32 ServiceCall Dialog Box ... 63

LIST OF TABLES

Table N	lo.
---------	-----

Title, Page

2-1 Select Item ... 14

2-2 Issuable Service Calls ... 17

A-1 Panel/Dialog Box List ... 19

CHAPTER 1 GENERAL

CubeSuite is an integrated development environment used to carry out tasks such as design, coding, build and debug for developing application systems for microcontrollers manufactured by NEC Electronics.

This manual describes the task debugging tool. This tool is useful for debugging programs using the "RX850V4" real-time OS functionality within this integrated program-development process.

1.1 Overview

When debugging programs using RX850V4 functionality, it is possible to use the task debugging tool to confirm and modify RX850V4 resource information (e.g. system information and memory area information) that changes dynamically as the program executes.

1.2 Features

Below are the features of the task debug tool.

- Confirm resource information

When the program running in the debugging tool is stopped at an arbitrary location, the current status of the resource information appears in the Realtime OS Resource Information panel.

- Change resource information

It is possible to dynamically modify the resource information (e.g. the RX850V4 system time and task status) by issuing service calls provided by the RX850V4 from the Realtime OS Resource Information panel.

CHAPTER 2 FUNCTIONS

This chapter describes the key functions provided by the task debug tool along with operation procedures.

2.1 Overview

The task debugging tool can be used to confirm and modify RX850V4 resource information (e.g. system information and memory area information) that changes dynamically as the program executes.

The operating procedures for the task debugging tool are described below.

(1) Start CubeSuite

Launch CubeSuite from the [start] menu of Windows.

Remark See "CubeSuite Start" for details on "Start CubeSuite".

(2) Open project

Open the project to debug.

Remark See "CubeSuite Start" for details on "Open project".

(3) Select debug tool

Select the type of debugging tool with which to debug the program (IECUBE, MINICUBE, MINICUBE2, or Simulator).

Remark See "CubeSuite Debug" for details on "Select debug tool".

(4) Download programs

Download the program to debug.

Remark See "CubeSuite Debug" for details on "Download programs".

(5) Open Realtime OS Resource Information Panel

Open the Realtime OS Resource Information panel.

Remarks 1. When a program using RX850V4 functionality is downloaded, this panel opens automatically.

2. The value will be indeterminate for resource information shown when RX850V4 system initialization is incomplete, because it will not be managed by the RX850V4.

(6) Execute/stop programs

Run the program to the location for which you wish to display resource information.

Remark See "CubeSuite Debug" for details on "Execute/stop programs".

(7) Confirm Resource Information

On the Realtime OS Resource Information panel tabs (e.g. [System] tab and [Memory Area] tab), check the current status of the resource information.

(8) Change Resource Information

Change the contents of the resource information (e.g. RX850V4 system time and task status) by issuing service calls from the Realtime OS Resource Information panel.

2.2 Open Realtime OS Resource Information Panel

The Realtime OS Resource Information panel is used to confirm or modify the resource information (e.g. system information and memory area information). This panel opens automatically when a program using the RX850V4 functionality is downloaded.

2.2.1 Select item

The task debugging tool enables you to select the items to display in the Realtime OS Resource Information panel. To select which items are displayed, right click on the header (header column or row) in the Realtime OS Resource Information panel, and from the context menu that appears, select "Display".

Realtime OS Resource I	V - V - V	м V	М	∀ Fi ∀ V ∀ C ∀	Int \ Ini \ E \	🛛 🛛 🛛
Eventflag Name	ID Queue	State		Current Flag Pattern	Initial Flag Pattern	Attribute
▶ • 📐 ID_FLG1	Display Notation	• •	× ×	ID Queue Status	1000	TA_TFIFO TA_\
			~ ~	Current Flag Pattern Initial Flag Pattern		
			~	Attribute		
<		10				>

Figure 2-1. Select Item

Remark Select items to display by selecting their checkboxes.

Table 2-1. Select Item

Checked	The item in question will be displayed.
Not checked	The item in question will not be displayed.

2.2.2 Change display order

The task debugging tool enables you to change the order of items displayed in the Realtime OS Resource Information panel.

To change the display order, drag the columns in the Realtime OS Resource Information panel, and drop them to the desired position.





2.3 Confirm Resource Information

Check the resource information when program execution is stopped via the various tabs of the Realtime OS Resource Information panel (e.g. [System] tab and [Memory Area] tab).

The Realtime OS Resource Information panel is made up of the following tabs.

- [System] tab
- [Memory Area] tab
- [Task] tab
- [Semaphore] tab
- [Eventflag] tab
- [Data Queue] tab
- [Mailbox] tab
- [Mutex] tab
- [Fixed-Sized Memory Pool] tab
- [Variable-Sized Memory Pool] tab
- [Cyclic Handler] tab
- [Interrupt Handler] tab
- [Initialize Routine] tab
- [Extended Service Call] tab
- [Ready Queue] tab
- [Timer Queue] tab

Remark Switch tabs in the tab selection area of the Realtime OS Resource Information panel.

2.4 Change Resource Information

You can change the contents of the resource information (e.g. RX850V4 system time and task status) by issuing service calls from the Realtime OS Resource Information panel.

Below is a list of service calls that can be issued from the Realtime OS Resource Information panel.

Tab Name	Service Call Name
[System] tab	set_tim, dis_dsp, ena_dsp, loc_cpu, unl_cpu
[Memory Area] tab	-
[Task] tab	rel_wai, ter_tsk, chg_pri, sus_tsk, rsm_tsk, frsm_tsk, wup_tsk, can_wup, act_tsk, can_act, ras_tex
[Semaphore] tab	sig_sem, pol_sem
[Eventflag] tab	set_flg, clr_flg
[Data Queue] tab	psnd_dtq, fsnd_dtq, prcv_dtq
[Mailbox] tab	-
[Mutex] tab	unl_mtx
[Fixed-Sized Memory Pool] tab	-
[Variable-Sized Memory Pool] tab	-
[Cyclic Handler] tab	sta_cyc, stp_cyc
[Interrupt Handler] tab	-
[Initialize Routine] tab	-
[Extended Service Call] tab	-
[Ready Queue] tab	rot_rdq
[Timer Queue] tab	-

Table 2-2.	Issuable	Service	Calls
	13344510	001 1100	ouns

Remark See "RX850V4 Coding" for details about service call functions.

2.4.1 Issue service call

To issue a service call, right click with the mouse in the footer (footer column or row) of one of the tabs on the Realtime OS Resource Information panel, and from the context menu that appears, select "Service Call".

Figure 2-3. Issue Service Call (Context Menu)

Realtime OS Resource Ir	format	ion	2000 0000 0000 40	14	
∕ S ∖ M ∖ T ∖ S)	E_ \	D \ М \ М	🗸 Fi 🔪 V 🗎 C '	⟨Int ⟨Ini \ E \	(R ∖ Ti \ ₹
Eventflag Name	ID	Queue Status	Current Flag Pattern	Initial Flag Pattern	Attribute
🕨 💊 ID_FLG1	0.01	Erech	<u>0-00000000000000000000000000000000000</u>	0x00000000	TA_TFIF0 TA_V
	2	Service Call 🔹 🕨	set_flg		1
			clr_flg		
				-	
<		400) (>

- **Remarks 1.** If a service call is not embedded in (linked to) the downloaded program, or the necessary conditions for executing the service-call process are not met, then the service call will be grayed out.
 - 2. When a service call name is selected from the context menu, the ServiceCall dialog box opens. Enter an appropriate value in the text box as required, then click [OK] to execute the service-call process.



ServiceCall	
set_flg (0x1,);
	OK Cancel

APPENDIX A WINDOW REFERENCE

This appendix describes the panels and dialog boxes of the task debugging tool.

A.1 Description

The panels and dialog boxes of the task debugging tool are listed below.

Table A-1. Panel/Dialog Box List

Panel/Dialog Box Name	Description
Realtime OS Resource Information panel	This panel displays the resource information (e.g. system information and memory area information) of the RX850V4.
ServiceCall dialog box	Execute a service call process.

Realtime OS Resource Information panel

This panel displays the resource information (e.g. system information and memory area information) of the RX850V4.

Realtime OS Resource Information	. \/ M \/ M \/ Fi \/ V \/ C \/ Int \/ Ini \/ E \/ B \/ Ti \
RTOS Name	R×850V4
Version	0x430
System Time	0x00000000001
Iterrupt Nest	0
Dispatching	Enable
CPU Lock	Unlocked
System Stack Area	0xFFFFC584 - 0xFFFFCDE8 (0x864)
Current System SP	0xFFFFCDE8
Idle Routine	0x0000A444
Number of Priority	4
Number of Task	5
Number of Semaphore	2
Number of Eventflag	1
Number of Data Queue	2
Number of Mailbox	2
Number of Mutex	2
Number of Fixed-Sized Memory Pool	2
Number of Variable-Sized Memory Po	ol 2
Number of Cyclic Handler	3
Number of Interrupt Handler	3
Number of Extended Service Call Rou	itine 1

Figure A-1.	Realtime	OS Resource	Information	Panel
-------------	----------	--------------------	-------------	-------

The following items are explained here.

- [How to open]
- [Description of each area]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Tab selection area

Select a tab to switch the content displayed in the Information display area. This panel has the following tabs:

- [System] tab
- [Memory Area] tab
- [Task] tab
- [Semaphore] tab

- [Eventflag] tab
- [Data Queue] tab
- [Mailbox] tab
- [Mutex] tab
- [Fixed-Sized Memory Pool] tab
- [Variable-Sized Memory Pool] tab
- [Cyclic Handler] tab
- [Interrupt Handler] tab
- [Initialize Routine] tab
- [Extended Service Call] tab
- [Ready Queue] tab
- [Timer Queue] tab

(2) Information display area

This area displays the resource information (e.g. system information and memory area information) of the RX850V4.

[System] tab

This tab displays the system information (e.g. RTOS Name and Version) of the RX850V4.

Figure A-2. [System] Tab

Realtime OS Resource Information S_ \ M \ T \ S \ E \ D \	M \ M \ Fi \ V \ C \ Int \ Ini \ E \ B \ Ti
RTOS Name	RX850V4
Version	0x430
System Time	0x00000000001
Iterrupt Nest	0
Dispatching	Enable
CPU Lock	Unlocked
System Stack Area	0xFFFFC584 - 0xFFFFCDE8 (0x864)
Current System SP	0xFFFFCDE8
Idle Routine	0x0000A444
Number of Priority	4
Number of Task	5
Number of Semaphore	2
Number of Eventflag	1
Number of Data Queue	2
Number of Mailbox	2
Number of Mutex	2
Number of Fixed-Sized Memory Pool	2
Number of Variable-Sized Memory Pool	2
Number of Cyclic Handler	3
Number of Interrupt Handler	3
Number of Extended Service Call Routine	. 1

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the system information (e.g. RTOS Name and Version) of the RX850V4. This area consists of the following items.

RTOS Name	"RX850V4" is shown.			
Version	The version number of the RX850V4 is shown.			
System Time	The system time of the RX850V4 (in milliseconds) is shown.			
Interrupt Nest	The nesting level of interrupt processes (including CPU exception processes) is shown.			
Dispatching	The system state of the RX850V4 is shown.			
	Disable Dispatch disabled state			
	Enable	Dispatch enabled state		
CPU Lock	The system state of the RX8	50V4 is shown.		
	Locked	CPU locked state		
	Unlocked	CPU unlocked state		
System Stack Area	The start address, end address, and size (in bytes) of the system stack are shown in the following format. Start address - End address (Size)			
Current System SP	If the processing program is operating on the system stack, the start address of the system stack is shown. If it is running on the task stack, then the current SP value is shown.			
Idle Routine	The start address of the idle routine is shown.			
Number of Priority	The maximum priority of the	The maximum priority of the task is shown.		
Number of Task	The total number of tasks is shown.			
Number of Semaphore	The total number of semaphores is shown.			
Number of Eventflag	The total number of eventflags is shown.			
Number of Data Queue	The total number of data queues is shown.			
Number of Mailbox	The total number of mailboxes is shown.			
Number of Mutex	The total number of mutexes is shown.			
Number of Fixed-Sized Memor Pool	The total number of fixed-sized memory pools is shown.			
Number of Variable-Sized Memory Pool	The total number of variable-sized memory pools is shown.			
Number of Cyclic Handler	The total number of cyclic ha	ndlers is shown.		
Number of Interrupt Handler	The total number of interrupt	handlers/CPU exception handlers is shown.		
Number of Extended Service Call Routine	The total number of extended service call routines is shown.			

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header column

Display	Displays cascade r	Displays cascade menus for selecting the header items to display.		
Selected item name	Version, System Current SP, Idle Semaphore, Nur Number of Mute: Sized Memory P Number of Exter	The following items are displayed for selection. Version, System Time, Interrupt Nest, Dispatching, CPU Lock, Stack Area, Current SP, Idle Routine, Number of Priority, Number of Task, Number of Semaphore, Number of Eventflag, Number of Data Queue, Number of Mailbox, Number of Mutex, Number of Fixed-Sized Memory Pool, Number of Variable- Sized Memory Pool, Number of Cyclic Handler, Number of Interrupt Handler, Number of Extended Service Call Routine Checked The item in question will be displayed.		
	Not checked	The item in question will not be displayed.		
Notation Selected item name	The following items Version, System Number of Priori Number of Priori Eventflag, Numb Number of Fixed Number of Cyclic	Displays cascade menus for selecting the display notation. The following items are displayed for selection. Version, System Time, Interrupt Nest, Stack Area, Current SP, Idle Routine, Number of Priority, hing, CPU Lock, Stack Area, Current SP, Idle Routine, Number of Priority, Number of Task, Number of Semaphore, Number of Eventflag, Number of Data Queue, Number of Mailbox, Number of Mutex, Number of Fixed-Sized Memory Pool, Number of Variable-Sized Memory Pool, Number of Cyclic Handler, Number of Interrupt Handler, Number of Extended Service Call Routine		
	DEC HEX	Displays value in signed decimal number. Displays value in hexadecimal number.		

(2) Footer column

Jump to Memory (Current SP)	Opens the Memory panel, and displays the contents of the Current System SP.		
Jump to Source (Idle Routine)	Opens the Editor panel, and displays the source code of the idle routine.		
Jump to Disassemble (Idle Rou- tine)	Opens the Disassemble panel, and displays the results of disassembling the idle routine.		
Service Call	Displays the types of service calls that can be issued. Note that if a service call is not embedded in (linked to) the downloaded program or the necessary conditions for executing the service-call process are not met, then the service call will be grayed out.		
	set_tim Set system time.		
	dis_dsp Disable dispatching.		
	ena_dsp	Enable dispatching.	
	loc_cpu	Lock the CPU.	
	unl_cpu	Unlock the CPU.	

[Memory Area] tab

This tab displays the memory area information (e.g. Area Name and Top Address) of the RX850V4.

Figure A-3. [Memory Area] Tab

	Realtime OS Resource I	nformation	
	/ S / M_ (T (S)	(E (D (M \ M \ Fi \ V \ C \ Int \ Ini \ E \ R \ Ti \ ₹
Γ	Area Name	Top Address	Size
(1) —	►• 🚺 rx_info	0x00003000	0x278
	• 🚺 rx_memory	0xFFFFC584	0xEA8
_			

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the memory area information (e.g. Area Name and Top Address) of the RX850V4. This area consists of the following items.

Area Name	The name of the managed memory area is shown.		
	rx_info	Area where initial information items related to OS resources that do not change dynamically are allocated as system information tables.	
	rx_memory	Area where the system stack, the task stack, data queue, fixed-sized memory pool and variable-sized memory pool are to be allocated.	
Top Address	The start address of the managed memory area is shown.		
Size	The size of the managed memory area (in bytes) is shown.		

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display	Displays cascade	Displays cascade menus for selecting the header items to display.		
Selected item name	5	The following items are displayed for selection. Top Address, Size		
	Checked	The item in question will be displayed.		
	Not checked	The item in question will not be displayed.		
Notation	Displays cascade	e menus for selecting the display notation.		
Selected item name	5	The following items are displayed for selection. Top Address, Size		
	DEC	Displays value in signed decimal number.		
	HEX	Displays value in hexadecimal number.		

(2) Footer row

Jump to Memory (Top Address)	Opens the Memory panel, and displays the contents of the managed memory
	area.

[Task] tab

This tab displays the task information (e.g. Task Name and ID) of the RX850V4.

Figure A-4. [Task] Tab

/ S (N	1)[T_](S	<u></u> (Ε	<u> </u>	Fi \ V \ C \ Int \ Ini	<u> </u>	<u> </u>
Task	Name	ID	Status	Wait Factor	Wait Data	Time Left
	D_TASK1	0x01	Dormant	-		0
	D_TASK2	0x02	Waiting-Suspended	FLG(0x01) ANDW TMO FIFO	0x111	99950
	🛅 ID_TASK3	0x03	Suspended		122	0
•	😼 ID_TASK4	0x04	Running			0
	ID_TASK5	0x05	Waiting	MTX(0x01) TMO FIFO	<u></u>	9950

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the task information (e.g. Task Name and ID) of the RX850V4. This layer consists of the following items.

Task Name	An icon indicating the current status of the task and the task name are shown in the following format. Icon Task name Note that if the task name is undefined, the name will appear as "ID".		
	DORMANT state		
	READY state RUNNING state		
	3	WAITING state	
	P	SUSPENDED state	
	1	WAITING-SUSPENDED state	
ID	The ID of the task is shown.		

Status	The current state of the task	is shown.		
	Dormant	DORMANT state		
	Ready	READY state		
	Running	RUNNING state		
	Waiting	WAITING state		
	Suspended	SUSPENDED state		
	Waiting-Suspended	WAITING-SUSPENDED state		
Wait Factor	The task wait factor (type of WAITING state, object ID and attribute of WAITING state) is shown in the format below. Type of WAITING state (Object ID) Attribute of WAITING state Note that if the current state of the task is other than WAITING state or WAITING- SUSPENDED state, "" appears. If the WAITING state type is sleeping state or delayed state, then "(Object ID)" is not shown.			
	[Type of WAITING state]			
	SLP	Sleeping state		
	DLY	Delayed state		
	SEM	Waiting state for a semaphore resource		
	FLG	Waiting state for an eventflag		
	SDTQ	Sending waiting state for data queue		
	RDTQ	Receiving waiting state for a data queue		
	MBX	Waiting state for a mailbox		
	MTX	Waiting state for a mutex		
	MPF	Waiting state for a fixed-sized memory pool		
	MPL	Waiting state for a variable-sized memory pool		
	[Attribute of WAITING state]			
	ANDW	AND waiting condition		
	ORW	OR waiting condition		
	ТМО	Waiting for timeout		
	FIFO	FIFO order		
	PRI	Task priority order		
Wait Data	shown. Note that if the task's current	ering the task's transition to WAITING state are state is other than waiting state for an eventflag, ata queue, or waiting state for a variable-sized		
	Wait bit pattern	Waiting state for an eventflag		
	Data element to be sent to the data queue	Sending waiting state for a data queue		
	Memory block size to be acquired	Waiting state for a variable-sized memory block		
Time Left		d state is released (in milliseconds) is shown. of the task is other than delayed state, "" appears.		

Disable All interrupts are disabled. Current Priority The current priority of the task is shown. Task Entry The start address of the task is shown. Current PC The current PC value of the task is shown. Current PC The current PC value of the task is shown. Current PC The current SP value of the task is shown. Stack Area The start address. end address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) Initial Priority The initial priority of the task is shown. Stark Area The suspansion cound of the task is shown. Suspend Count The suspansion cound of the task is shown. Address - End address (Size) The initial priority of the task is shown. Attribute The address of the task (so coing language, initial addrest is shown. Attribute The address of the task (so coing language, initial addreation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language initial addreasity of the task is shown. TA_ASM Assembly language Ta_ASM Ta_ACT READY state Nothing displayed Normal task Initial preemption s	Interrupt	The current interrupt state of the task is shown.					
Current Priority The current priority of the task is shown. Task Entry The start address of the task is shown. Current PC The current PC value of the task is shown. Current SP The current SP value of the task is shown. Stack Area The start address, and address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) Initial priority Initial Priority The initial priority of the task is shown. Suppend Count The suspension count of the task is shown. Activate Count The autopute count of the task is shown. Activate Count The attributes of the task (the task's coding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language of task] TA_HLNG C language Initial interrupt state [Coding language of task] TA_ACT TA_ASIM Assembly language [Initial activation state of task] TA_ASIM Assembly language [Initial activation. Ta_NSTR Restricted task Nothing displayed DORMANT state [Initial activation state of task] TA_DISPREEMPT Preemption is ababled		Disable	All interrupts are disabled.				
Task Entry The start address of the task is shown. Current PC The current PC value of the task is shown. Current SP The current SP value of the task is shown. Stack Area The start address, end address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) The initial priority of the task is shown. Supend Count The suspension count of the task is shown. Wakeup Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation state of task is shown. Activate Count The activation state and initial interrupt state of mask. Coding language of task! Tacht NG C language Ta_hLNG C language Tack type Ta_hLNG C language Tack type Ta_hLNG C language Tackt Tackt Ta_hLNG C language Tackt Ta_hLNG C language Tacktrye		Enable	All interrupts are enabled.				
Current PC The current PC value of the task is shown. Current SP The current SP value of the task is shown. Stack Area The start address, end address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) Initial Priority The initial priority of the task is shown. Suspend Count The suspension count of the task is shown. Wakeup Count The extravion request count of the task is shown. Activate Count The activation request count of the task is shown. Attribute The attributes of the task (the task is shown. Attribute The attributes of the task (the task is shown. Attribute The attributes of the task (the task is shown. Attribute The attributes of the task (the task is shown. Attribute The attributes of the task (the task is shown. Attribute The attributes of task is task ontrivation state of task? TA_HLNG C language TA_ASM Assembly language TA_ACT READY state Nothing displayed Normal task Initial preemption state of task? TA_DISPREEMT TA_DISPREEMT Preemption is disabled at task activation	Current Priority	The current priority of the tas	k is shown.				
Current SP The current SP value of the task is shown. Stack Area The start address, and address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) Initial Priority The initial priority of the task is shown. Suspend Count The suspension count of the task is shown. Makeup Count The wakeup request count of the task is shown. Activate Count The activation request count of the task is often and. Activate Count The activation state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state Initial interrupt state Coding language Initial activation state Task type Initial preemption state Initial interrupt state ICoding language Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state Itask type] Ta_ASTR Restricted task Initial interrupt state of task] TA_DISINT All interrupts are enabled at task activation. Ita_DISINT Ta_ENAINT All interrupts are enabled at task activation. TA_ENAINT <t< td=""><td>Task Entry</td><td colspan="6">The start address of the task is shown.</td></t<>	Task Entry	The start address of the task is shown.					
Stack Area The start address, end address, and size (in bytes) of the task stack are shown in the following format. Start address - End address (Size) Initial Priority The initial priority of the task is shown. Suspend Count The suspension count of the task is shown. Wakeup Count The wakeup request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state initial interrupt state Coding language Initial activation state Task type Initial preemption state Initial interrupt state Initial recrupt state ICoding language Initial activation state Task type Initial preemption state Initial interrupt state Initial activation state of task] TA_HLNG C language TA_ASM Assembly language Initial recrupt state Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state ITa_NEXTR ITak type] TA_ASTR Restricted task Nothing displayed Normal task Initial interrupt s	Current PC	The current PC value of the task is shown.					
the following format. Start address - End address (Size) Initial Priority The initial priority of the task is shown. Suspend Count The suspension count of the task is shown. Wakeup Count The wakeup request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation state fask (the task is oding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language of task] TA_LNG C language TA_LING C language Clanguage TA_ACT READY state Nothing displayed Nothing displayed DORMANT state Ifak type] TA_RSTR Restricted task Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. Initial interrupt state of task] TA_DISINT All	Current SP	The current SP value of the t	ask is shown.				
Suspend Count The suspension count of the task is shown. Wakeup Count The wakeup request count of the task is shown. Activate Count The activation request count of the task is shown. Activate Count The activation request count of the task is shown. Attribute The attributes of the task (the task's coding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language of task! Coding language of task! TA_HING C language TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state ONTHING displayed DORMANT state ITa_RSTR Restricted task Nothing displayed Normal task Initial interrupt state of task! TA_DISPREEMPT Preemption is disabled at task activation. Initial interrupt state of task! TA_DISPREEMPT Preemption is disabled at task activation. Initial interrupt state of task! TA_DISPREEMPT Preemption is enabled at task activation. Initial interrupt state of task! TA_DISINT All interrupts are enabled at task activation. Initial interrupt state of task is shown. The extended information of the task is shown. Te cur	Stack Area	the following format.					
Wakeup Count The wakeup request count of the task is shown. Activate Count The activation request count of the task is shown. Attribute The activation request count of the task is shown. Attribute The activation state of task (the task's coding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state Initial interrupt state [Coding language Initial activation state Task type Initial preemption state Initial interrupt state [Coding language Initial activation state Task type Initial preemption state [Initial interrupt state [Coding language Initial activation state of task] TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state [Task type] TA_RSTR TA_DISPREEMPT Preemption is disabled at task activation. [Initial interrupt state of task] TA_DISPREEMPT TA_DISPREEMPT Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISPREEMPT TA_ENAINT All interrupts are disabled at task activation. [Initial interrupt state of task]	Initial Priority	The initial priority of the task	is shown.				
Activate Count The activation request count of the task is shown. Attribute The attributes of the task (the task's coding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state Initial interrupt state Icoding language Initial activation state Task type Initial preemption state Initial interrupt state ICoding language Initial activation state Task type Initial preemption state Initial interrupt state Icoding language Initial activation state Task type Initial preemption state Initial interrupt state ICoding language Initial activation state Task type Initial preemption state Initial interrupt state ICoding language Initial activation state Task type Initial preemption state Initial interrupt state Initial activation state of task TA_ASM Assembly language Initial activation state of task Initial interrupt state Initial interrupt state Inack type] TA_RSTR Restricted task Nothing displayed Initial interrupt state of task] Initial interrupt state of task] Initial interrupt state of task] Inactive state of task Initial interrupt state of task Initial interrupt state of task activation. Initial interrupt state of task is shown. Initerrupt state of task	Suspend Count	The suspension count of the task is shown.					
Attribute The attributes of the task (the task's coding language, initial activation state, task type, initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state Initial interrupt state Coding language of task] TA_HLNG C language TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. Initial interrupt state of task] TA_DISPREEMPT TA_DISPREEMPT Preemption is enabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at t	Wakeup Count	The wakeup request count of the task is shown.					
type, initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state Initial interrupt state [Coding language of task] TA_HLNG C language TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state [Ta_RSTR Nothing displayed Normal task [Initial preemption state of task] TA_RSTR Restricted task Nothing displayed Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is disabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task excepti	Activate Count	The activation request count of the task is shown.					
TA_HLNG C language TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state Nothing displayed Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task Nothing displayed Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT TA_DISPREEMPT Preemption is disabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. [Initial interrupt state of task] TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Tex Entry The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling	Attribute	type, initial preemption state and initial interrupt state) are shown in the following format. Coding language Initial activation state Task type Initial preemption state					
TA_ASM Assembly language [Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task Nothing displayed Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exceptio		[Coding language of task]					
[Initial activation state of task] TA_ACT READY state Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task Nothing displayed Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISPREEMPT Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Tex Entry The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions Disable task exceptions		TA_HLNG	C language				
TA_ACT READY state Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. Initial interrupt state of task] TA_DISPREEMPT Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Extend Information The extended information of the task is shown. Tex status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions Disable task exceptions		TA_ASM Assembly language					
Nothing displayed DORMANT state [Task type] TA_RSTR Restricted task TA_RSTR Restricted task Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is enabled at task activation. Nothing displayed Preemption is enabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Tex Entry The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TEX_DIS Disable task exceptions		[Initial activation state of task]				
[Task type] TA_RSTR Restricted task Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TEX_DIS Disable task exceptions		TA_ACT	READY state				
TA_RSTR Restricted task Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TEX_DIS Disable task exceptions		Nothing displayed	DORMANT state				
Nothing displayed Normal task [Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. Nothing displayed Preemption is enabled at task activation. Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Extend Information The extended information of the task is shown. The start address of the task exception handling routine is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		[Task type]					
[Initial preemption state of task] TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Tex Entry The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		TA_RSTR	Restricted task				
TA_DISPREEMPT Preemption is disabled at task activation. Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions Disable task exceptions		Nothing displayed	Normal task				
Nothing displayed Preemption is enabled at task activation. [Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TITEX_DIS Disable task exceptions		[Initial preemption state of tas	sk]				
Initial interrupt state of task] TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		TA_DISPREEMPT	Preemption is disabled at task activation.				
TA_DISINT All interrupts are disabled at task activation. TA_ENAINT All interrupts are enabled at task activation. Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TITEX_DIS Disable task exceptions		Nothing displayed	Preemption is enabled at task activation.				
TA_ENAINT All interrupts are enabled at task activation. Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		[Initial interrupt state of task]					
Extend Information The extended information of the task is shown. Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		TA_DISINT	All interrupts are disabled at task activation.				
Tex Entry The start address of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions		TA_ENAINT	All interrupts are enabled at task activation.				
Note that if the task exception handling routine is undefined, the name will appear as "". Tex Status The current status of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions	Extend Information	The extended information of	the task is shown.				
Note that if the task exception handling routine is undefined, the name will appear as "". TTEX_DIS Disable task exceptions	Tex Entry	Note that if the task exception					
	Tex Status	Note that if the task exception					
TTEX_ENA Enable task exceptions		TTEX_DIS	Disable task exceptions				
		TTEX_ENA	Enable task exceptions				

Tex Request Pattern	1 8 1	The pending exception code of the task exception handling routine is shown. Note that if the task exception handling routine is undefined, the name will appear as "".			
Tex Executing Pattern		he task exception handling routine is shown. n handling routine is undefined, the name will appear			
Tex Attribute	0 0 0	ask exception handling routine is shown. n handling routine is undefined, the name will appear			
	TA_HLNG	C language			
	TA_ASM Assembly language				

(b) Second layer

See the [Mutex] tab for details about locking mutex information.

Realtime	OS Resource	Inform	ation				8
/ s \ м	I / T_ (S	(E	Y D Y M Y M	. 🛛 Fi 🔪 V 🔪 C	: \ Int \ Ini	(E (R	\ Ti \ ₹
Task	Name	ID	Status	Wait Factor		Wait Data	Time Left
	ID_TASK1 0x01 Dormant			-		-	0
	• 🖬 ID_TASK2 0x02		Waiting-Suspende	d FLG(0x01) AN	DW TMO FIFO	0x111	99950 0
	D_TASK3	0x03	Suspended	1.25	520°		
• •	ID_TASK4 0x04		Running	10000			0
м	utex Name	ID	Queue Status	Locking Task ID	ocking Task ID Attribute		
	D_MTX	1 0x0)1 Waiting Tasks	0x04	TA_TFIF0		
Task	Name	ID	Status	Wait Factor		Wait Data	Time Left
	D_TASK5	0x05	Waiting	MTX(0x01) TM	IO FIFO	-	9950
<	illi)					>

Figure A-5. [Task] Tab (Locking Mutex Information)

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display	Displays cascade menus for selecting the header items to display.			
Selected item name	ID, Status, Wait I Entry, Current P(Wakeup Count, /	are displayed for selection. Factor, Wait Data, Time Left, Interrupt, Current Priority, Task C, Current SP, Stack Area, Initial Priority, Suspend Count, Activate Count, Attribute, Extend Information, Tex Entry, Tex uest Pattern, Tex Executing Pattern, Tex Attribute		
	Checked	The item in question will be displayed.		
	Not checked	The item in question will not be displayed.		
Notation	Displays cascade menus for selecting the display notation.			
Selected item name	ID, Wait Factor, V Current SP, Stac	are displayed for selection. Wait Data, Time Left, Current Priority, Task Entry, Current PC, k Area, Initial Priority, Suspend Count, Wakeup Count, Activate formation, Tex Entry, Tex Request Pattern, Tex Executing Pat-		
	DEC	Displays value in signed decimal number.		
	HEX	Displays value in hexadecimal number.		

(2) Footer row

Jump to Source (Task Entry)	Opens the Editor panel, and displays the source code of the task.
Jump to Disassemble (Task Entry)	Opens the Disassemble panel, and displays the results of disassembling the task.
Jump to Source (Current PC)	Opens the Editor panel, and displays the contents of the Current PC.
Jump to Disassemble (Current PC)	Opens the Disassemble panel, and displays the contents of the Current PC.
Jump to Memory (Current SP)	Opens the Memory panel, and displays the contents of the Current SP.
Jump to Source (Tex Entry)	Opens the Editor panel, and displays the source code of the task exception handling routine.
Jump to Disassemble (Tex Entry)	Opens the Disassemble panel, and displays the results of disassembling the task exception handling routine.

Service Call	Note that if a servic or the necessary c	of service calls that can be issued. ce call is not embedded in (linked to) the downloaded program, onditions for executing the service-call process are not met, Il will be grayed out.
	rel_wai	Release task from waiting.
	ter_tsk	Terminate task.
	chg_pri	Change task priority.
	sus_tsk	Suspend task.
	rsm_tsk	Resume suspended task.
	frsm_tsk	Forcibly resume suspended task.
	wup_tsk	Wakeup task.
	can_wup	Cancel task wakeup requests.
	act_tsk	Activate task (queues an activation request).
	can_act	Cancel task activation requests.
	ras_tex	Raise task exception handling.

[Semaphore] tab

This tab displays the semaphore information (e.g. Semaphore Name and ID) of the RX850V4.

Figure A-6. [Semaphore] Tab

	Re	altime OS Resource Info	rmation					×
	/ s	\ M \ T \ S_ \ E	(D	"М.УУ	<pre>Fi V V</pre>	C (Int (Ii	ni (E (B	i \ Ti \ ₹
(1)		Semaphore Name	ID	Queue Status	Current Count	Max Count	Initial Count	Attribute
(.)		ID_SEM1	0x01	Empty	1	1	1	TA_TFIF0
_	<							>

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the semaphore information (e.g. Semaphore Name and ID) of the RX850V4. This layer consists of the following items.

Semaphore Name	An icon indicating the current status of the semaphore and the semaphore name are shown in the following format. Icon Semaphore name Note that if the semaphore name is undefined, the name will appear as "ID".				
	1	There are no waiting tasks.			
ID	The ID of the semaphore is shown.				
Queue Status	The current status of the semaphore is shown.				
	Waiting Tasks There are waiting tasks.				
	Empty	There are no waiting tasks.			
Current Count	The current resource count of the semaphore is shown.				
Max Count	The maximum resource cour	nt of the semaphore is shown.			
Initial Count	The initial resource count of	the semaphore is shown.			
Attribute	The task queuing method is	shown.			
	TA_TFIFO	FIFO order			
	TA_TPRI	Task priority order			

(b) Second layer

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the semaphore's wait queue.

See the [Task] tab for details about waiting task information.

Figure A-7.	[Semaphore] Tab (Waiting Task Information)
-------------	--

1	Semaphore Name		ID	Queue Status	Current Cou	int Max C	ount In	tial Count	Attribu	te
8	• HID_SEM1		0x01	Waiting Tas	0	1	1		TA_TFIFO	
	Task Name	ID	Statu	s Wait Factor	Wait Data	Time Left	Interrup	Current	Priority	Tas
	• 10_TA	0x	Waiti	SEM(0x01		99993	Enable	1		0x0

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display		Displays cascade menus for selecting the header items to display.			
	Selected item name	C	s are displayed for selection. s, Current Count, Max Count, Initial Count, Attribute		
		Checked	The item in question will be displayed.		
		Not checked	The item in question will not be displayed.		
Ν	lotation	Displays cascade menus for selecting the display notation.			
	Selected item name	0	s are displayed for selection. ht, Max Count, Initial Count		
		DEC	Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.		

(2) Footer row

Service Call	Note that if a servic or the necessary co	of service calls that can be issued. e call is not embedded in (linked to) the downloaded program, onditions for executing the service-call process are not met, Il will be grayed out.		
	sig_sem Release semaphore resource.			
	pol_sem	Acquire semaphore resource (polling).		

[Eventflag] tab

This tab displays the eventflag information (e.g. Eventflag Name and ID) of the RX850V4.

Figure A-8. [Eventflag] Tab



The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the eventflag information (e.g. Eventflag Name and ID) of the RX850V4. This layer consists of the following items.

Eventflag Name	An icon indicating the current status of the eventflag and the eventflag name are shown in the following format. Icon Eventflag name Note that if the eventflag name is undefined, the name will appear as "ID".					
	4	There are waiting tasks.				
	♦	There are no waiting tasks.				
ID	The ID of the eventflag is shown.					
Queue Status	The current status of the eventflag is shown.					
	Waiting Tasks	There are waiting tasks.				
	Empty	There are no waiting tasks.				
Current Flag Pattern	The current bit pattern of the eventflag is shown.					
Initial Flag Pattern	The initial bit pattern of the eventflag is shown.					

Attribute		The attributes of the eventflag (task queuing method, and maximum number of tasks that can be queued, and bit pattern clearing flag) are shown.				
	[Task queuing method]	[Task queuing method]				
	TA_TFIFO	FIFO order				
	TA_TPRI	Task Priority order				
	[Maximum number of tas	[Maximum number of tasks that can be queued]				
	TA_WSGL	Only one task				
	TA_WMUL	Multiple tasks				
	[Bit pattern clearing flag]	[Bit pattern clearing flag]				
	TA_CLR	Bit pattern cleared if the request conditions are met.				
	Nothing displayed	Bit pattern not cleared if the request conditions are met.				

(b) Second layer

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the eventflag's wait queue.

See the $\ensuremath{\left[\text{Task} \right]}$ tab for details about waiting task information.

Figure A-9.	[Eventflag] Tab (Waiting Task Information)
-------------	--

S	(M (T (S)	E_ (D \ M	\ м.	🛛 Fi 🖉 V 🗎 C 🔪	Int	(Ini (E	(B (Ti \ ₹
E	ventflag Name	ID	Queue Sta	atus	Current Flag Pattern	Ini	tial Flag Patte	m	Attribu	te
Θ.	• 🌗 ID_FLG1	ID_FLG1 0x01 Wait		asks	0x00000000	0x	00000000	TA_TFIFO TA_		
	Task Name ID Status Wait Factor		it Factor	Wait Data		Time Left		Interrup		
► Pm ID_TASK1		0x01	0x01 Waiting		FLG(0x01) ANDW TMO FIFO		0x111 99		972	Enable
The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

C	visplay	Displays cascade menus for selecting the header items to display.				
	Selected item name	0	s are displayed for selection. s, Current Flag Pattern, Initial Flag Pattern, Attribute			
		Checked	The item in question will be displayed.			
		Not checked	The item in question will not be displayed.			
Ν	lotation	Displays cascade r	nenus for selecting the display notation.			
	Selected item name	0	are displayed for selection. Pattern, Initial Flag Pattern			
		DEC	Displays value in signed decimal number.			
		HEX	Displays value in hexadecimal number.			

Service Call	Note that if a servic or the necessary c	of service calls that can be issued. e call is not embedded in (linked to) the downloaded program, onditions for executing the service-call process are not met, Il will be grayed out.
	set_flg	Set eventflag.
	clr_flg	Clear eventflag.

[Data Queue] tab

This tab displays the data queue information (e.g. Data Queue Name and ID) of the RX850V4.

Figure A-10. [Data Queue] Tab

	Realtime OS Resource Info / S M T S E		V V V	(Fi (V (C (Int (I	ni \ E \ R \ Ti \ ₹
(1)	Data Queue Name	ID	Queue Status	Total Buffers	Free Buffers	Attribute
	▶• PID_DTQ1	0x01	Empty	4	4	TA_TFIF0
	<		1111			>

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the data queue information (e.g. Data Queue Name and ID) of the RX850V4. This layer consists of the following items.

Data Queue Name	are shown in the following fo Icon Data queue name	t status of the data queue and the data queue name rmat. ame is undefined, the name will appear as "ID".
	1	There are queued tasks (sending waiting tasks).
	1	There are queued tasks (receiving waiting tasks).
	A	There are queud data (receiving waiting data).
	\$	There are no queued tasks/data (waiting tasks/ receiving waiting data).
ID	The ID of the data queue is s	shown.
Queue Status	The current status of the data	a queue is shown.
	Waiting Tasks (Send)	There are queued tasks (sending waiting tasks).
	Waiting Tasks (Receive)	There are queued tasks (receiving waiting tasks).
	Waiting Data	There are queud data (receiving waiting data).
	Empty	There are no queued tasks/data (waiting tasks/ receiving waiting data).
Total Buffers	Displays the maximum numb	er of data buffers that can be queued.

Free Buffers		of free buffers in the data queue. uffers is the total number of buffers minus the number of buff- data.
Attribute	If the queuing metho	method of the sending waiting tasks. d if the receiving waiting tasks is "data reception request ing method of the receiving waiting data will be "data send
	TA_TFIFO	FIFO order
	TA_TPRI	Task priority order

<1> Sending waiting task/receive waiting task information

The sending/receiving waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the data queue's wait queue.

See the [Task] tab for details about sending/receiving waiting task information.

Figure A-11. [Data Queue] Tab (Sending Waiting Task Information)

Rea	ltime OS Resource I	Inform	ation			and the second			
/ S.	\ M \ T \ S	ΎΕ	Y d _	\́ м \́ м ∖́	(Fi (V	(C (In	t (Ini)	(E (R (Ti.	\ 🖛
	Data Queue Name		ID S	Queue Status	Total Buffer	s Free Bu	iffers Att	ribute	
	• 🦪 ID_DTQ1	0	0x01 1	Waiting Tas	4	0	TA	_TFIFO	
	Data								
	🗐 0x10								
	📕 0x20								
	📕 0x30								
	📕 🗐 0x40								
	Task Name	ID	Status	Wait Factor	Wait Data	Time Left	Interrupt	Current Priority	Task
	▶ 🛅 ID_TA	0x	Waiti	SDTQ(0x	0x50	9992	Enable	1	0x00
<				III					>

Figure A-12. [Data Queue] Tab (Receiving Waiting Task Information)

S	ΥМ ΥТ ΥS Υ	E Y D	<u>— (М</u>	Y M Y Fi V	(V \ C \	Int (Ini.	<u> </u>	(F	} ∖ Ti \	1.
D	ata Queue Name	ID	Queue	Status	Total Buffers	Free Bu	ffers	Attrib	ute	
	• 🚰 ID_DTQ1	0x01	Waiting	Tasks(Rec	4	4		TA_T	FIFO	
	Task Name	ID	Status	Wait Factor		Wait Data	Time	Left	Interrupt	С
l.	▶ Pm ID_TASK1	0x01	Waiting	RDTQ(0x01)	TMO FIFO		9541		Enable	1

<2> Receiving waiting data information

The receiving waiting data information (e.g. Data) only appears if there are data queued in the data queue.

Figure A-13. [Data Queue] Tab (Receiving Waiting Data Information)

Re	altime	OS Resource Inf	ormation		tal was wee		×
/ S	(I	И (Т (S (E) D	_ (м (м	(Fi \ V \	C \ Int \ I	ni ⟨ E R Ti ₹
	Data	Queue Name	ID	Queue Status	Total Buffers	Free Buffers	Attribute
	•	🗿 ID_DTQ1	0x01	Waiting Data	4	1	TA_TFIFO
	1)ata					
		▶ 🚆 0x10					
		週 0x20					
		🗒 0x30					
<	lenter I			in .			>

This area consists of the following items.

Data th	the contents of the data is shown.
---------	------------------------------------

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

D	isplay	Displays cascade menus for selecting the header items to display.			
	Selected item name	9	are displayed for selection. s, Total Buffers, Free Buffers, Attribute		
		Checked	The item in question will be displayed.		
		Not checked	The item in question will not be displayed.		
N	lotation	Displays cascade r	nenus for selecting the display notation.		
	Selected item name	The following items ID, Total Buffers,	s are displayed for selection. Free Buffers		
		DEC	Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.		

Service Call	Note that if a servic or the necessary co	of service calls that can be issued. ce call is not embedded in (linked to) the downloaded program, onditions for executing the service-call process are not met, Il will be grayed out.
	psnd_dtq	Send to data queue (polling).
	fsnd_dtq	Forced send to data queue.
	prcv_dtq	Receive from data queue (polling).

[Mailbox] tab

This tab displays the mailbox information (e.g. Mailbox Name and ID) of the RX850V4.

Figure A-14. [Mailbox] Tab

1	S \ M \ T \ S	(E)	(d) m _ (i	M 🛛 Fi 🔪 V 🗎 C 🎽	⟨Int ⟨Ini ⟨E ⟨B ⟨Ti ∖₹
	Mailbox Name	ID	Queue Status	Message Max Priority	Attribute
	▶ • 📪 ID_MBX1	0x01	Empty	0x10	TA_TFIFO TA_MPRI

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the mailbox information (e.g. Mailbox Name and ID) of the RX850V4. This layer consists of the following items.

Mailbox Name	An icon indicating the current status of the mailbox and the mailbox name are shown in the following format. Icon Mailbox name Note that if the mailbox name is undefined, the name will appear as "ID".				
	There are waiting tasks.				
	There are waiting messages.				
	There are no waiting tasks/messages.				
ID	The ID of the mailbox is shown.				
Queue Status	The current status of the mai	lbox is shown.			
	Waiting Tasks	There are waiting tasks.			
	Waiting Messages	There are waiting messages.			
	Empty	There area no waiting tasks/messages.			
Message Max Priority	The maximum priority of the	message is shown.			

Attribute	The attributes of the mailbox method) are shown.	(task queuing method and message queuing		
	[Task queuing method]			
	TA_TFIFO	FIFO order		
	TA_TPRI	Task priority order		
	[Message queuing method]			
	TA_MFIFO	FIFO order		
	TA_MPRI	Message priority order		

<1> Waiting task information

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the mailbox's wait queue.

See the [Task] tab for details about waiting task information.

Figure A-15. [Mailbox] Tab (Waiting Task Information)

Rea	altime OS Resource	Info	ormabi	on						
/ S	\ M \ T \ S	X E	Y	D / M_	1)	vi 🛛 Fi 🔪 V 🗎 C.	. \Int\Ir	πi \ Ε \	B \ Ti	\ ₹
	Mailbox Name	ID		Queue Statu	IS	Message Max Priority	Attribute			
	• 🚰 ID_MBX1	0x	י 01	Waiting Tas	ks	0x10	TA_TFIF	O TA_MPR	6	
	Task Name		ID	Status	W	/ait Factor	Wait Data	Time Left	Interrupt	Curr
	D_TAS	K1	0x01	1 Waiting	М	BX(0x01) TMO FIFO	*	9980	Enable	1
<										>

<2> Waiting message information

The waiting message information (e.g. Message Address and Message Priority) only appears if there are messages queued in the mailbox's wait queue.

Figure A-16. [[Mailbox] Tab	(Waiting Message	Information)
----------------	---------------	------------------	--------------

S	́ М \ Т \ S	(E)	(D) M_ (M)	(Fi	\ Ini \ E \ R \ Ti \
1	Mailbox Name	ID	Queue Status	Message Max Priority	Attribute
9 [] E	• 🗿 ID_MBX1	0x01	Waiting Messages	0x10	TA_TFIFO TA_MPRI
	Message Address		Message Priority		
	🕨 🕨 🖂 OxFFFFD)3D8	0x1		

This area consists of the following items.

Message Address	The start address of the message is shown.
Message Priority	The priority of the message is shown.

The following context menu appears when the header row is right clicked with the mouse.

Display	Displays cascade r	nenus for selecting the header items to display.	
Selected item name	5	s are displayed for selection. s, Message Max Priority, Attribute	
	Checked	The item in question will be displayed.	
	Not checked	The item in question will not be displayed.	
Notation	Displays cascade menus for selecting the display notation.		
Selected item name	The following items ID, Message Ma	s are displayed for selection. x Priority	
	DEC	Displays value in signed decimal number.	
	HEX	Displays value in hexadecimal number.	

[Mutex] tab

This tab displays the mutex information (e.g. Mutex Name and ID) of the RX850V4.

Figure A-17. [Mutex] Tab

	/ S \ M \ T \ S	\ E	. (D (M)	M_ \ Fi \ V	│ C │ Int │ Ini │ E │ R │ Ti │ ₹
(1)	Mutex Name	ID	Queue Status	Locking Task ID	Attribute
(')	▶ • 🚮 ID_MTX1	0x01	Empty	+	TA_TFIFO
	<				>

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the mutex information (e.g. Mutex Name and ID) of the RX850V4. This layer consists of the following items.

Mutex Name	An icon indicating the current status of the mutex and the mutex name are shown in the following format. Icon Mutex name Note that if the mutex name is undefined, the name will appear as "ID".			
	6	There are waiting tasks.		
	Image: There are no waiting tasks.			
ID	The ID of the mutex is shown.			
Queue Status	The current status of the mutex is shown.			
	Waiting Tasks	There are waiting tasks.		
	Empty	There are no waiting tasks.		
Locking Task ID	The ID of the locking task is s	shown.		
Attribute	The task queuing method is a	shown.		
	TA_TFIFO	FIFO order		
	TA_TPRI	Task priority order		

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the mutex's wait queue.

See the [Task] tab for details about waiting task information.

Figure A-18.	[Mutex] Tab	(Waiting	Task	Information)
--------------	-------------	----------	------	--------------

S.,	. 🗸 M 🛛 T 🗸 S	🗸 Е	(c) (M	∕ M _ (Fi (V	\с.	\Int\Ir	πi (Ε (R \ Ti	. \ =
	Mutex Name	ID	Que	eue Status	Locking Task ID	Attr	ibute			
9	🛛 🛛 🚮 ID_MTX1 🛛 0x01 🛛 Waiting		aiting Tasks 0x01 TA_TFIFO							
	Task Name	_	ID	Status	Wait Factor		Wait Data	Time Left	Interrupt	Cur
		SK2	0x02	Waiting	MTX(0x01) TMO FI	FO		10000	Enable	3

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display	Displays cascade menus for selecting the header items to display.				
Selected item name	9	s are displayed for selection. s, Locking Task ID, Attribute			
	Checked	The item in question will be displayed.			
	Not checked	The item in question will not be displayed.			
Notation	Displays cascade menus for selecting the display notation.				
Selected item name	The following items are displayed for selection. ID, Locking Task ID				
	DEC	Displays value in signed decimal number.			
	HEX	Displays value in hexadecimal number.			

Service Call	Displays the types of service calls that can be issued.					
	Note that if a service call is not embedded in (linked to) the downloader or the necessary conditions for executing the service-call process are then the service call will be graved out.					
	unl_mtx	Unlock mutex.				

[Fixed-Sized Memory Pool] tab

This tab displays the fixed-sized memory pool information (e.g. Fixed-Sized Memory Pool Name and ID) of the RX850V4.

Figure A-19. [Fixed-Sized Memory Pool] Tab

	Realtime OS Resource Information						
_	/ S \ M \ T \ S \ E \ D	. (м.,	(M) Fi_ (V \ C \ Ini	t (Ini (E	: (R (Ti.	. \ 🔻
(1)	Fixed-Sized Memory Pool Name	ID	Queue Status	Top Address	Block Size	Total Blocks	Free
(.)	▶ • 🚰 ID_MPF1	0x01	Empty	0xFFFFD274	0x8	4	4
	<	Ш					>

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the fixed-sized memory pool information (e.g. Fixed-Sized Memory Pool Name and ID) of the RX850V4.

This layer consists of the following items.

Fixed-Sized Memory Pool Name	An icon indicating the current status of the fixed-sized memory pool and a sized memory pool name are shown in the following format. Icon Fixed-sized memory pool name Note that if the fixed-sized memory pool name is undefined, the name will as "ID".					
	<u>6</u>	There are waiting tasks.				
	<u>6</u>	There are no waiting tasks.				
ID	The ID of the fixed-sized memory pool is shown.					
Queue Status	The current status of the fixed-sized memory pool is shown.					
	Waiting Tasks	There are waiting tasks.				
	Empty	There are no waiting tasks.				
Top Address	The start address of the fixed	l-sized memory pool is shown.				
Block Size	The size per block (in bytes)	of the fixed-sized memory pool is shown.				
Total Blocks	The block count of the fixed-	sized memory pool is shown.				
Free Blocks	The number of free memory	blocks is shown.				

Attribute	The task queuing method is a	shown.		
	TA_TFIFO	FIFO order		
	TA_TPRI	Task priority order		

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the fixedsized memory pool's wait queue.

See the [Task] tab for details about waiting task information.

Figure A-20. [Fixed-Sized Memory Pool] Tab (Waiting Task Information)

/ s.	\	M \ T \ S \	E \ D	(м	(M) Fi_ (V \ C.	\ In	t (Ir	ni (E	Y	R \ Ti.	⊠ ₹ /
	Fix	ed-Sized Memory Po	ol Name	ID	Queue Status	Top Add	dress	Block	< Size	Tot	al Blocks	Free
	-	ID_MPF1	0x01	Waiting Tasks	Vaiting Tasks 0xFFFFD		274 0x8		4		0	
	Task Name ID		Status	Wait Factor	Wai		it Data Time		Left Interrupt	Cum		
		▶ Pm ID_TASK1	0x01	Waiting	MPF(0x01) TM	O FIFO			1000)	Enable	1

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

C	Display	Displays cascade menus for selecting the header items to display.			
	Selected item name	The following items are displayed for selection. ID, Queue Status, Top Address, Block Size, Total Blocks, Free Blocks, Attribute			
		Checked	The item in question will be displayed.		
		Not checked	The item in question will not be displayed.		
٨	lotation	Displays cascade menus for selecting the display notation.			
	Selected item name	The following items are displayed for selection. ID, Top Address, Block Size, Total Blocks, Free Blocks			
		DEC	Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.		

Opens the Memory panel, and displays the contents of the fixed-sized memory pool.

[Variable-Sized Memory Pool] tab

This tab displays the variable-sized memory pool information (e.g. Variable-Sized Memory Pool Name and ID) of the RX850V4.

Figure A-21. [Variable-Sized Memory Pool] Tab

	Realtime OS Resource Information						
_	/ S \ M \ T \ S \ E \ D \	М (M (Fi) V.	. (C (Int)	(Ini (E	(R (Ti	\ 🗲
(1)	Variable-Sized Memory Pool Name	ID	Queue Status	Top Address	Total Size	Free Size	Avaik
(1)	▶ • 🚰 ID_MPL1	0x01	Empty	0xFFFFD294	0x8	0x8	0x8
_)		>

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the variable-sized memory pool information (e.g. Variable-Sized Memory Pool Name and ID) of the RX850V4.

This layer consists of the following items.

Variable-Sized Memory Pool Name	An icon indicating the current status of the variable-sized memory pool variable-sized memory pool name are shown in the following format. Icon Variable-sized memory pool name Note that if the variable-sized memory pool name is undefined, the nam appear as "ID".					
	<u>ê</u>	There are waiting tasks.				
	6	There are no waiting tasks.				
ID	The ID of the variable-sized memory pool is shown.					
Queue Status	The current status of the variable-sized memory pool is shown.					
	Waiting Tasks	There are waiting tasks.				
	Empty	There are no waiting tasks.				
Top Address	The start address of the varia	able-sized memory pool is shown.				
Total Size	The size (in bytes) of the var	iable-sized memory pool is shown.				
Free Size	The total size (in bytes) of the	e free memory blocks is shown.				
Available Max Block Size	The maximum memory block memory pool is shown.	size available (in bytes) of the variable-sized				

Attribute	The task queuing method is a	shown.
	TA_TFIFO	FIFO order
	TA_TPRI	Task priority order

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the variablesized memory pool's wait queue.

See the [Task] tab for details about waiting task information.

Figure A-22. [Variable-Sized Memory Pool] Tab (Waiting Task Information)

/ S	. (M (T (S (E \ [) Y	M)	(M Fi) V .	- (c	(Int)	(Ini X	E \	(R (Ti.	\ =
3	Variable-Sized Memory	Pool Na	ame	ID	Queue Status	Тори	Address	Total Siz	ze	Free Size	Avail
	• 6 ID_MPL1			0x01	1 Waiting Tasks	0xFF	FFD294	0x8		0x8	0x8
	Task Name	ID	Stat	tus	Wait Factor		Wait Da	ta Time	Left	Interrupt	Curr
	► Pa ID_TASK1	0x01	Wa	iting	MPL(0x01) TMO F	FIFO	0x18	9980)	Enable	1

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

C	visplay	Displays cascade menus for selecting the header items to display.			
	Selected item name	0	s are displayed for selection. s, Top Address, Total Size, Free Size, Available Max Block		
		Checked	The item in question will be displayed.		
		Not checked	The item in question will not be displayed.		
٨	lotation	Displays cascade menus for selecting the display notation.			
	Selected item name	The following items are displayed for selection. ID, Top Address, Total Size, Free Size, Available Max Block Size			
		DEC	Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.		

[Cyclic Handler] tab

This tab displays the cyclic handler information (e.g. Cyclic Handler Name and ID) of the RX850V4.

Figure A-23. [Cyclic Handler] Tab

/	S Y M Y T Y S Y	E \ I	D (M (M \ Fi.	V	\mathcal{C}_{-} (Int	\ Ini \ E	⟨ B ⟨ Ti ∖ ₹
	Cyclic Handler Name	ID	Status	Interval	Phase	Time Left	Entry	Extend Informatio
	▶ • 🛃 ID_CYC1	0x01	TCS_STP	1000	5	0	0x0000A6E0	0x0

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the cyclic handler information (e.g. Cyclic Handler Name and ID) of the RX850V4. This area consists of the following items.

Cyclic Handler Name	An icon indicating the current status of the cyclic handler and the cyclic handler name are shown in the following format. Icon Cyclic handler name Note that if the cyclic handler name is undefined, the name will appear as "ID". Image: Constraint of the cyclic handler name is undefined to the name will appear as "ID". Image: Constraint of the cyclic handler name is undefined to the name will appear as "ID". Image: Constraint of the cyclic handler name is undefined to the name will appear as "ID".		
		Operational state (STA state)	
ID	The ID of the cyclic handler is shown.		
Status	The current status of the cyclic handler is shown.		
	TCS_STP	Non-operational state (STP state)	
TCS_STA Operational state (Operational state (STA state)	
Interval	The activation cycle (in milliseconds) of the cyclic handler is shown.		
Phase	The initial activation phase (i	n milliseconds) of the cyclic handler is shown.	
Time Left	The time left before the next	activation (in milliseconds) of the cyclic handler is shown.	
Entry	The start address of the cycli	c handler is shown.	
Extend Information	The extended information of	the cyclic handler is shown.	

Attribute	tion state and existen	cyclic handler (the cyclic handler's coding language, initial activa- ce of saved activation phases) are shown in the following format. nitial activation state Existence of saved activation phases cyclic handler]	
	TA_HLNG	C language	
	TA_ASM	Assembly language	
	[Initial activation state	e of cyclic handler]	
	TA_STA	Operational state (STA state)	
	Nothing displayed	Non-operational state (STP state)	
	[Existence of saved a	ctivation phases]	
	TA_PHS	There are saved activation phases.	
	Nothing displayed	There are no saved activation phases.	

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display		Displays cascade menus for selecting the header items to display.		
Selected item name		The following items are displayed for selection. ID, Status, Interval, Phase, Time Left, Entry, Extend Information, Attribute		
		Checked	The item in question will be displayed.	
		Not checked	The item in question will not be displayed.	
1	Notation	Displays cascade menus for selecting the display notation.		
	Selected item name	5	are displayed for selection. se, Time Left, Entry, Extend Information	
	DEC		Displays value in signed decimal number.	
		HEX	Displays value in hexadecimal number.	

Jump to Source (Entry)	Opens the Editor panel, and displays the source code of the cyclic handler.		
Jump to Disassemble (Entry)	Opens the Disassemble panel, and displays the results of disassembling the cyclic handler.		
Service Call	Displays the types of service calls that can be issued. Note that if a service call is not embedded in (linked to) the downloaded pro or the necessary conditions for executing the service-call process are not r then the service call will be grayed out. sta_cyc Start cyclic handler operation.		
	stp_cyc	Stop cyclic handler operation.	

[Interrupt Handler] tab

This tab displays the interrupt handler/CPU exception handler information (e.g. Exception Code and Entry) of the RX850V4.

Figure A-24. [Interrupt Handler] Tab

Realtime OS Resource / S M T 1	1/ 1/ 1/	M \ M \ Fi \ V \ C \ In \ Ini \ E \ R \ Ti \ ₹
Exception Code	Entry	Attribute
• • 🚽 0x40	0x00009F6C	TA_HLNG TA_CPUEXC
• 🥣 0x90	0x00009F2C	TA_ASM

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the interrupt handler/CPU exception handler information (e.g. Exception Code and Entry) of the RX850V4.

This area consists of the following items.

Exception Code	An icon indicating the type of the handler and the exception code are shown in the fol- lowing format.			
	Icon Exception code			
	d	CPU exception handler		
	i	Interrupt handler		
Entry	The start address of the interrupt handler/CPU exception handler is shown.			
Attribute	The attributes of the interrupt handler/CPU exception handler (coding language and type of the handler) are shown.			
	[Coding language of interrupt handler/CPU exception handler]			
	TA_HLNG	C language		
	TA_ASM	Assembly language		
	[Type of handler]			
	TA_CPUEXC	CPU exception handler		
	Nothing displayed	Interrupt handler		

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

С	isplay	Displays cascade menus for selecting the header items to display.		
	Selected item name	The following items are displayed for selection. Entry, Attribute		
		Checked	The item in question will be displayed.	
		Not checked	The item in question will not be displayed.	
Ν	lotation	Displays cascade menus for selecting the display notation.		
	Selected item name	The following items are displayed for selection. Exception Code, Entry		
		DEC	Displays value in signed decimal number.	
		HEX	Displays value in hexadecimal number.	

Jump to Source (Entry)	Opens the Editor panel, and displays the source code of the interrupt handler/ CPU exception handler.
Jump to Disassemble (Entry)	Opens the Disassemble panel, and displays the results of disassembling the inter- rupt handler/CPU exception handler.

[Initialize Routine] tab

This tab displays the initialize routine information (e.g. Entry and Extend Information) of the RX850V4.

Figure A-25. [Initialize Routine] Tab

	Realtime OS Resource Information			
	/ S (M (T (S)	E \ D \ M \ I	M \ Fi \ V \ C \ Int \ Ini_ \ E \ R \ Ti \	₹
(1)	Entry	Extend Information	Attribute	
(.,	• 3 0x0000A6E8	0x0	TA_HLNG	
				_

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the initialize routine information (e.g. Entry and Extend Information) of the RX850V4. This area consists of the following items.

Entry	The start address of the initialize routine is shown.		
Extend Information	The extended information of the initialize routine is shown.		
Attribute	The coding language of the initialize routine is shown.		
	TA_HLNG	C language	
	TA_ASM	Assembly language	

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

C	lisplay	Displays cascade menus for selecting the header items to display.			
	Selected item name	The following items are displayed for selection. Extend Information, Attribute			
		Checked	The item in question will be displayed.		
		Not checked	The item in question will not be displayed.		
Ν	lotation	Displays cascade menus for selecting the display notation.			
	Selected item name	The following items are displayed for selection. Entry, Extend Information			
		DEC	Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.		

Jump to Source (Entry)	Opens the Editor panel, and displays the source code of the initialize routine.	
Jump to Disassemble (Entry)	Opens the Disassemble panel, and displays the results of disassembling the initialize routine.	

[Extended Service Call] tab

This tab displays the extended sevice call routine information (e.g. Function Code and Entry) of the RX850V4.

Figure A-26. [Extended Service Call] Tab



The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

This area displays the extended sevice call routine information (e.g. Function Code and Entry) of the RX850V4. This area consists of the following items.

Function Code	The function code of the extended service call routine is shown.		
Entry	The start address of the extended service call routine is shown.		
Attribute	The coding language of the extended service call routine is shown.		
	TA_HLNG	C language	
	TA_ASM	Assembly language	

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display		Displays cascade menus for selecting the header items to display.		
	Selected item name	The following items are displayed for selection. Entry, Attribute		
		Checked	The item in question will be displayed.	
		Not checked	The item in question will not be displayed.	
Ν	lotation	Displays cascade menus for selecting the display notation.		
	Selected item name	The following items are displayed for selection. Function Code, Entry		
		DEC	Displays value in signed decimal number.	
		HEX	Displays value in hexadecimal number.	

Jump to Source (Entry)	Opens the Editor panel, and displays the source code of the extended service call routine.
Jump to Disassemble (Entry)	Opens the Disassemble panel, and displays the results of disassembling the extended service call routine.

[Ready Queue] tab

This tab displays the ready queue information (e.g. Priority and Task Num) of the RX850V4.

Figure A-27. [Ready Queue] Tab

and the second	3.7	e <mark>OS Re</mark> M \ T.	<mark>source Information</mark> \ S \ E \ D \ M \ M \ Fi \ V \ C \ Int \ Ini \ E \ R_ \ Ti	
	Prior	ty	Task Num	
Đ	++	1	1	
	•	1 2	0	
Đ	•	6 3	2	
÷	•	6 4	2	
<				

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the ready queue information (e.g. Priority and Task Num) of the RX850V4. This layer consists of the following items.

Priority	An icon indicating the current shown in the following format Icon Task priority	status of the ready queue and the task priority are		
	There are queued tasks.			
	0	There are no queued tasks.		
Task Num	The total number of queued t is shown.	asks (tasks with of READY state or RUNNING state)		

The executing task information (e.g. Task Name and ID) only appears if there are tasks queued in the ready queue.

See the [Task] tab for details about executing task information.

							R_ \ Ti \ ₹	
Priorty	Task Num							
e • 💋 1	1							
Task Na	Task Name		Status	Wait Factor	Wait Data	Time Left	Interrupt	Current Priority
	ID_TASK1	0x01	Running	**		0	Enable	1
Priorty	Task Num							
• 💋 2	0							
⊕ • 3 2								
🕀 • 🚰 4 2								
<	1)		>

Figure A-28. [Ready Queue] Tab (Executing Task Information)

[Context menu]

The context menu displayed in response to a right mouse click differs as follows depending on the area clicked.

(1) Header row

Display		Displays cascade menus for selecting the header items to display.		
	Selected item name	The following items are displayed for selection. Task Num		
		Checked	The item in question will be displayed.	
		Not checked	The item in question will not be displayed.	
Ν	lotation	Displays cascade menus for selecting the display notation.		
	Selected item name	The following items are displayed for selection. Priority, Task Num		
		DEC	Displays value in signed decimal number.	
		HEX	Displays value in hexadecimal number.	

Service Call	Displays the types of service calls that can be issued.		
	Note that if a service call is not embedded in (linked to) the downloaded program,		
	or the necessary conditions for executing the service-call process are not met,		
	then the service call will be grayed out.		
	rot_rdq Rotate task Precedence.		

[Timer Queue] tab

This tab displays the timer queue information (e.g. Object Type and Object Num) of the RX850V4.

Figure A-29. [Timer Queue] Tab

Realtime OS Resource Information / S \ M \ T \ S \ E \ D \ M \ M \ Fi \ V \ C \ Int \ Ini \ E \ R \ Ti \			
Object Type	Object Num		
🕀 🕨 🛃 Task	1		
🕀 🔹 🛃 Cyclic Handler	1		
<			

The following items are explained here.

- [How to open]
- [Description of each area]
- [Context menu]

[How to open]

- From the [Debug] menu, select [Download].
- From the [View] menu, select [Realtime OS] >> [Resource Information].

[Description of each area]

(1) Information display area

(a) First layer

This layer displays the timer queue information (e.g. Object Type and Object Num) of the RX850V4. This layer consists of the following items.

Object Type	An icon indicating the current status of the timer queue and the object shown in the following format. Icon Object type		
	[lcon]		
		There are queued tasks.	
	13	There are no queued tasks.	
		There are queued cyclic handlers.	
		There are no queued cyclic handlers.	
	[Object type]		
	Task	Task	
	Cyclic Handler	Cyclic handler	
Object Num	The total number of queued objects (tasks and cyclic handlers) is shown.		

<1> Waiting task information

The waiting task information (e.g. Task Name and ID) only appears if there are tasks queued in the timer queue.

See the [Task] tab for details about waiting task information.

Figure A-30.	[Timer Queue] Tab (Waiting	Task Information)
--------------	--------------	----------------	-------------------

Re	altime OS Resource Inf	ormatio	n		Anna Jeana			8
/ s	S 🛛 M 👌 T 👌 S 🏹	Е 🛛 🛛	/ м	⟨ M ∖ Fi ∖ V	\ C \ II	nt (Ini (E \ R	. ∕ Ti_ \ ₹
	Object Type	Objec	t Num					
	🔹 🆏 Task	1						
	Task Name	ID	Status	Wait Factor	Wait Data	Time Left	Interrupt	Current Prio
	D_TASK1	0x01	Waiting	DLY TMO FIFO		10000	Enable	1
	Object Type	Objec	t Num					1
Đ	🔹 Cyclic Handler	1						
<			lill.					>

<2> Cyclic handler information

The cyclic handler information (e.g. Cyclic Handler Name and ID) only appears if there are cyclic handlers queued in the timer queue.

See the [Cyclic Handler] tab for details about cyclic handler information.

Figure A-31. [Timer Queue] Tab (Cyclic Handler Information)

Re / s	altime OS Resource Info 6 \ M \ T \ S \ E	rmation E \ D	(м (м	. (Fi (v V c	:	Ini (E (F	🔀 3)∕ Ti_ \ ₹
Object Type Object Num								
Đ	• 🎇 Task	1						
	• 🦪 Cyclic Handler	1						
	Cyclic Handler Name	e ID	Status	Interval	Phase	Time Left	Entry	Extend Inform
	▶ 🐬 ID_CYC1	0x01	TCS_STA	1000	5	1000	0x00009F7C	0х0
<		100						>

The following context menu appears when the header row is right clicked with the mouse.

D	isplay	Displays cascade menus for selecting the header items to display.		
	Selected item name	The following items are displayed for selection. Object Num		
		Checked The item in question will be displayed.		
		Not checked The item in question will not be displayed.		
N	otation	Displays cascade menus for selecting the display notation.		
	Selected item name	The following items are displayed for selection. Object Num		
		DEC Displays value in signed decimal number.		
		HEX	Displays value in hexadecimal number.	

ServiceCall dialog box

Execute a service call process.

Figure A-32. ServiceCall Dialog Box

	ServiceCall	
(1) -	set_fig (0x1,);	
[Function buttons] -	ОК	Cancel

The following items are explained here.

- [How to open]
- [Description of each area]
- [Function buttons]

[How to open]

- From the Realtime OS Resource Information panel context menu, select [Service Call] >> [Service Call Name].

[Description of each area]

(1) Service call display area

This area displays the service call selected in the context menu, in C format.

Remark If the service call requires a value to be input, a text box will appear in this area. Enter the value in question, then click [OK] to run the service call process.

[Function buttons]

Button	Function
ОК	Execute a service call process.
Cancel	Ignore the setting and closes this dialog box.

APPENDIX B INDEX

С

[Cyclic Handler] tab ... 50

D

[Data Queue] tab ... 38

Е

[Eventflag] tab ... 35 [Extended Service Call] tab ... 56

F

[Fixed-Sized Memory Pool] tab ... 46 Functions ... 13

I

[Initialize Routine] tab ... 54 [Interrupt Handler] tab ... 52

Μ

[Mailbox] tab ... 41 [Memory Area] tab ... 25 [Mutex] tab ... 44

R

[Ready Queue] tab ... 58 Realtime OS Resource Information panel ... 20 [Cyclic Handler] tab ... 50 [Data Queuex] tab ... 38 [Eventflag] tab ... 35 [Extended Service Call] tab ... 56 [Fixed-Sized Memory Pool] tab ... 46 [Initialize Routine] tab ... 54 [Interrupt Handler] tab ... 52 [Mailbox] tab ... 41 [Memory Area] tab ... 25 [Mutex] tab ... 44 [Ready Queue] tab ... 58 [Semaphore] tab ... 33 [System] tab ... 22 [Task] tab ... 27

[Timer Queue] tab ... 60 [Variable-Sized Memory Pool] tab ... 48

S

[Semaphore] tab ... 33 ServiceCall dialog box ... 63 [System] tab ... 22

Т

[Task] tab ... 27 [Timer Queue] tab ... 60

۷

[Variable-Sized Memory Pool] tab ... 48

W Window reference ... 19

Published by: NEC Electronics Corporation (http://www.necel.com/) Contact: http://www.necel.com/support/