

REV	REF	DATE	DRAWN BY
1.00	Release	29.11.2019	KOS

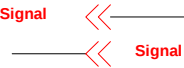
Renesas Starter Kit+ for RX72N CPU Board Schematics

SHEET	DESCRIPTION
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Note:

C : Capacitor
D : Diode
J : Connector, Jumper
L : Inductor
LED : Light Emitting Diode
MR : Resistor Array
PWR : Power Jack
R : Fixed Resistor
RES : Reset Switch
RV : Potentiometer
SW : Switch
T : Test Point
U : Integrated Circuit
X : Crystal, Oscillator

* "DNF" marking means that component is not fitted by default.
**The following off-page connectors used in this schematics do not indicate the signal direction.



Board Code:

RTK5572NNDC00000BE : RSK+RX72N MP Board
RTK5572NNHC00000BE : RSK+RX72N MP Board (Encryption version)

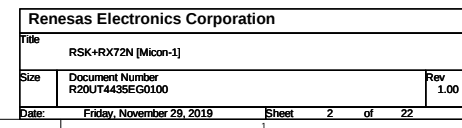
Abbreviations:

CAN : Controller Area Network
IIC : Philips(TM) Inter-Integrated Circuit Connection Bus
LED : Light Emitting Diode
MCU : Microcontroller Unit
PDC : Parallel Data Caputre Unit
PSU : Power Supply Unit
RSK : Renesas Starter Kit
SDHI : SD Host Interface
SSI : Serial Sound Interace
TFT : Thin Film Transistor
USB : Universal Serial Bus

REE Drawing No. D016482_04

Renesas Electronics Corporation			
Title RSK+RX72N [Index]			
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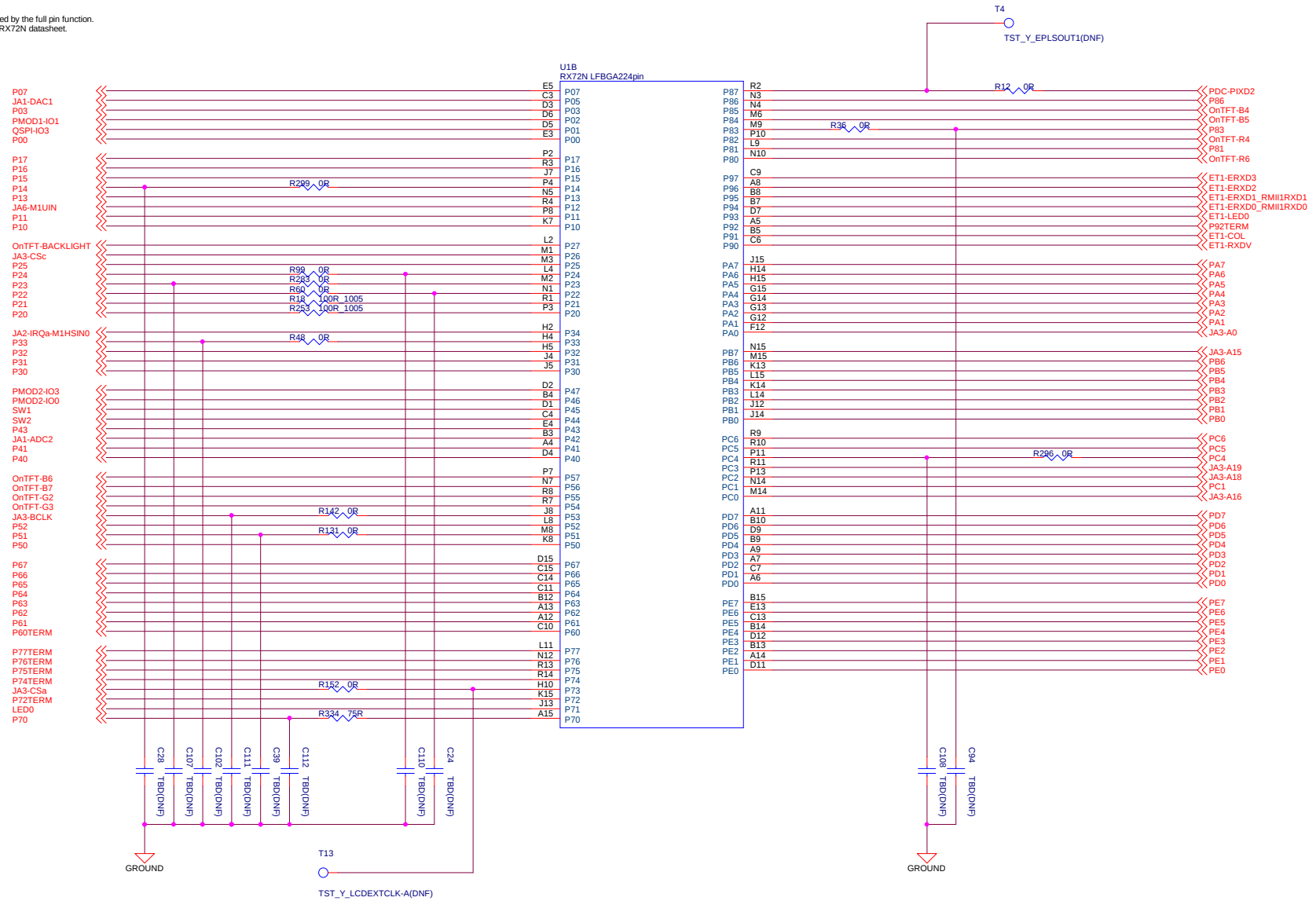
Note:
Microcontroller's pins are not described by the full pin function.
For full pin functions details, refer to RX72N datasheet.



RX72N Microcontroller-2

Note:

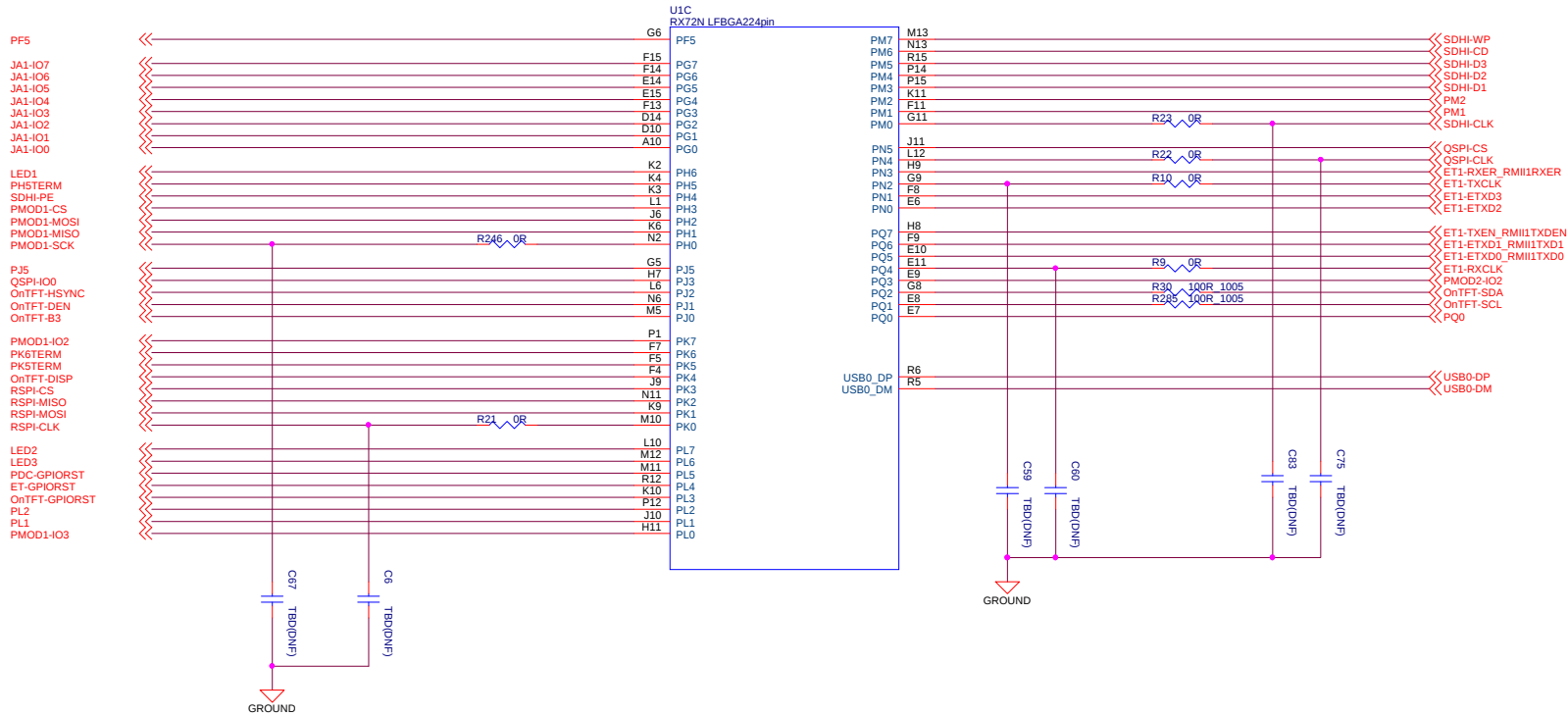
Microcontroller's pins are not described by the full pin function.
For full pin functions details, refer to RX72N datasheet.



RX72N Microcontroller-3

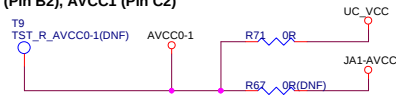
Note:

Microcontroller's pins are not described by the full pin function.
For full pin functions details, refer to RX72N datasheet.

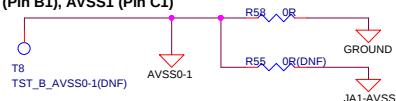


MCU Pin Function Select-1

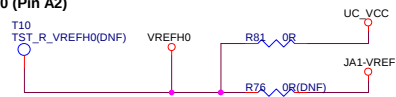
AVCC0 (Pin B2), AVCC1 (Pin C2)



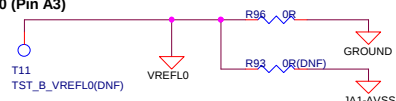
AVSS0 (Pin B1), AVSS1 (Pin C1)



VREFH0 (Pin A2)



VREFL0 (Pin A3)



RES# (Pin G7)



PL5 (Pin M11)



PL3 (Pin K10)



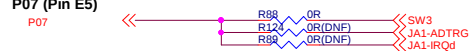
EMLE (Pin F6)



MD_FINED (Pin G4)



P07 (Pin E5)



P03 (Pin D3)



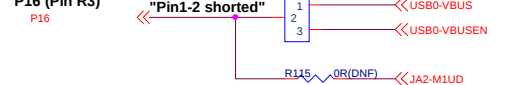
P00 (Pin E3)



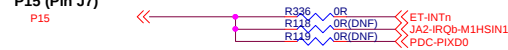
P17 (Pin P2)



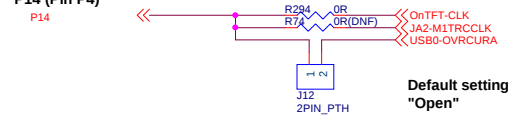
P16 (Pin R3)



P15 (Pin J7)



P14 (Pin P4)



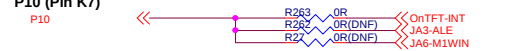
P13 (Pin N5)



P11 (Pin P8)



P10 (Pin K7)



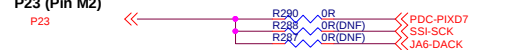
P25 (Pin M3)



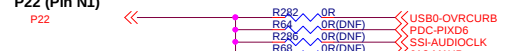
P24 (Pin L4)



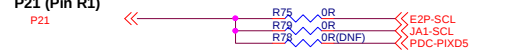
P23 (Pin M2)



P22 (Pin N1)



P21 (Pin R1)



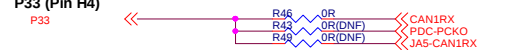
P20 (Pin P3)



P35 (Pin H3)



P33 (Pin H4)



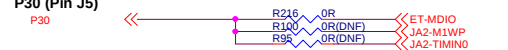
P32 (Pin H5)



P31 (Pin J4)



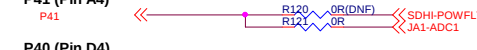
P30 (Pin J5)



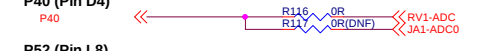
P43 (Pin E4)



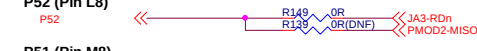
P41 (Pin A4)



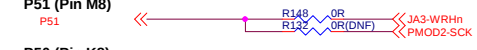
P40 (Pin D4)



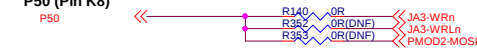
P52 (Pin L8)



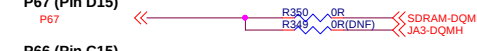
P51 (Pin M8)



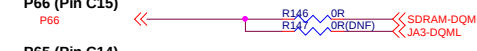
P50 (Pin K8)



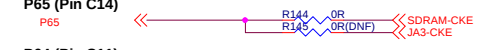
P67 (Pin D15)



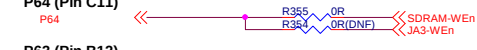
P66 (Pin C15)



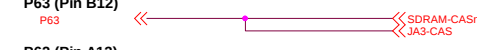
P65 (Pin C14)



P64 (Pin C11)



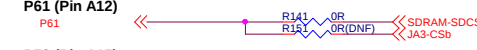
P63 (Pin B12)



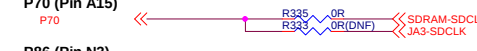
P62 (Pin A13)



P61 (Pin A12)



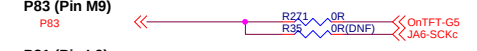
P70 (Pin A15)



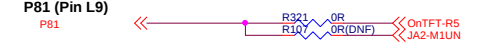
P86 (Pin N3)



P83 (Pin M9)



P81 (Pin L9)



PA7 (Pin J15)



PA6 (Pin H14)



PA5 (Pin H15)



PA4 (Pin G15)



PA3 (Pin G14)



PA2 (Pin G13)



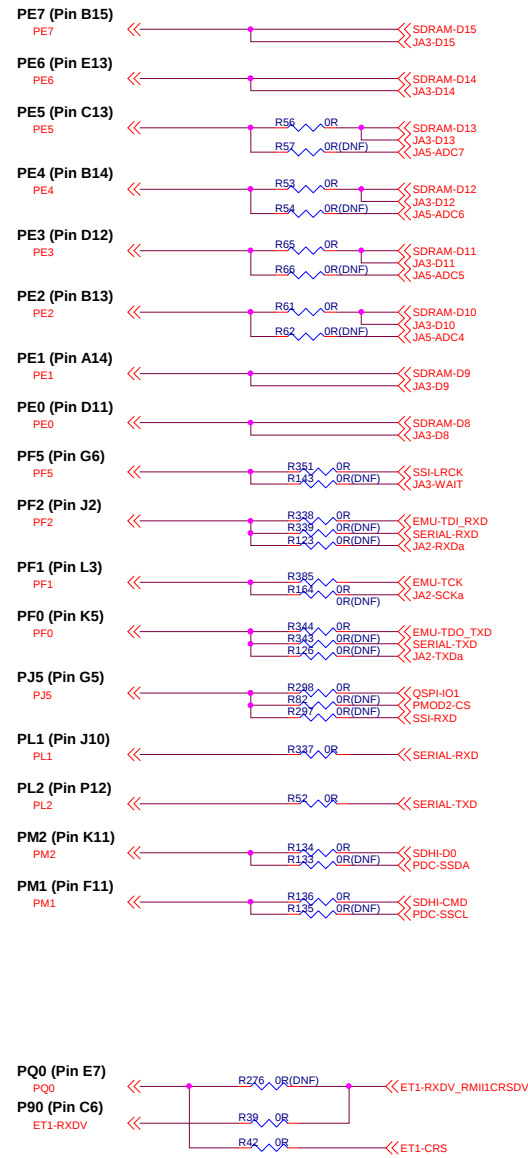
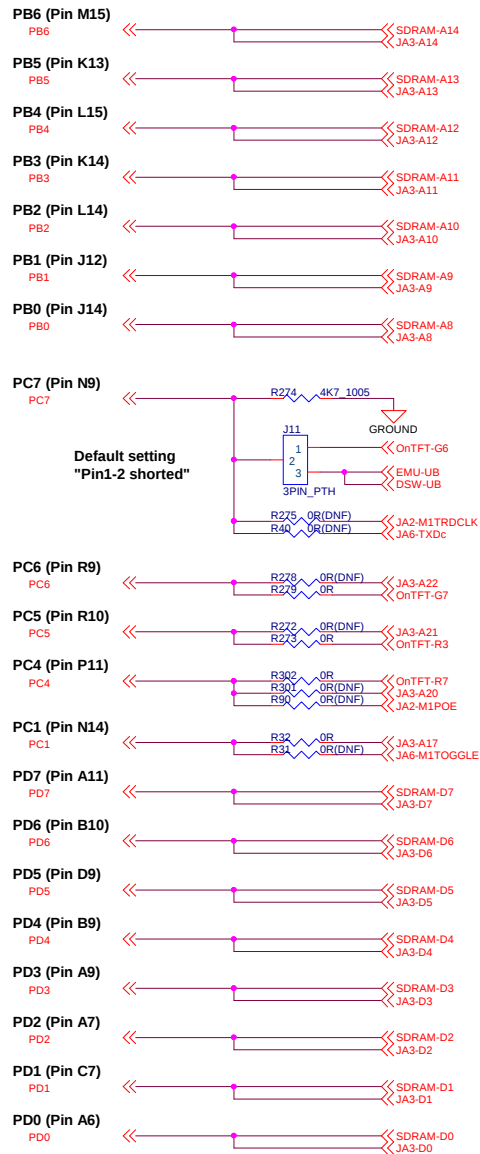
PA1 (Pin G12)



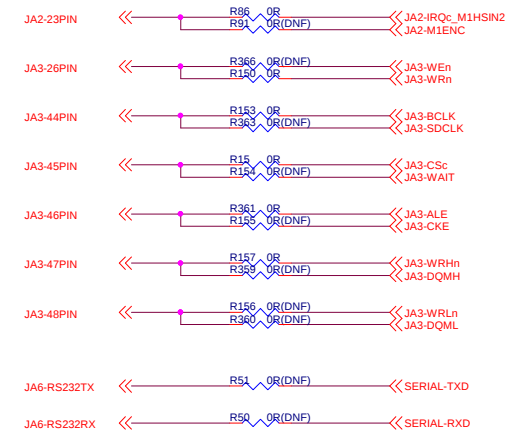
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MCU Pin Function Select-2



Application Header Function Select



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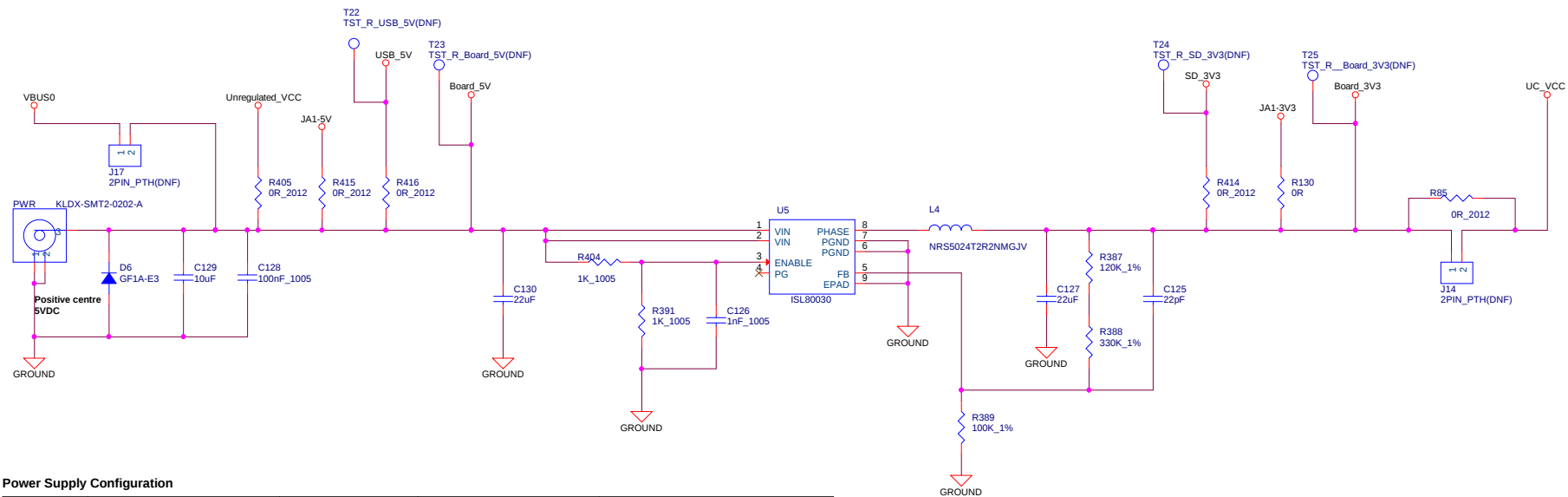
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Size Document Number R20UT4435EG0100

Rev 1.00

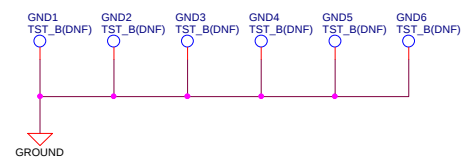
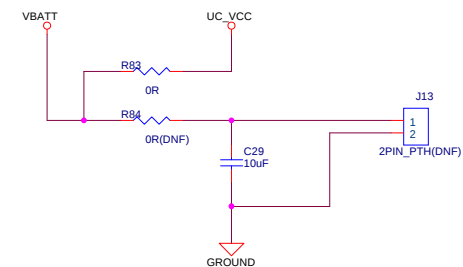
Date: Friday, November 29, 2019 Sheet 6 of 22

Power Supply Unit

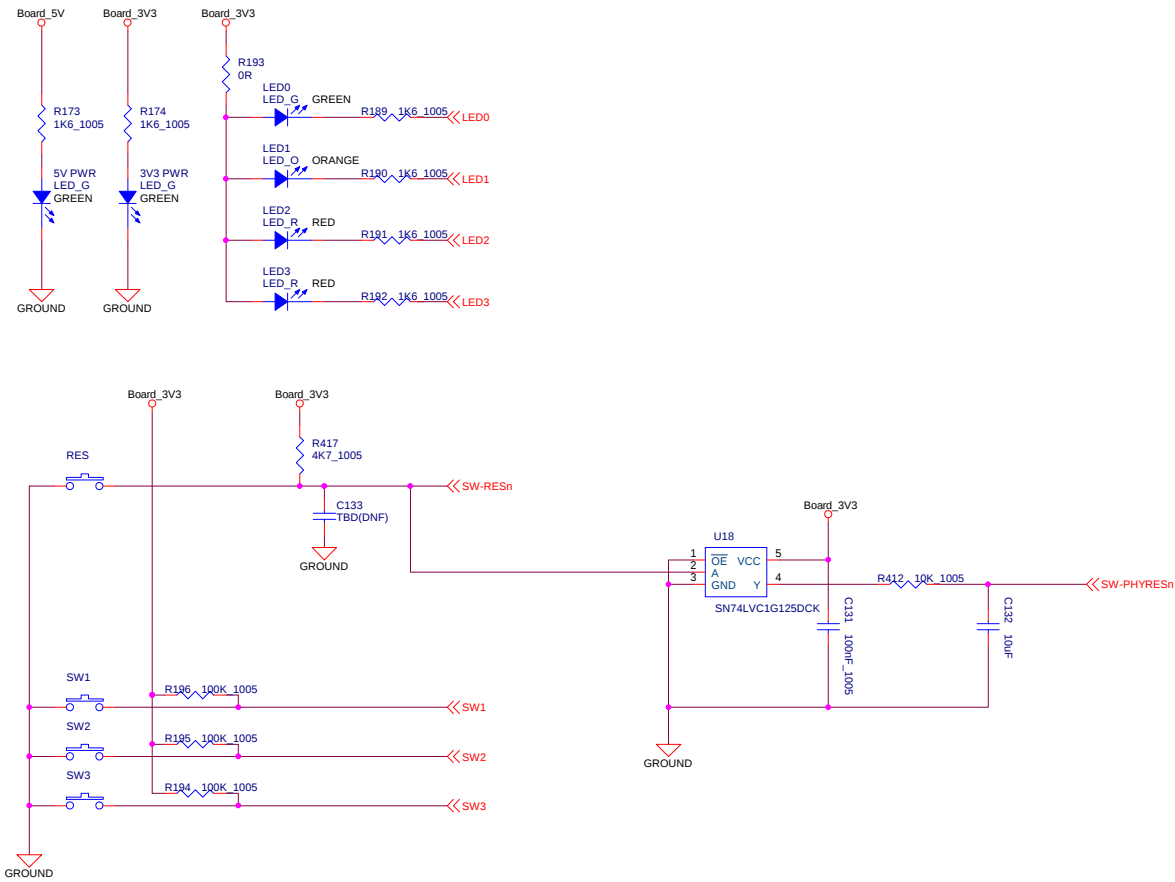


Power Supply Configuration

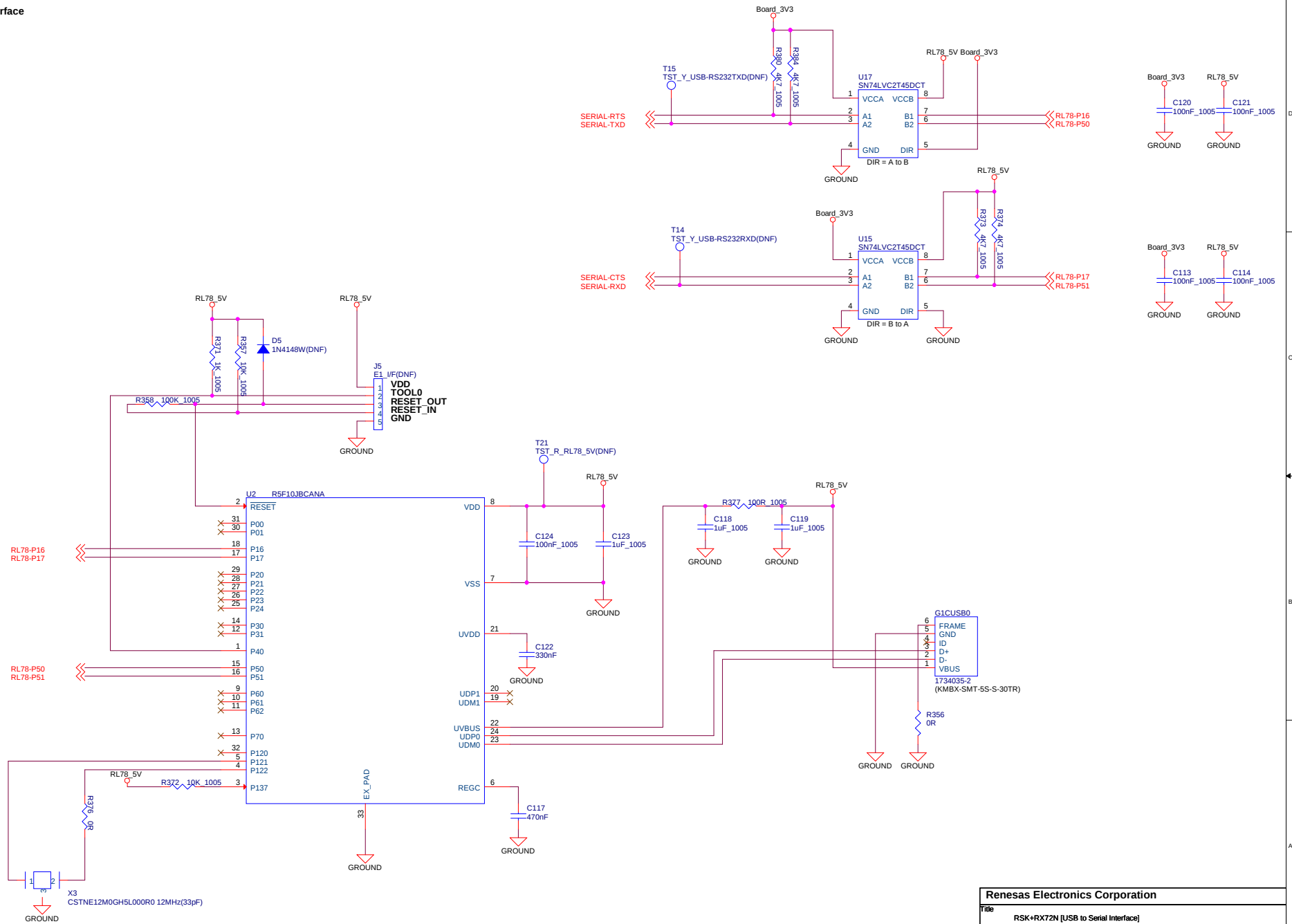
J17	Power Supply Source	Board_5V, USB_5V	UC_VCC, Board_3V3, SD_3V3	Unavailable Feature
open	PWR / Unregulated_VCC / JA1-5V	5V	3.3V	USB (Function-Buspowered)
shorted pin	VBUS	5V	3.3V	USB (Host), USB (Function-Selfpowered)



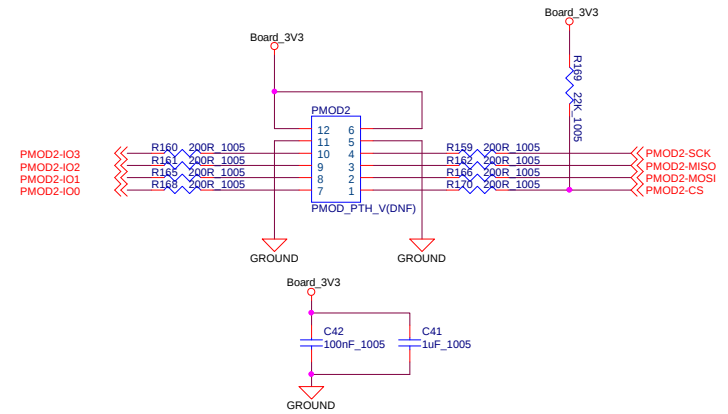
Switches, LEDs, RESET



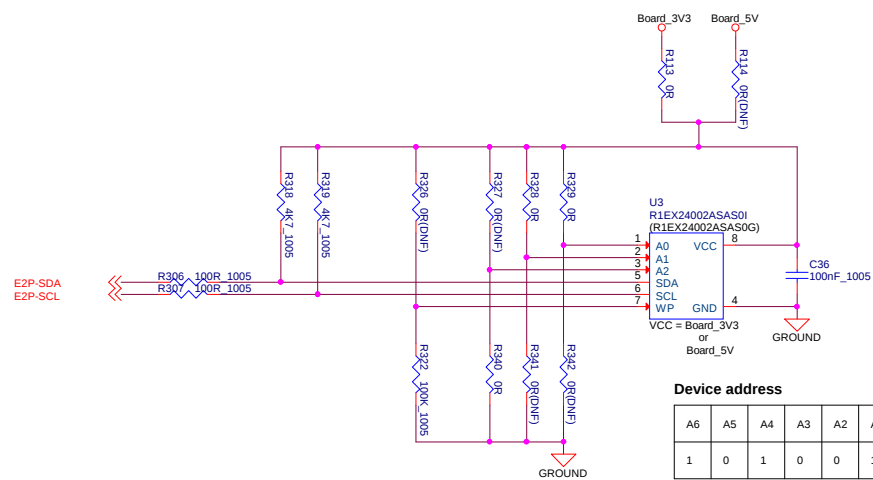
USB to Serial Interface



PMOD1: Angle type connector
PMOD2: Vertical type connector (spare)



Warning:
NEVER FIT R113 and R114 simultaneously.



A6	A5	A4	A3	A2	A1	A0
1	0	1	0	0	1	1

Application Headers

The diagram illustrates the application headers for the RSK+RX72N, showing connections for JA1, JA2, JA3, JA5, and JA6. The headers are populated with various components and connected to external signals.

JA1 Header:

- JA1-AVCC, JA1-5V, JA1-VREFH, JA1-3V3
- JA1-ADC0, JA1-ADC2, JA1-DAC0, JA1-I00, JA1-I02, JA1-I04, JA1-I06, JA1-IRQd, JA1-SDA
- JA1-ADTRG, JA1-ADC1, JA1-ADC3, JA1-DAC1, JA1-I01, JA1-I01, JA1-I03, JA1-I05, JA1-I07, JA1-SCL
- JA1-APP1(DNF)

JA2 Header:

- JA2-RESn, JA2-NMIn
- JA2-IRQa-M1HSIN0, JA2-IRQb-M1HSIN1, JA2-M1UD, JA2-M1UP, JA2-M1VP, JA2-M1WP, JA2-M1WP, JA2-TIMOUT0, JA2-TIMIN0, JA2-23PIN, JA2-M1TRCLK
- JA2-EXTAL, JA2-Vs1, JA2-SCLaTX, JA2-TXDa, JA2-RXDa, JA2-SCKa, JA2-CTSaRTSa, JA2-M1UN, JA2-M1VN, JA2-M1VN, JA2-M1WN, JA2-TIMOUT1, JA2-TIMIN1, JA2-M1POE, JA2-M1TRCLK
- JA2-APP2(DNF)

JA5 Header:

- JA5-ADC4, JA5-ADC6, JA5-CAN1TX
- JA5-ADC5, JA5-ADC7, JA5-CAN1RX
- JA5-APP5(DNF)

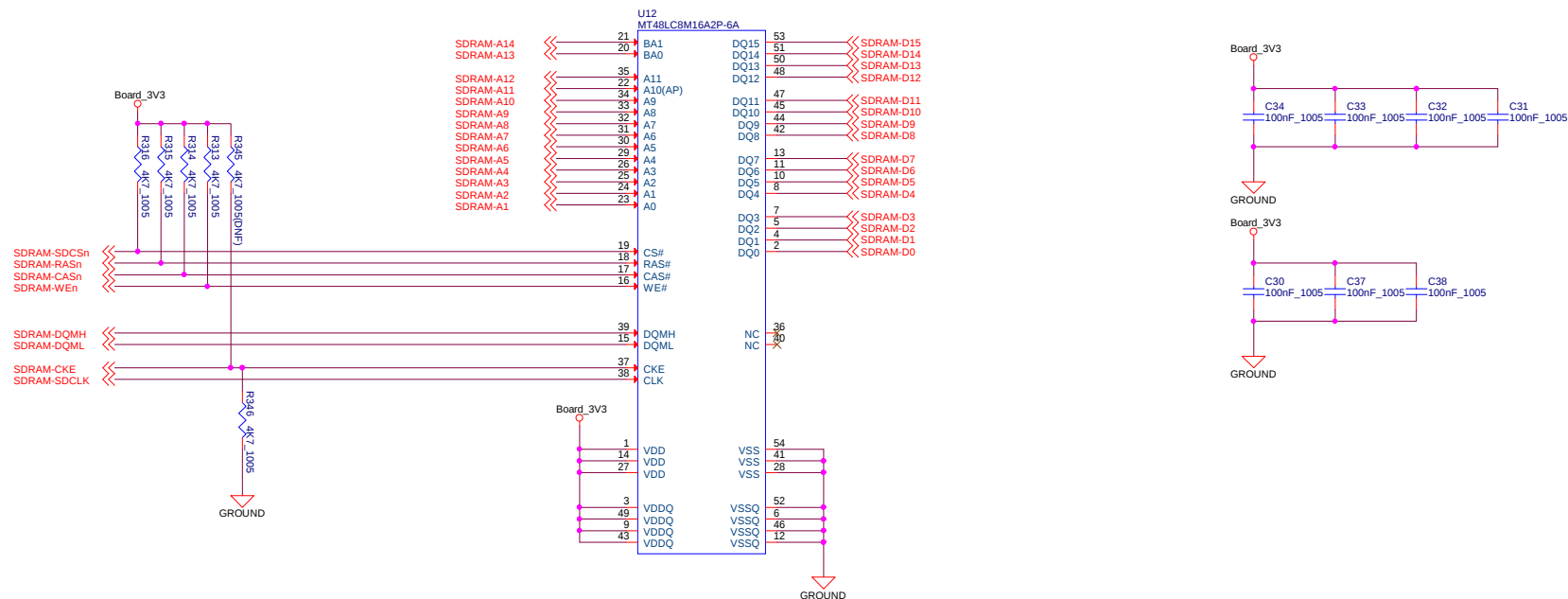
JA6 Header:

- JA6-DREQ, JA6-RS232TX, JA6-RXDb, JA6-TXDC, JA6-SCKc, JA6-M1TOGGLE, JA6-M1VIN
- JA6-DACK, JA6-STBYn, JA6-RS232RX, JA6-SCLbTX, JA6-SCLbCK, JA6-SCKcRX, JA6-M1Uin, JA6-M1Win, JA6-M1VIN
- JA6-APP6(DNF)

JA3 Header:

- JA3-A0, JA3-A2, JA3-A4, JA3-A6, JA3-A8, JA3-A10, JA3-A12, JA3-A14, JA3-D0, JA3-D2, JA3-D4, JA3-D6, JA3-RDn, JA3-CSa, JA3-D8, JA3-D10, JA3-D12, JA3-D14, JA3-A16, JA3-A18, JA3-A20, JA3-A22, JA3-45PIN, JA3-47PIN, JA3-CAS
- JA3-A1, JA3-A3, JA3-A5, JA3-A7, JA3-A9, JA3-A11, JA3-A13, JA3-A15, JA3-D1, JA3-D3, JA3-D5, JA3-D7, JA3-26PIN, JA3-CSb, JA3-D9, JA3-D11, JA3-D13, JA3-D15, JA3-A17, JA3-A19, JA3-A21, JA3-44PIN, JA3-46PIN, JA3-48PIN, JA3-RAS
- JA3-APP3(DNF)

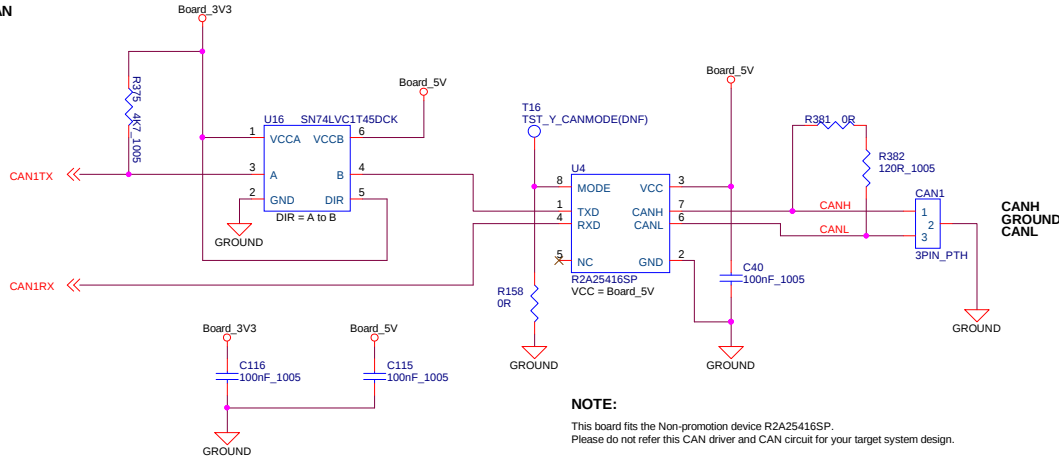
SDRAM(128Mbits)



A



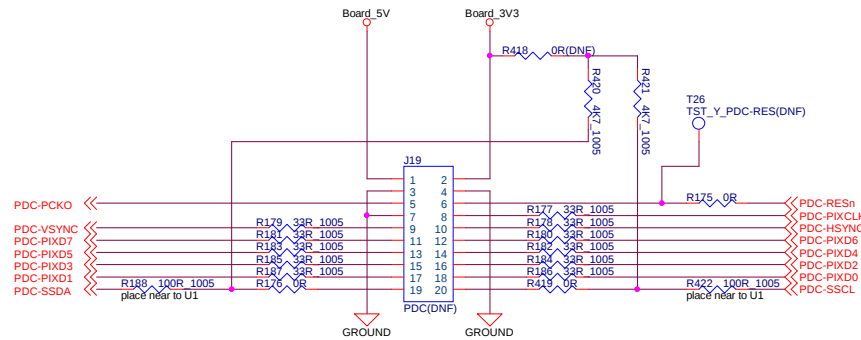
CAN



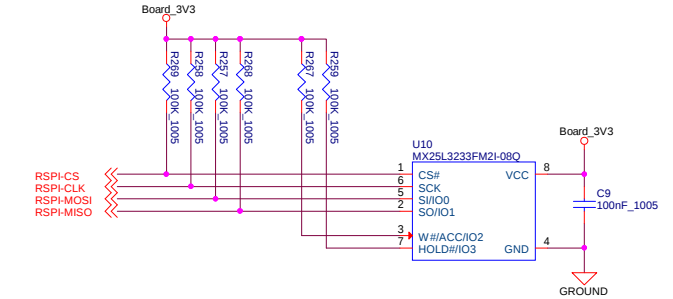
PDC

Warning:

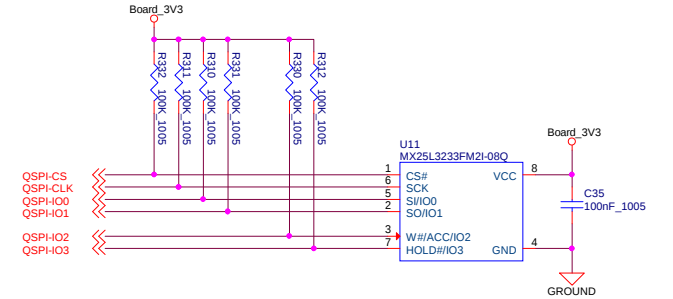
When the voltage of your PDC target system differs from this board,
please do not supply a board power to your system directly.
When the voltage of your PDC target system differs from this board, please do not fit resistor R418.
Please fix pull-up resistor on your PDC target system.



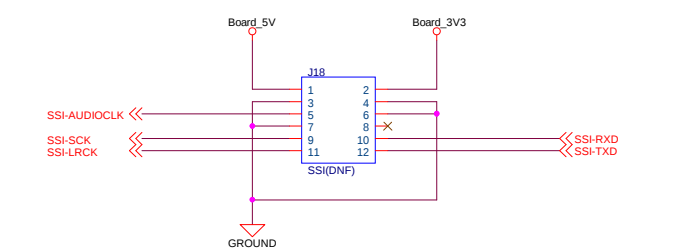
RSPI- SPI Serial Flash(32Mbits)



QSPI- SPI Serial Flash(32Mbits)



SSI



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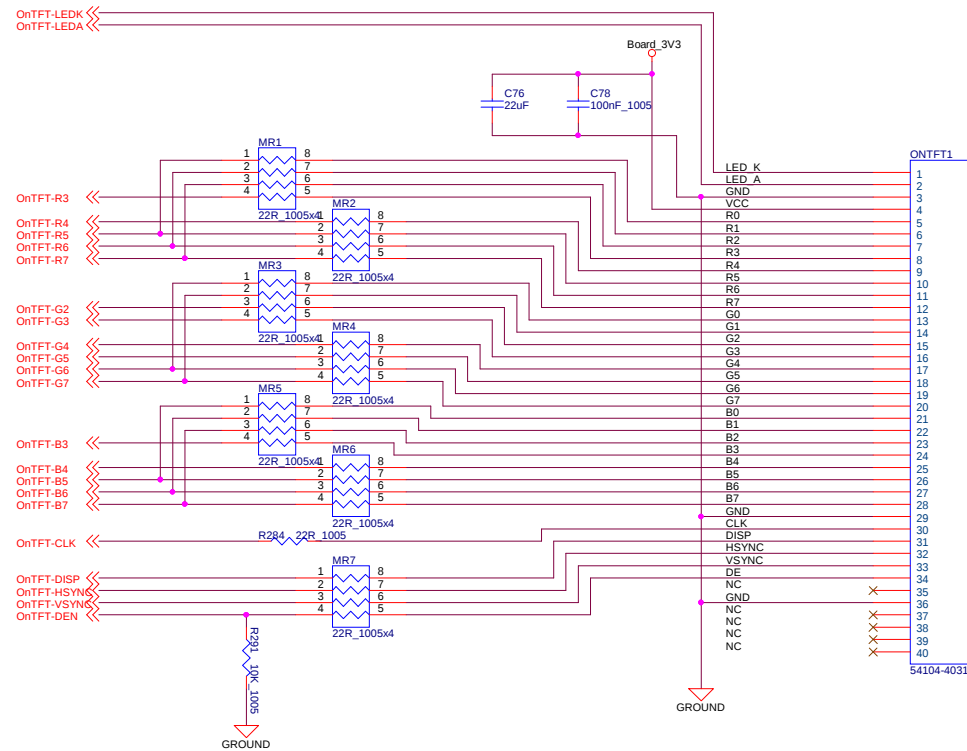
Title		
RSK+RX72N [CAN, RSPI, QSPI, PDC, SSI]		
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On-board TFT

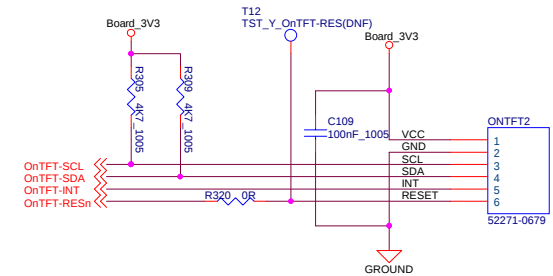
Note:

On-board TFT circuit is the design based on NHD-4.3-480272EF-ATXL#-CTP (4.3" 480x272 with Capacitive Touch).

Display Interface



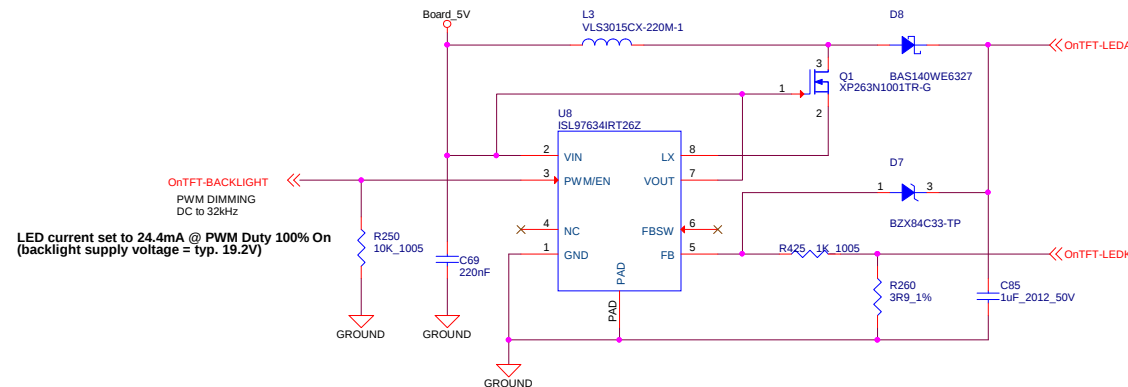
Capacitive Touch Interface



Device address for touch panel driver

A6	A5	A4	A3	A2	A1	A0
0	1	1	1	0	0	0

LED Backlight



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Title		
RSK+RX72N [On-board TFT]		
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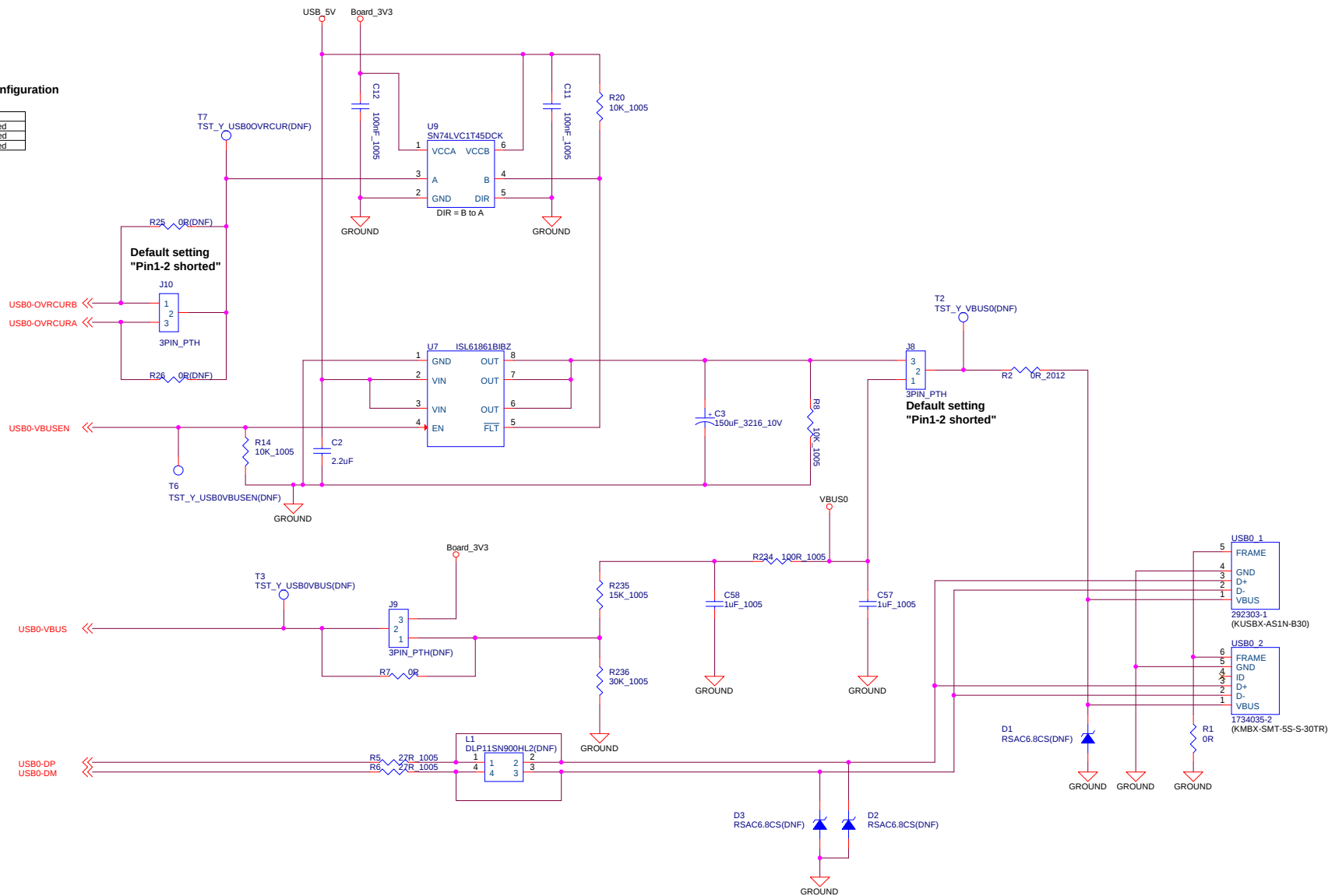
USB0 Host/Function

USB0 Host/Function Select

J9	Function Mode
1-2 shorted	Function Mode
2-3 shorted	Host Mode

Self-powered/Bus-powered Configuration for Function Mode

J9	R7	Self-powered
1-2 shorted	Remove	Self-powered
2-3 shorted	Remove	Bus-powered
Open	Fit	Self-powered



Ethernet (PHY)

ET1-TXCLK
ET1-TXEN_RMII1TXD0
ET1-ETXD0_RMII1TXD0
ET1-ETXD1_RMII1TXD1
ET1-ETXD2
ET1-ETXD3

ET1-RXCLK
ET1-RXDV_RMII1CRSDV
ET1-ERXD0_RMII1RXD0
ET1-ERXD1_RMII1RXD1
ET1-ERXD2
ET1-ERXD3
ET1-RXER_RMII1RXER
ET1-COL
ET1-CRS

ET-MDC
ET-MDIO
ET-INTn

SW-PHYRESn

ET-GPIORST

CLKOUT25M

PHYAD[2-0]=001
CONFIG[2-0]=000 -> MII(Default), 001 -> RMII
ISOLATE=0 -> Disable
SPEED=1 -> 100Mbps
DUPLEX=0 -> Full Duplex
NWAY=1 -> Auto-nego Enable

U13
TXC
TXEN/TX_EN
TXD0/TXD[0]
TXD1/TXD[1]
TXD2
TXD3

RXC
RXDV/CRSDV/CONFIG2
RXD0/RXD[0]/DUPLEX
RXD1/RXD[1]/PHYAD2
RXD2/PHYAD1
RXD3/PHYAD0
RXER/RX_ER/ISO
COL/CONFIG0
CRS/CONFIG1

MDC
MDIO
INTRP#
RST#
LED0/NWAYEN
LED1/SPEED

XI/REFCLK
XO

LED0:Link With ACT(Default) / Link
LED1:Speed(Default) / ACT

KS29041NL

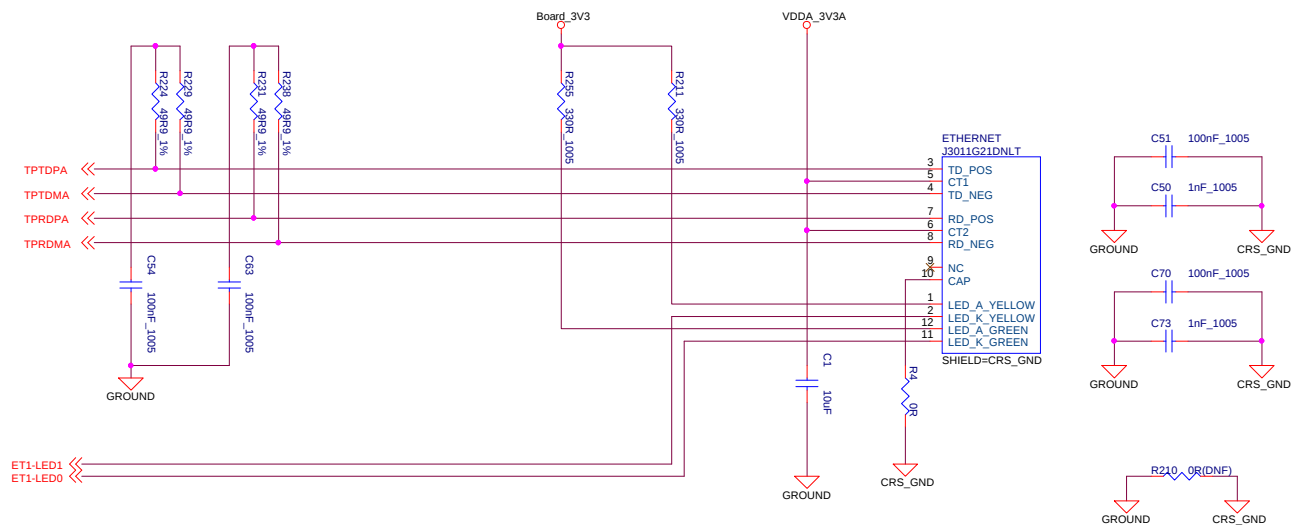
TX+
TX-
RX+
RX-

VDDPLL_1.8
VDDA_3.3
VDD_IO3.3
REXT
GND
GND_PAD

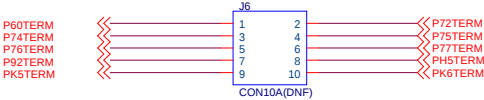
C61
C64
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