REV	REF	DATE	DRAWN BY
1.00	Release	04.04.2011	YOI
2.00	TRAC#1998, 2043	13.09.2011	YOI

Renesas Starter Kit for RX630
CPU Board Schematics

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Note:

R : Fixed Resistor
RV : Potentiometer
U : Integrated Circuit
X : Crystal
RES : Reset Switch
SW : Switch
LED : Light Emitting Diode
PWR : Power Jack
J : Connector, Jumper

*** "DNF" marking means that component does not fit by default.**

Board Variation:

R0K505630C000BE : WS/MP Product

REE Drawing No. D010162

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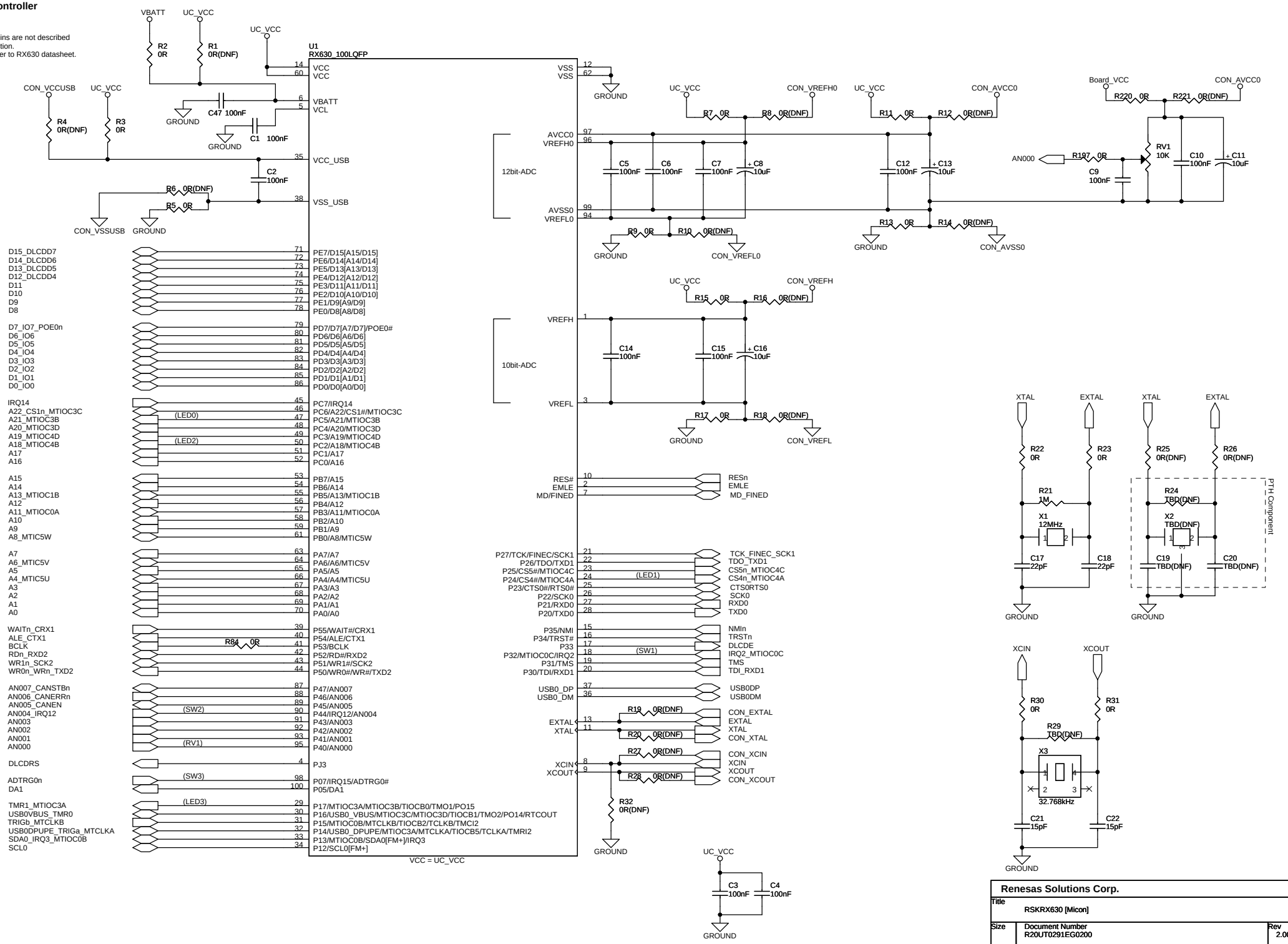
RX630 Microcontroller

Note:

Microcontroller's pins are not described by the full pin function.
For full details, refer to RX630 datasheet.



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Title: RSKRX630 [Micon]		
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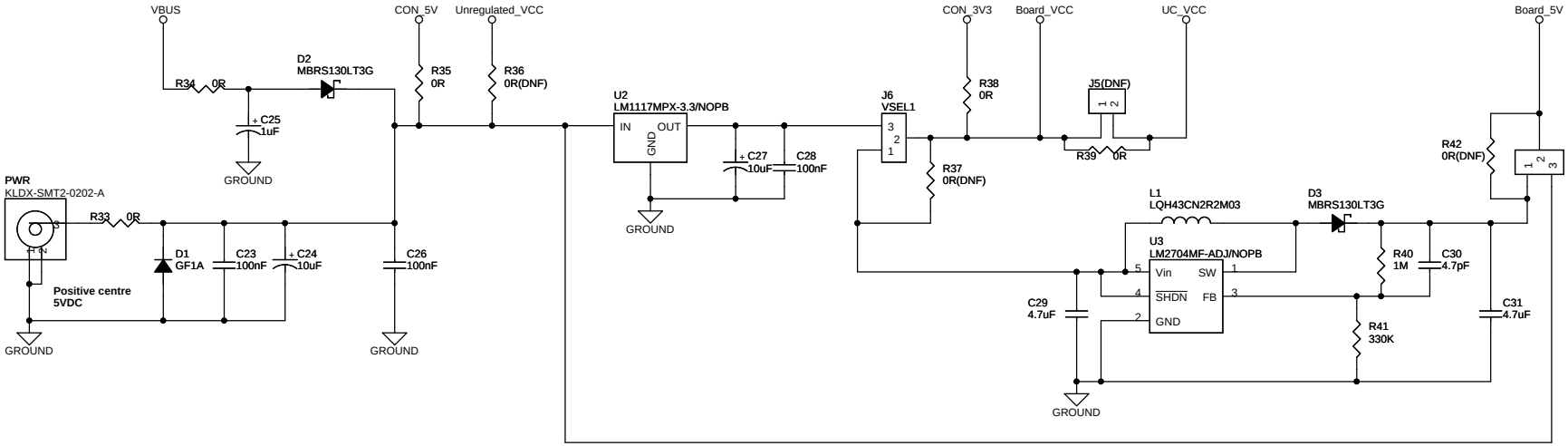
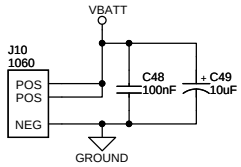
Power Supply Unit



Power Supply Cofiguration

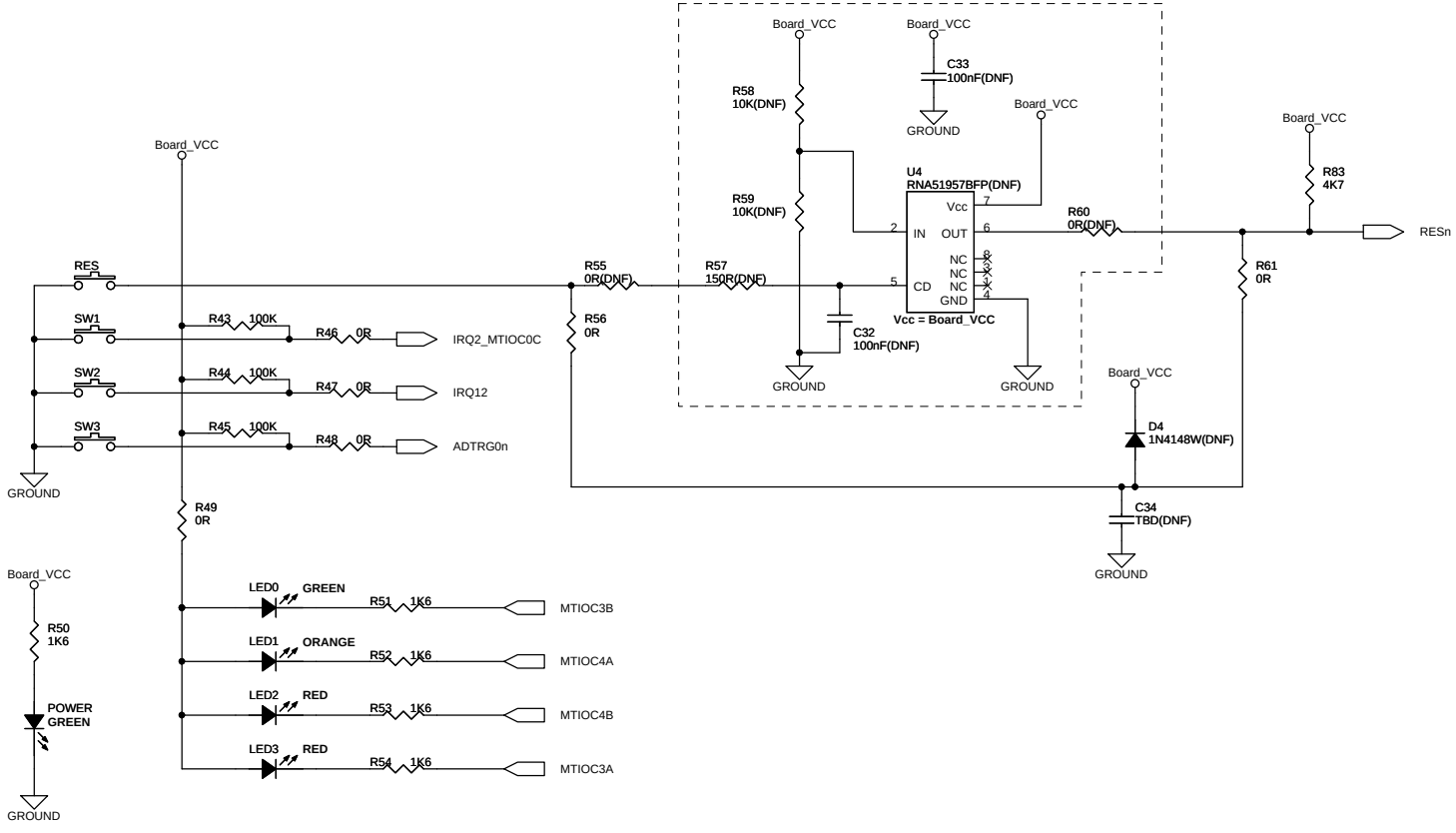
J6	J7	Supply Type
Shorted Pin1-2	Shorted Pin1-2	E1(3V3), CON_3V3
Shorted Pin2-3	Shorted Pin2-3	PWR, VBUS, CON_5V
all open	all open	DO NOT SET

Coin cell holder for VBATT

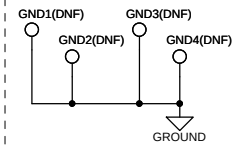


Switches, LEDs, RESET

Circuit not Fitted on RSKRX630 CPU Board.
RX630 internal Power on Rreset is used.



Ground Test Point



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Title: RSKRX630 [PSU, RESET, SW, LED]		
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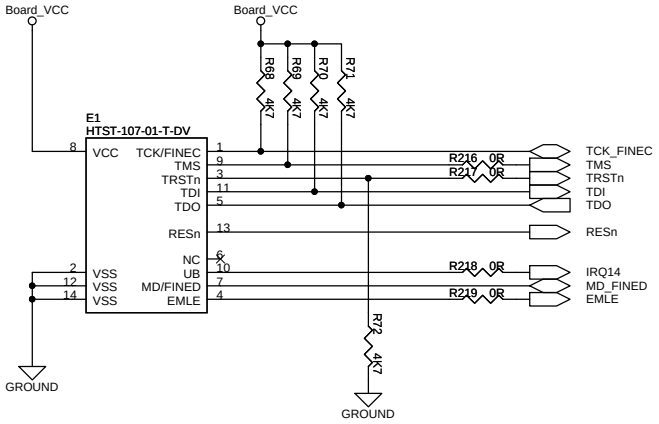
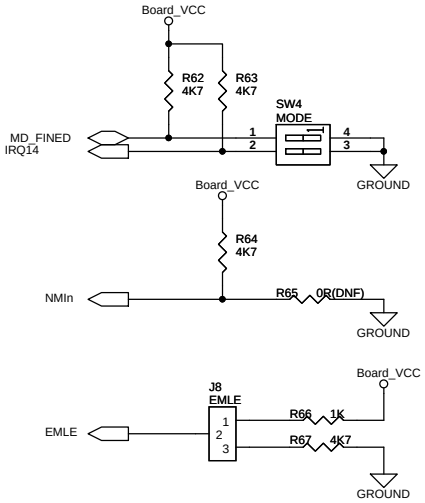
E1 Emulator Interface



MCU Mode Settings



MCU Operating Mode Configuration

SW4 Pin1	SW4 Pin2	Operating Mode
OFF	Don't care	Single Chip Mode
ON	OFF	User Boot Mode USB Boot Mode
ON	ON	Boot Mode (SCI)

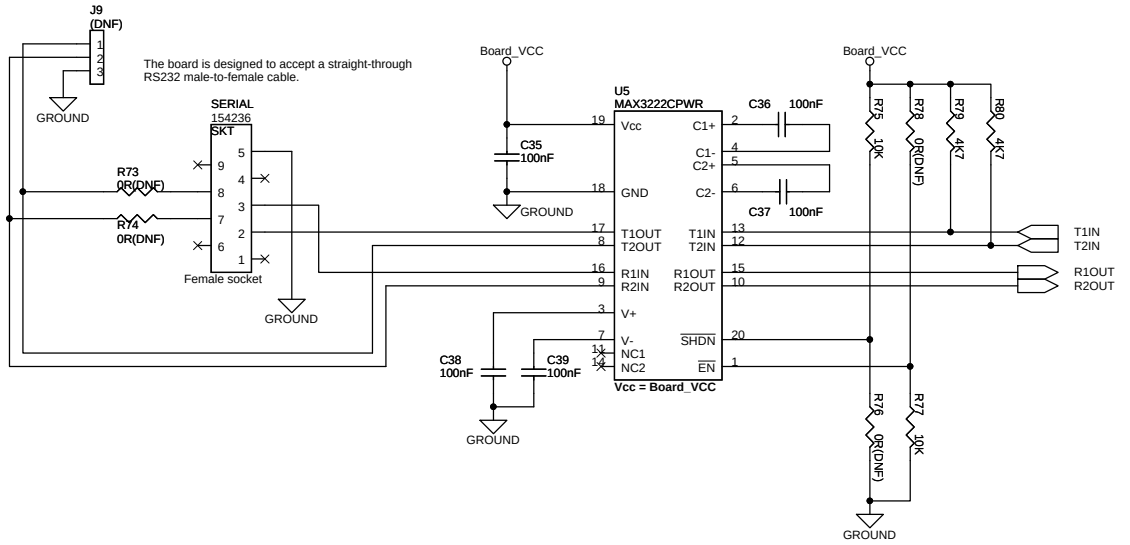
USB Boot Mode Power Configuration

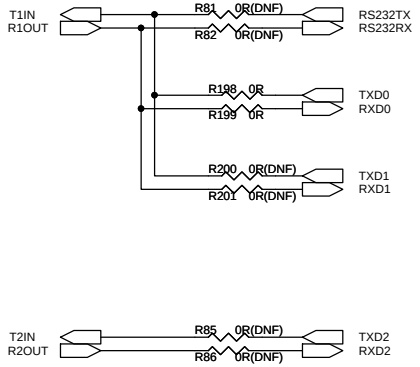
R65	Power Configuration for USB Boot Mode
Fit	Self Powered
Remove	Bus Powered

Emulator Configuration

J8	Emulator Configuration
Shorted Pin1-2	E1 debugging with Hot plug-in
Shorted Pin2-3	E1 normal debugging Microcontroller signle operation (without E1)
all open	DO NOT SET

Serial Port





To App Header
(Do not connect MCU pins)

SCI0

SCI1

Note:
SCI1(TXD1, RXD1 and SCK1) is shareing with the JTAG pins.
So, E1 and SCI1 are used exclusively.

SCI2

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Title	RSKRX630 [E1, MCU Mode Setting, Serial Port]		
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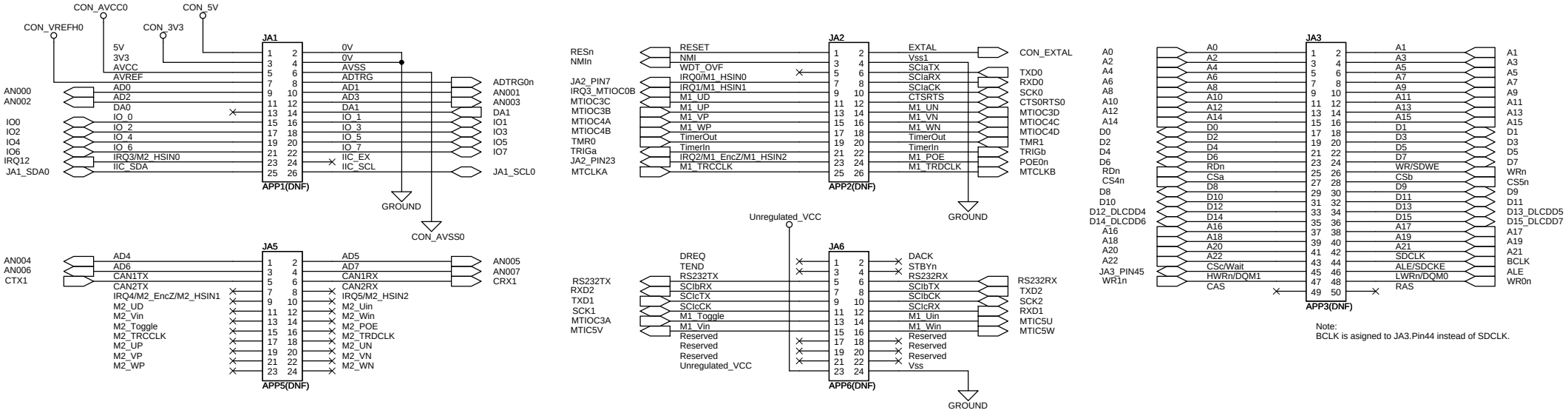
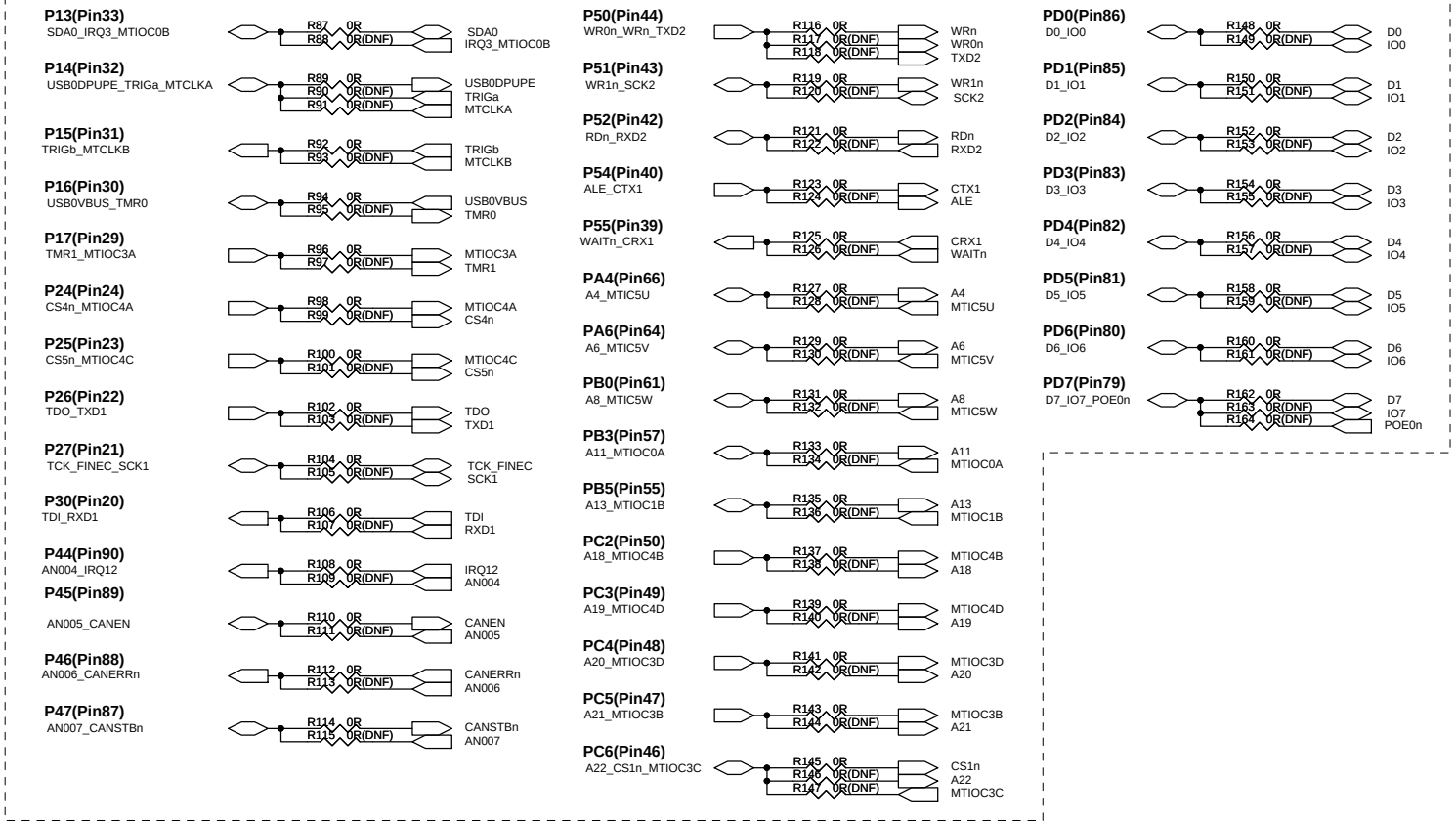
Application Headers

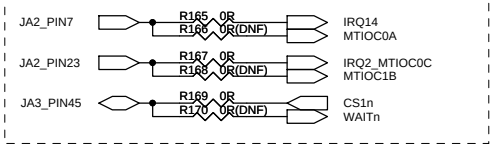


Note:
BCLK is asigned to JA3.Pin44 instead of SDCLK.

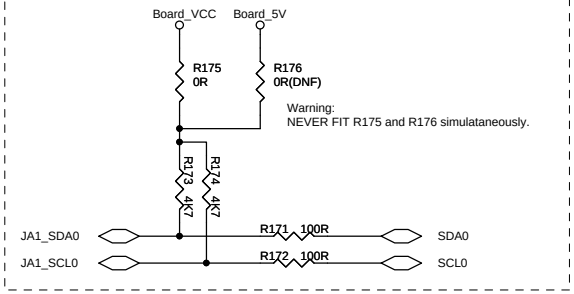
MCU Pin Function Select



Header Function Select



IIC resistor handling



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Title	RSKRX630 [App Headers, Header Func Select, Pin Func Select]
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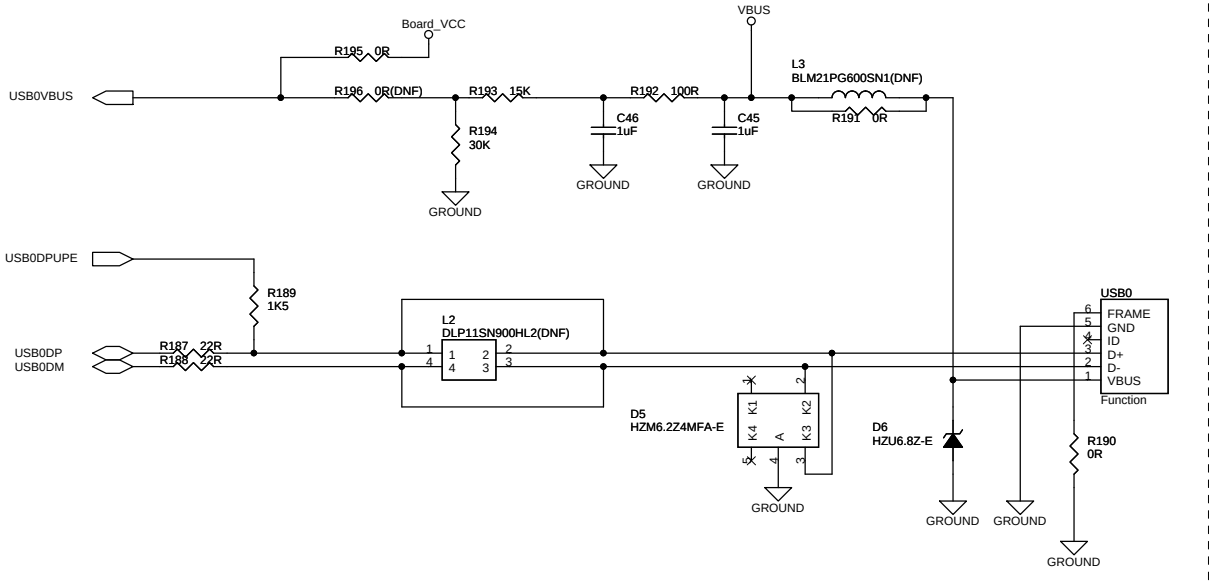
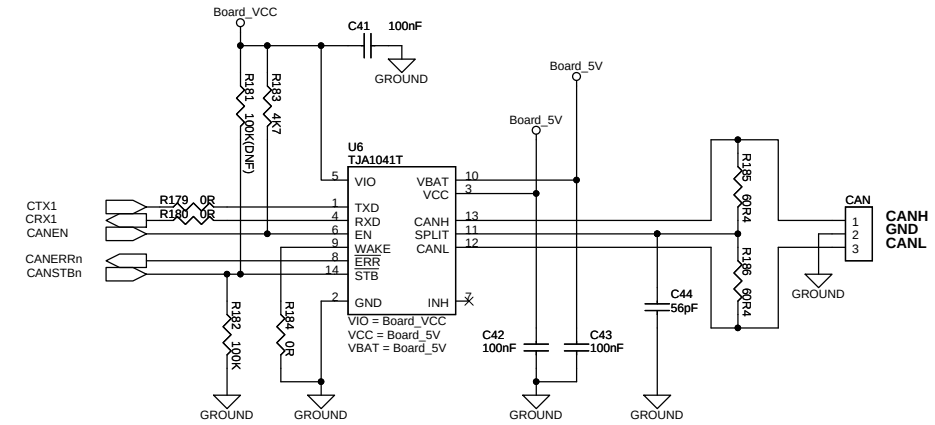
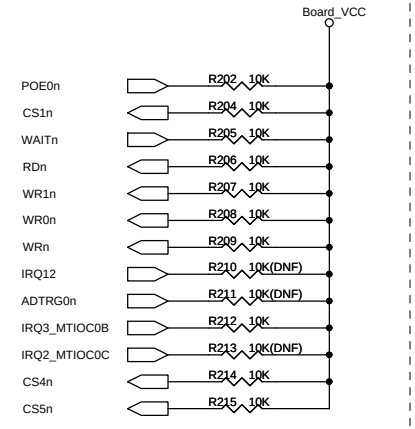
USB



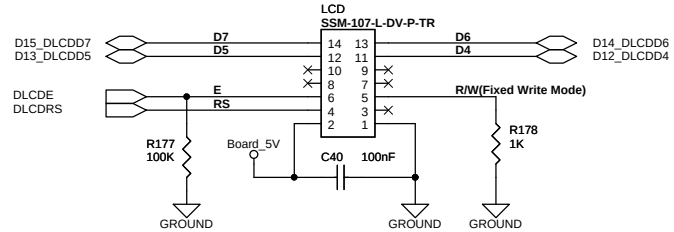
CAN



Pull-up resistor



Debug LCD



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Title: RSKRX630 [Debug LCD, CAN, USB]		
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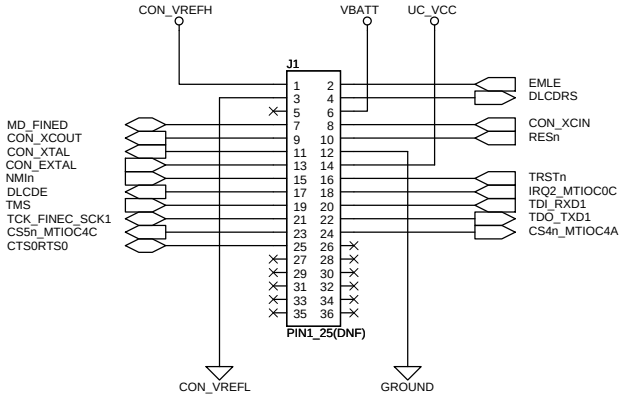
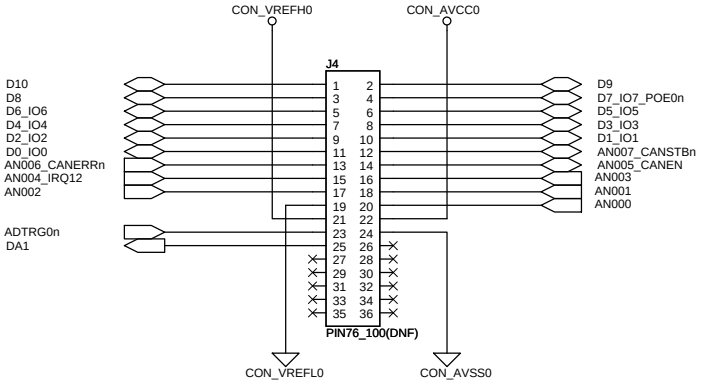
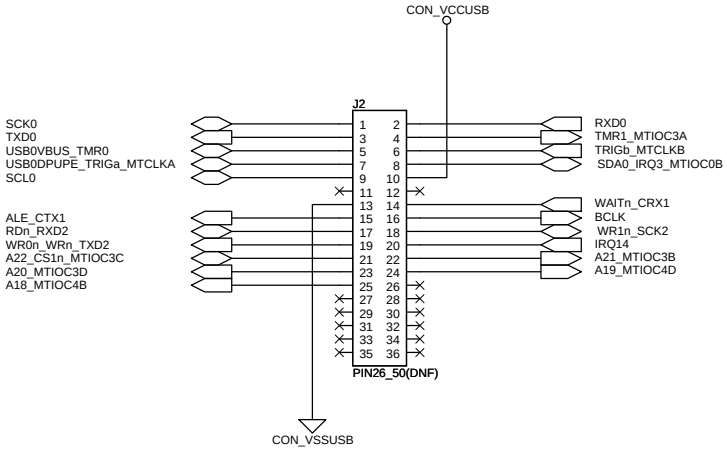
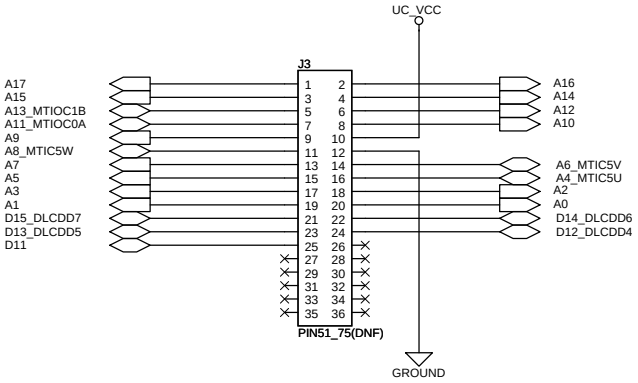
Microcontroller Pin Headers









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Title: RSKRX630 [Micon Pin Headers]		
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