

RA8 Group

Application Board for RA8 Microcontroller Group
Parallel Graphics Expansion Board 2 v1
User's Manual

Renesas RA8 Family
RA8 Series

All information contained in these materials, including products and product specifications, represents information on the product at the time of publication and is subject to change by Renesas Electronics Corp. without notice. Please review the latest information published by Renesas Electronics Corp. through various means, including the Renesas Electronics Corp. website (<https://www.renesas.com>).

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.

"Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.5.0-1 October 2020)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Renesas Parallel Graphics Expansion Board Disclaimer

By using this Parallel Graphics Expansion Board, the User accepts the following terms, which are in addition to, and control in the event of disagreement, with Renesas' General Terms and Conditions available at [renesas.com/legal-notices](https://www.renesas.com/legal-notices).

The Parallel Graphics Expansion Board is not guaranteed to be error free, and the entire risk as to the results and performance of the Parallel Graphics Expansion Board assumed by the User. The Parallel Graphics Expansion Board is provided by Renesas on an "as is" basis without warranty of any kind whether express or implied, including but not limited to the implied warranties of good workmanship, fitness for a particular purpose, title, merchantability, and non-infringement of intellectual property rights. Renesas expressly disclaims any implied warranty.

Renesas does not consider the Parallel Graphics Expansion Board to be a finished product and therefore the Parallel Graphics Expansion Board may not comply with some requirements applicable to finished products, including, but not limited to recycling, restricted substances and electromagnetic compatibility regulations. Refer to Certifications section, for information about certifications and compliance information for the Parallel Graphics Expansion Board. It is the kit User's responsibility to make sure the kit meets any local requirements applicable to their region.

Renesas or its affiliates shall in no event be liable for any loss of profit, loss of data, loss of contract, loss of business, damage to reputation or goodwill, any economic loss, any reprogramming or recall costs (whether the foregoing losses are direct or indirect) nor shall Renesas or its affiliates be liable for any other direct or indirect special, incidental or consequential damages arising out of or in relation to the use of this Parallel Graphics Expansion Board, even if Renesas or its affiliates have been advised of the possibility of such damages.

Renesas has used reasonable care in preparing the information included in this document, but Renesas does not warrant that such information is error free nor does Renesas guarantee an exact match for every application or parameter to part numbers designated by other vendors listed herein. The information provided in this document is intended solely to enable the use of Renesas products. No express or implied license to any intellectual property right is granted by this document or in connection with the sale of Renesas products. Renesas reserves the right to make changes to specifications and product descriptions at any time without notice. Renesas assumes no liability for any damages incurred by you resulting from errors in or omissions from the information included herein. Renesas cannot verify, and assumes no liability for, the accuracy of information available on another company's website.

Precautions

This Application Board is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area, or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. There is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures:

- Ensure attached cables do not lie across the equipment.
- Reorient the receiving antenna.
- Increase the distance between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that which the receiver is connected.
- Power down the equipment when not in use.
- Consult the dealer or an experienced radio/TV technician for help.

Note: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken:

- The user is advised that mobile phones should not be used within 10 m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Application Board does not represent an ideal reference design for an end product and does not fulfill the regulatory standards for an end product.

Renesas RA Family

Parallel Graphics Expansion Board 2 v1

Contents

1. Kit Overview.....	2
1.1 Assumptions and Advisory Notes.....	4
2. Kit Contents	5
3. Ordering Information.....	5
4. Connectivity	6
4.1 Parallel Graphics Expansion Port (J1).....	6
4.2 FPC Connector for LCD Data (J2).....	8
4.3 FPC Connector for LCD Capacitive Touch (J3).....	10
4.4 LCD Backlight Controller (U1)	10
5. Certifications	11
5.1 EMC/EMI Standards.....	11
5.2 Material Selection, Waste, Recycling and Disposal Standards	11
5.3 Safety Standards.....	11
6. Design and Manufacturing Information	12
7. Website and Support.....	12
Revision History	13

Figures

Figure 1. Parallel Graphics Expansion Board 2 Top Side.....	2
Figure 2. Parallel Graphics Expansion Board 2 Bottom Side	3
Figure 3. Parallel Graphics Expansion Board 2 Kit Contents	5
Figure 4. Parallel Graphics Expansion Port.....	7
Figure 5. FPC Connector for LCD Data.....	9
Figure 6. FPC Connector for LCD Capacitive Touch.....	10
Figure 7. LCD Backlight Controller	10

Tables

Table 1. Parallel Graphics Expansion Port.....	6
Table 2. FPC Connector for LCD Data.....	8
Table 3. FPC Connector for LCD Capacitive Touch	10
Table 4. Parallel Graphics Expansion Board 2 Design Package Contents.....	12

1. Kit Overview

The Parallel Graphics Expansion Board 2 Application Kit for RA8 MCU Group enables users to add LCD functionality using an RGB parallel digital interface to seamlessly evaluate the features of the RA8 MCU group. With a programmable evaluation kit such as the EK-RA8E2, users can develop embedded systems applications using Flexible Software Package (FSP) and e² studio IDE. Whilst designed for the RA8 MCU Group, this application board can be applied to any evaluation board with the correct peripheral output and compatible Parallel Graphics Expansion Port.

The key features of the Parallel Graphics Expansion Board 2 are as follows:

Key features:

- Adds LCD Functionality to Evaluation Kits via a 24-bit RGB Interface
- 800 × 480 pixel 5-Inch TFT Display with Capacitive Touch Panel ([E50GE-I-RW420-C](#))
- Compatible with the EK-RA8E2

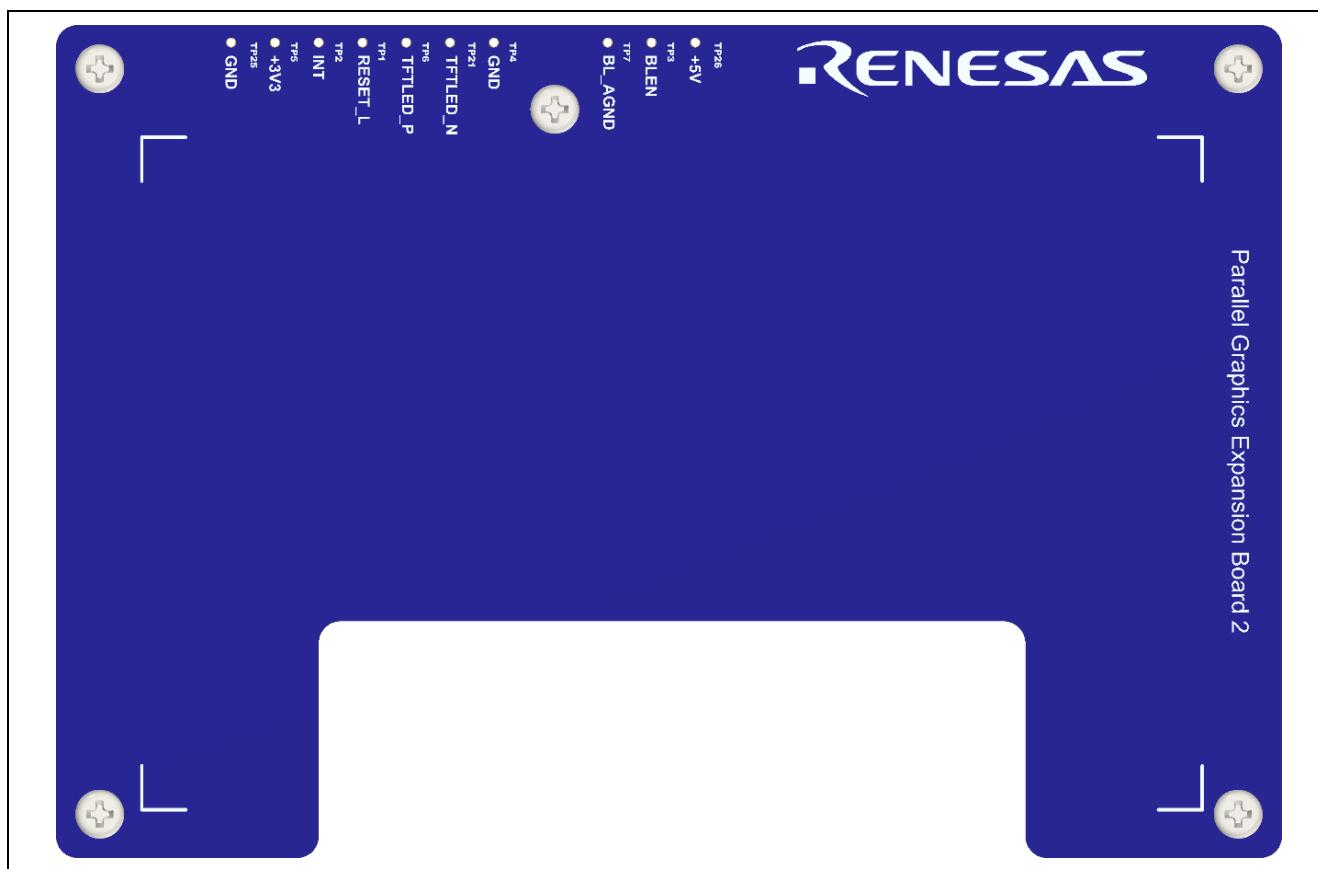


Figure 1. Parallel Graphics Expansion Board 2 Top Side

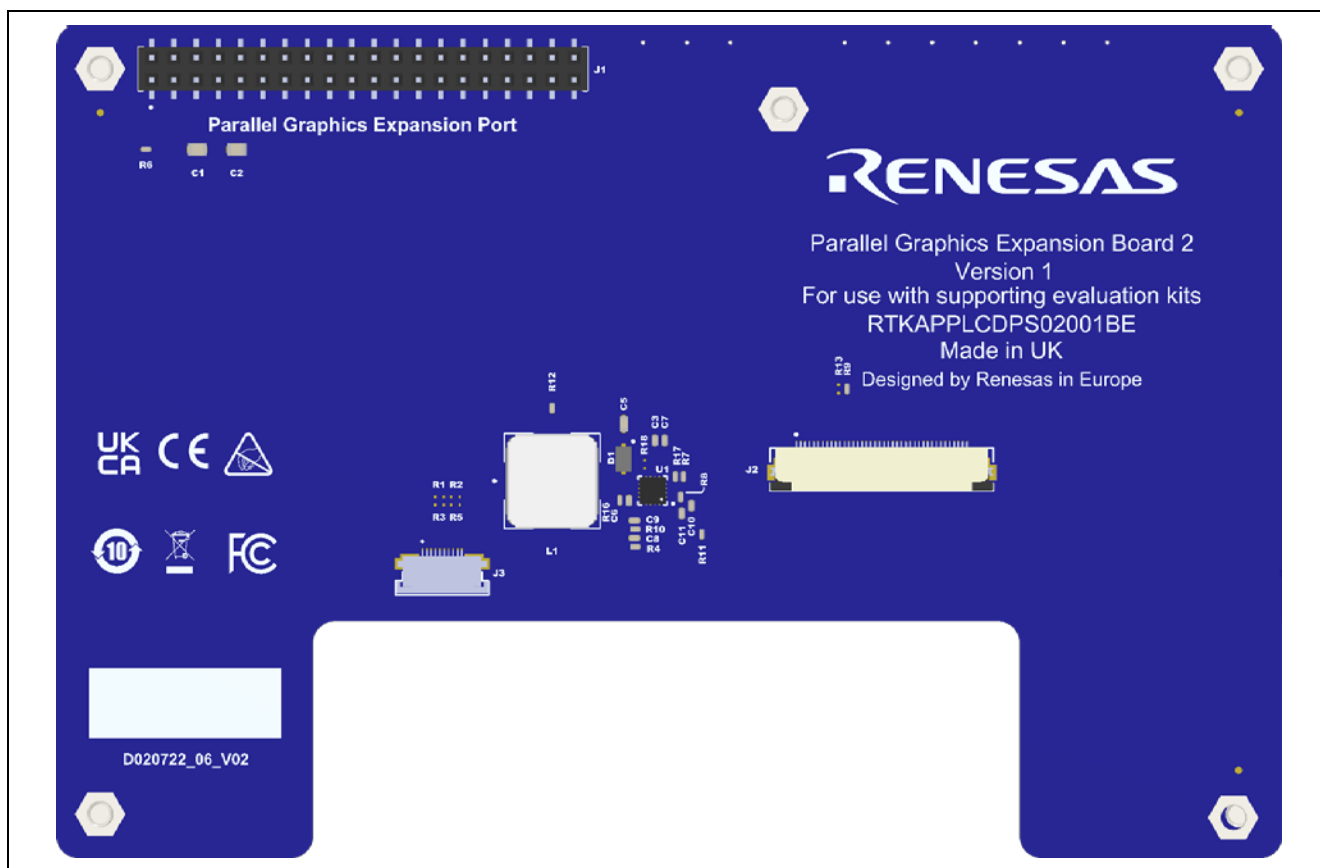


Figure 2. Parallel Graphics Expansion Board 2 Bottom Side

1.1 Assumptions and Advisory Notes

1. It is assumed that the user has a basic understanding of microcontrollers and embedded systems hardware.
2. Flexible Software Package (FSP) and an Integrated Development Environment (IDE) such as e² studio are required to develop embedded applications that apply to the Parallel Graphics Expansion Board 2.
3. It is assumed the user has an EK-RA8E2 (or similar MCU) to add LCD functionality to, this board cannot be programmed directly.
4. It is recommended that the user refers to the Quick Start Example Project that EK-RA8E2 board comes pre-programmed with.
5. Instructions to download and install software, import example projects, build them and program the EK-RA8E2 board are provided in the Quick Start Guide.

2. Kit Contents

The following components are included in the kit:

1. Parallel Graphics Expansion Board 2 fitted with an 800 × 480 pixel 5-Inch TFT Display
2. Display mounting hardware (spacers and fixing screws)

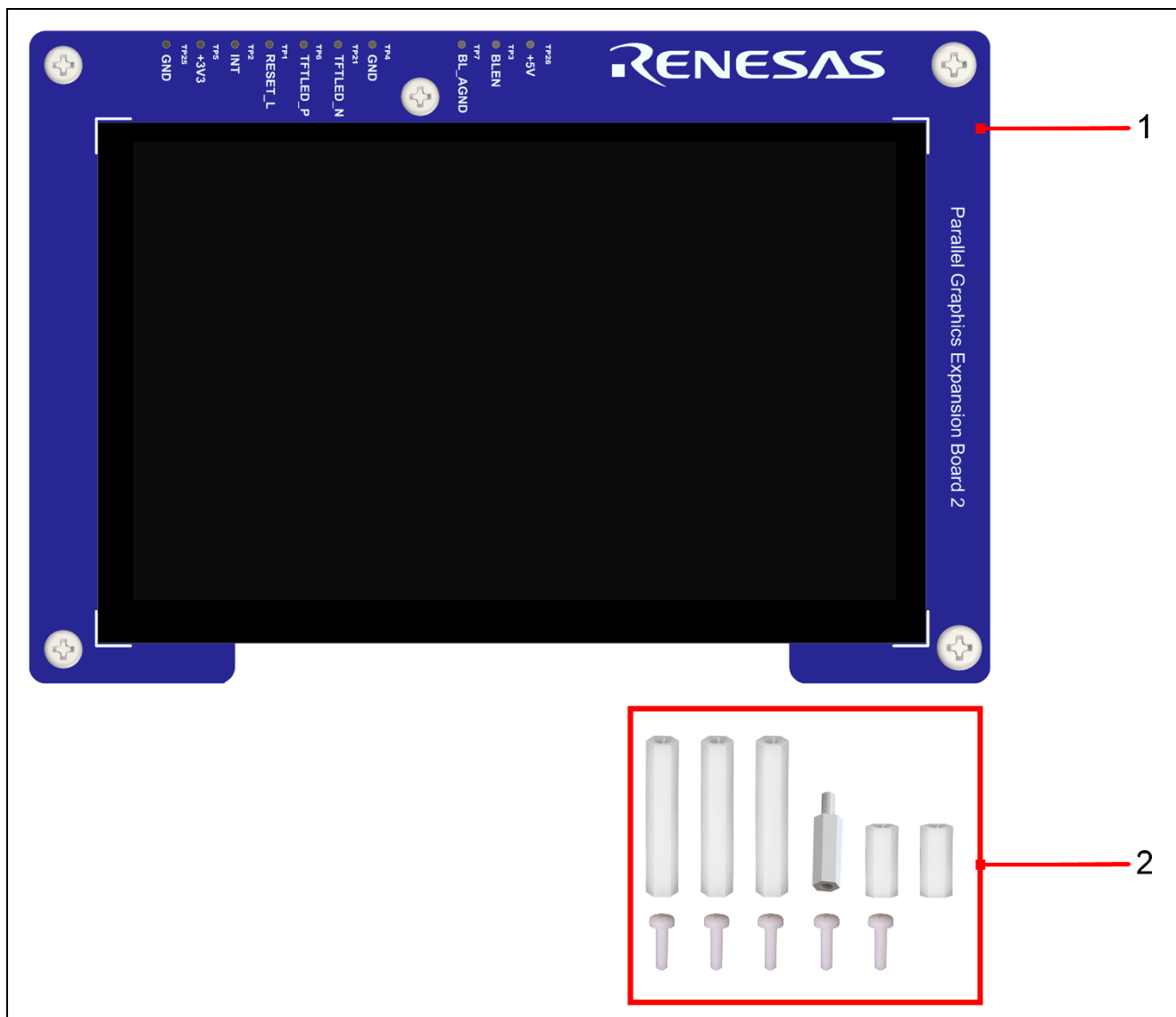


Figure 3. Parallel Graphics Expansion Board 2 Kit Contents

3. Ordering Information

- Parallel Graphics Expansion Board 2 v1 kit orderable part number: RTKAPPLCDPS02001BE

Note: The underlined character in the orderable part number represents the kit version.

Dimensions: 95mm (width) x 140mm (length)

4. Connectivity

4.1 Parallel Graphics Expansion Port (J1)

The Parallel Graphics Expansion Board 2 supports a 5-inch 800 x 480 TFT LCD with capacitive touchscreen to be connected to RA8 MCUs via the 40-pin Socket Parallel Graphics Expansion Port (J1).

Table 1. Parallel Graphics Expansion Port

J1 Pin	Signal Description	Test Point
1	SDA	-
2	BLEN	TP3
3	SCL	-
4	INT	TP2
5	RESET_L	TP1
6	P3V3	TP5
7	P5V	TP26
8	P3V3	TP5
9	CLK	-
10	VSYNC/TCON0	-
11	HSYNC/TCON1	-
12	DE/TCON2	-
13	STBYB/DISP	-
14	EXTCLK	-
15	DATA0/B0	-
16	DATA1/B1	-
17	DATA2/B2	-
18	DATA3/B3	-
19	DATA4/B4	-
20	DATA5/B5	-
21	DATA6/B6	-
22	DATA7/B7	-
23	DATA8/G0	-
24	DATA9/G1	-
25	DATA10/G2	-
26	DATA11/G3	-
27	DATA12/G4	-
28	DATA13/G5	-
29	DATA14/G6	-
30	DATA15/G7	-
31	DATA16/R0	-
32	DATA17/R1	-
33	DATA18/R2	-
34	DATA19/R3	-
35	DATA20/R4	-
36	DATA21/R5	-
37	DATA22/R6	-
38	DATA23/R7	-
39	GND	TP4 and TP25
40	GND	

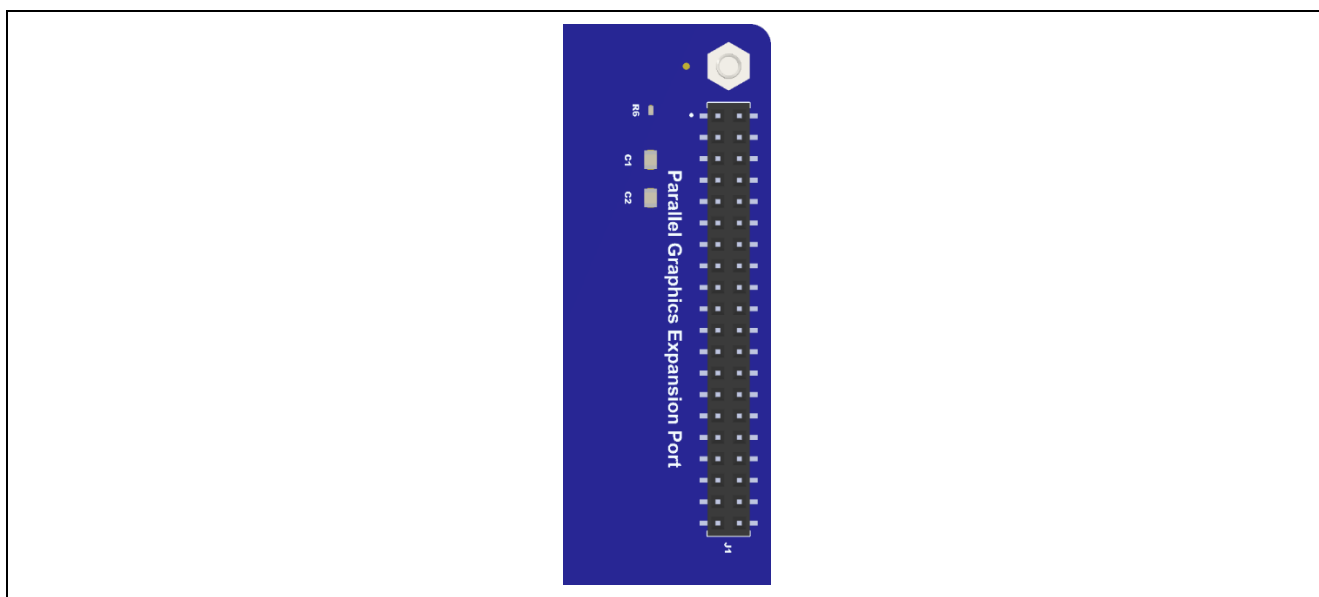


Figure 4. Parallel Graphics Expansion Port

4.2 FPC Connector for LCD Data (J2)

The TFT LCD peripheral, data and power lines connect to the Parallel Graphics Expansion Board 2 via the 40-pin FPC connector (J2).

Table 2. FPC Connector for LCD Data

J2 Pin	Signal Description	LCD Panel Pin	LCD Signal Description	Test Point
40	LEDK / TFTLED_N	1	VLED-	TP6
39	LEDA / TFTLED_P	2	VLED+	TP21
38	GND	3	GND	TP4 and TP25
37	P3V3	4	VDD	TP5
36	DATA16/R0	5	R0	-
35	DATA17/R1	6	R1	-
34	DATA18/R2	7	R2	-
33	DATA19/R3	8	R3	-
32	DATA20/R4	9	R4	-
31	DATA21/R5	10	R5	-
30	DATA22/R6	11	R6	-
29	DATA23/R7	12	R7	-
28	DATA8/G0	13	G0	-
27	DATA9/G1	14	G1	-
26	DATA10/G2	15	G2	-
25	DATA11/G3	16	G3	-
24	DATA12/G4	17	G4	-
23	DATA13/G5	18	G5	-
22	DATA14/G6	19	G6	-
21	DATA15/G7	20	G7	-
20	DATA0/B0	21	B0	-
19	DATA1/B1	22	B1	-
18	DATA2/B2	23	B2	-
17	DATA3/B3	24	B3	-
16	DATA4/B4	25	B4	-
15	DATA5/B5	26	B5	-
14	DATA6/B6	27	B6	-
13	DATA7/B7	28	B7	-
12	GND	29	GND	TP4 and TP25
11	EXTCLK / CLK	30	DCLK	-
10	STBYB/DISP	31	STBYB/DISP	-
9	HSYNC/TCON1	32	HSYN	-
8	VSYSN/TCON0	33	VSYN	-
7	DE/TCON2	34	DE	-
6	NC	35	NC	-
5	GND	36	GND	TP4 and TP25
4	NC	37	NC (XR)	-
3	NC	38	NC (YD)	-
2	NC	39	NC (XL)	-
1	NC	40	NC (YU)	-

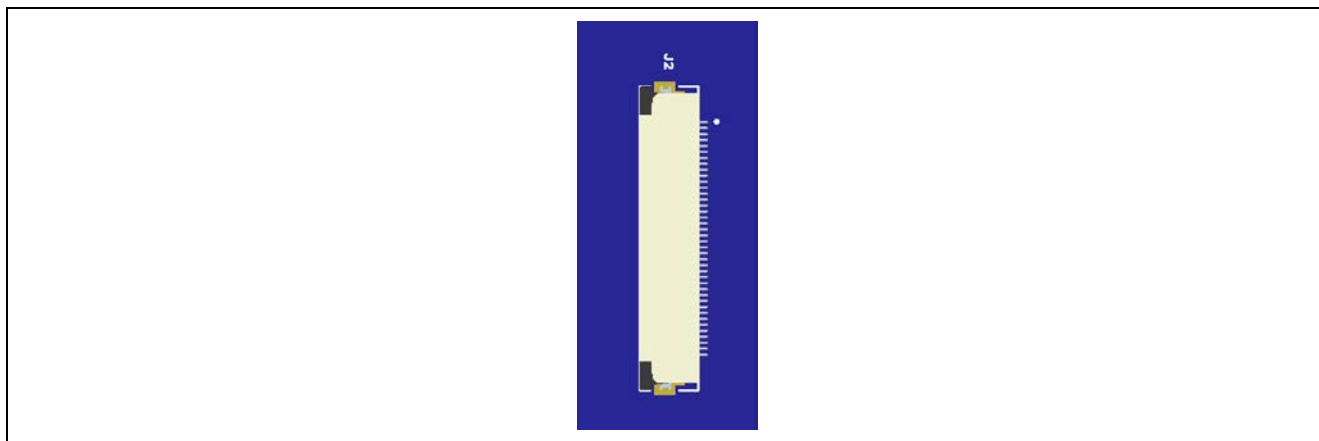


Figure 5. FPC Connector for LCD Data

4.3 FPC Connector for LCD Capacitive Touch (J3)

The TFT LCD capacitive touch panel connects to the Parallel Graphics Expansion Board 2 via the 10-pin FPC connector J3. The capacitive touch controller (GT911) is controlled using I²C. If INT is held low on power up or reset, the I²C address is 0x5D (default). If INT is held high on power up or reset the I²C address is 0x14.

Table 3. FPC Connector for LCD Capacitive Touch

J3 Pin	Signal Description	LCD Panel Description	Test Point
1	GND	VSS	TP4 and TP25
2	P3V3	VDD	TP5
3	SCL	SCL	-
4	GND	VSS	TP4 and TP25
5	SDA	SDA	-
6	GND	VSS	TP4 and TP25
7	RESET_L	/RST	TP1
8	NC	NC	-
9	INT	INT	TP2
10	GND	VSS	TP4 and TP25

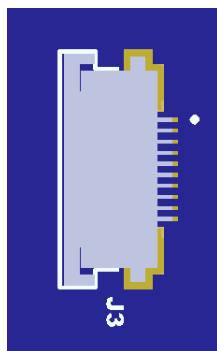


Figure 6. FPC Connector for LCD Capacitive Touch

4.4 LCD Backlight Controller (U1)

The display backlight is controlled by a Renesas ISL97682 backlight controller U1. This device implements compact 2 channel LED drivers each set to drive a constant current of 20mA. These are in parallel to provide a total of 40mA to the display backlight. The over voltage protection (OVP) pin is set to 19.5V max (18.6V nominal) to protect the LED backlight. Brightness can be controlled by applying a PWM signal to the backlight enable (BLEN) pin.

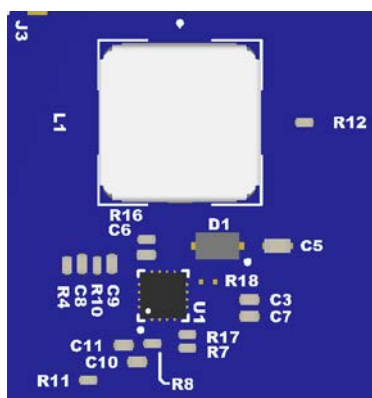


Figure 7. LCD Backlight Controller

5. Certifications

The Parallel Graphics Expansion Board 2 v1 kit meets the following certifications/standards. See page 3 of this user's manual for the disclaimer and precautions.

5.1 EMC/EMI Standards

- FCC Notice (Class A)



This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

NOTE- This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/television technician for help.

- Innovation, Science and Economic Development Canada ICES-003 Compliance:

CAN ICES-3 (A)/NMB-3(A)

- CE Class A (EMC)



This product is herewith confirmed to comply with the requirements set out in the Council Directives on the Approximation of the laws of the Member States relating to Electromagnetic Compatibility Directive 2014/30/EU.

Warning – This is a Class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures to correct this interference.

- UKCA Class A (EMC)



This product is in conformity with the following relevant UK Statutory Instrument(s) (and its amendments): 2016 No. 1091 Electromagnetic Compatibility Regulations 2016.

Warning – This is a Class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures to correct this interference.

- Taiwan: Chinese National Standard 13438, C6357 compliance, Class A limits
- Australia/New Zealand AS/NZS CISPR 32:2015, Class A

5.2 Material Selection, Waste, Recycling and Disposal Standards

- EU RoHS
- WEEE
- China SJ/T 113642014, 10-year environmental protection use period.

5.3 Safety Standards

- UL 94V-0

6. Design and Manufacturing Information

The design and manufacturing information for the Parallel Graphics Expansion Board 2 v1 kit is available in the “APP_LCD-EK_PAR_2 Design Package” available on [renesas.com/app_lcd-ek_par_2](https://www.renesas.com/app_lcd-ek_par_2).

Design package file name: app_lcd-ek_par_2-v1-designpackage.zip

Table 4. Parallel Graphics Expansion Board 2 Design Package Contents

File Type	Content	File/Folder Name
File (PDF)	Schematics	app_lcd-ek_par_2-v1-schematics
File (PDF)	Mechanical Drawing	app_lcd-ek_par_2-v1-mechdwg
File (PDF)	3D Drawing	app_lcd-ek_par_2-v1-3d
File (PDF)	BOM	app_lcd-ek_par_2-v1-bom
Folder	Manufacturing Files	Manufacturing Files
Folder	Design Files	Design Files - Altium

7. Website and Support

Visit the following URLs to learn about the kit and the RA8 family of microcontrollers, download tools and documentation, and get support.

Parallel Graphics Expansion Board 2 Resources	renesas.com/app_lcd-ek_par_2
RA Kit Information	renesas.com/ra/kits
RA Product Information	renesas.com/ra
RA Product Support Forum	renesas.com/ra/forum
RA Videos	renesas.com/ra/videos
Renesas Support	renesas.com/support
RA Flexible Software Package (FSP)	renesas.com/fsp

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Jun.12.25	—	Initial release

Parallel Graphics Expansion Board 2 v1 – User's Manual

Publication Date: Jun.12.25

Published by: Renesas Electronics Corporation

Parallel Graphics Expansion Board 2 v1 – User's Manual