

RX74M Group

MCK-RX74M Evaluation Kit Manual

Renesas RX Family
RX700 Series

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General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

For Your Safety

Be sure to read the precautions and instructions in this manual before using this product.

Meaning of Notations

In this manual, items related to the safe use of the product are indicated as described below.

The degree of injury to persons or damage to property that could result if the designated content in this manual is not followed is indicated as follows.

 Danger	Indicates content that, if not followed, could result in death or serious injury to the user, and which is highly urgent.
 Warning	Indicates content that, if not followed, could result in death or serious injury to the user.
 Caution	Indicates content that, if not followed, could result in injury to persons or physical damage.

Warnings Regarding Use of the Product

■ Danger Items

 Danger	
  	• The product should be used only by persons (users) having a thorough knowledge of electrical and mechanical components and systems, a full knowledge of the risks associated with handling them, and training in inverter motor control and handling motors, or equivalent skills. Users should be limited to persons who have carefully read the Caution Items contained in this manual. • The product contains high-temperature components that could be dangerous. Do not touch the product or cables while power is being supplied. • Carefully check to make sure that there are no pieces of conductive materials or dust adhering to the board, connectors, and cables. • Do not touch the motor while power is being supplied. • Ensure that the motor is insulated and placed in a stable location before supplying power.

■ Warning Items

 Warning	
	Always insert plugs, connectors, and cables securely, and confirm that they are fully inserted. Incomplete connections could cause fire, burns, electric shock, or injury.
	Use the power supply apparatus specified in the manual. Failure to do so could cause fire, burns, electric shock, injury, or malfunction.
	Disconnect the power supply and unplug all cables when the system will not be used for a period of time or when moving the system. Failure to do so could cause fire, burns, electric shock, or malfunction. This will protect the system against damage due to lightning.
	Use a mechanism (switch, outlet, etc.) located within reach to turn off (disconnect) the power supply. In case of emergency, it may be necessary to cut off the power supply quickly.
	In this product, the ground of the main power supply circuit is connected to the ground of the CPU board. When the user evaluates the product, be aware that the product and the measuring instruments may be damaged depending on the connection method of the measuring instruments.
	Turn off the power supply immediately if you notice abnormal odor, smoke, abnormal sound, or overheating. Continuing to use the system in an abnormal condition could cause fire, burns, or electric shock.
	Do Not Disassemble, Modify, or Repair! Doing so could cause fire, burns, electric shock, injury, or malfunction.
	Do not use the product for any purpose other than initial evaluation of motor control in a testing room or lab. Do not integrate the product or any part of it into other equipment. Do not insert or remove cables or connectors when the product is powered on. The product has no safety case. The user must cover the product for safety protection. Failure to observe the above could cause fire, electric shock, burns, or malfunction. The product may not perform as expected if used for other than its intended purpose.

■ Caution Items

 Caution	
	Caution – Hot! The motor gets hot. Touching it could cause high-temperature burns.
	Follow the procedure specified in the manual when powering the system on or off. Failure to do so could cause overheating or malfunction.
	Caution – Static Electricity Use the antistatic band. Failure to do so could cause malfunction or unstable motion.

Renesas RX Family

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1. Overview

MCK-RX74M is a motor control evaluation kit. By using this product, motor control with MCK-RX74M can be performed easily.

MCK-RX74M has characteristics shown below.

- (1) Supports Brushless DC motor.
- (2) Supports 1-/2-/3-shunt current sensing.
- (3) Supports Motor Control Development Support Tool.
- (4) Provides overcurrent protection function using overcurrent detection circuit.

1.1 Presupposition and precautions of this document

1. Experience of using tools: This document assumes that the user has used terminal emulation program of Integrated Development Environment (IDE) such as e²studio before.*¹
2. Knowledge about the development subject: This document assumes that the user has a basic knowledge to modify the sample project regarding MCU and embedded system.
3. Before using this product, wear an antistatic wrist strap. If you touch this product with static charge on your body, a device failure may occur, or operation may become unstable.
4. All screen shots provided in this document is for reference. Actual screen displays may differ depending on the software and development tool version which you use.
5. The onboard debugger circuit on this product does not have electric isolation. *¹ For evaluations of motor drive operation, please use the communication board (MC-COM) to connect to the PC and use the Renesas Motor Workbench development support tool. Because the communication board is equipped with an isolator, isolation between the PC and this product is ensured. Furthermore, Renesas Motor Workbench allows variables to be read and written without stopping the MCU, enabling safe evaluation and debugging of motor drive operation. *²

*¹ When using the onboard debugger, this product and the PC share a common GND. In that case, supplying external power to the inverter board or driving the motor may damage the board or the PC.

*² If the MCU is stopped by the debugger while the motor is running, the MOSFETs on the inverter board may remain ON (conducting state), which may result in damage to the motor or the inverter board.

2. Product Contents

This kit consists of the following parts.

1. Inverter Board (RTK0EM0000B12020BJ) x1
2. CPU Board (RTK0EMXFN0C00000BJ) x1
3. Communication board (RTK0EMXC90Z00000BJ) x1
4. Brushless DC Motor (R42BLD30L3) x1
5. Communication cable x1
6. USB Cable x1
7. Screw x12
8. Standoff x12

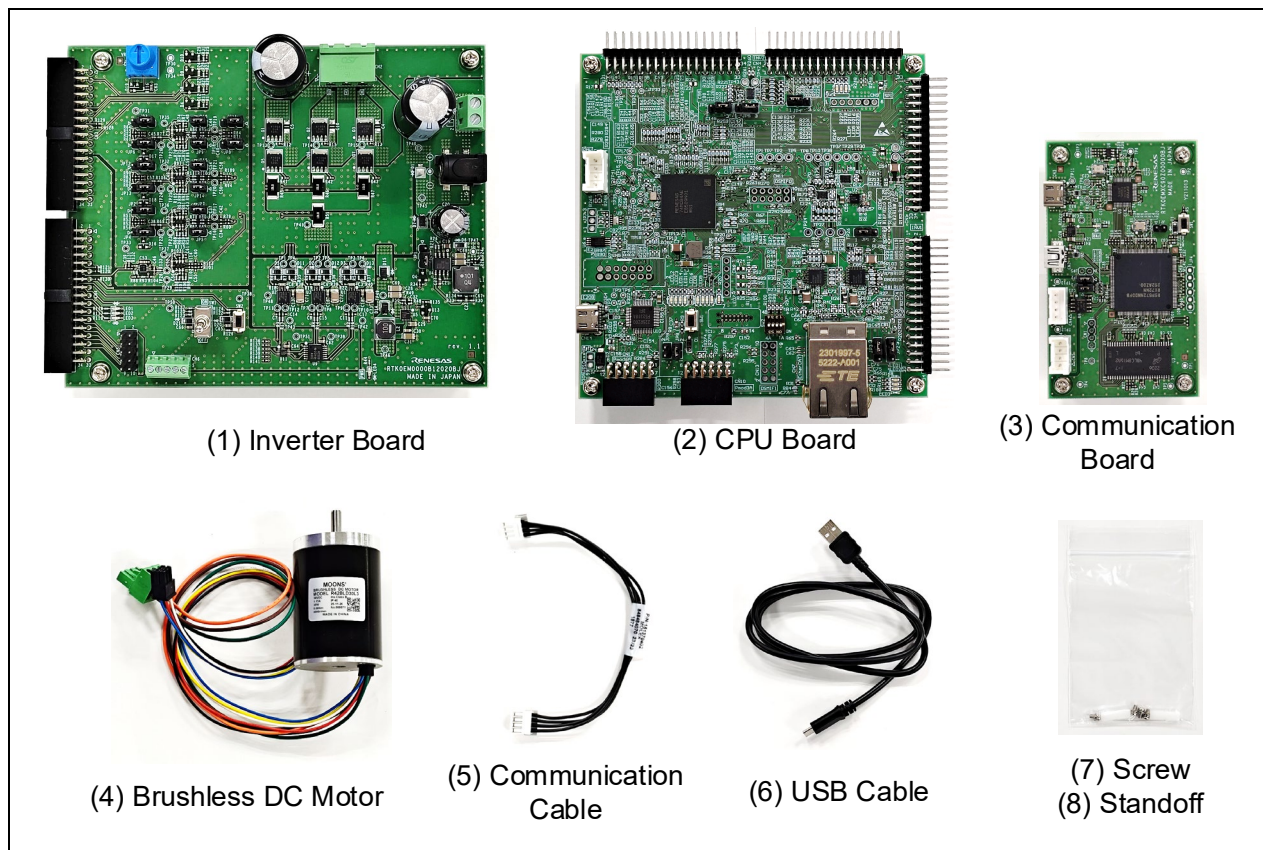


Figure 2-1 Product contents

3. Product Order Information

Product number to ordering this product	Included item
RTK0EMXFN0S00020BJ (MCK-RX74M)	CPU Board (RTK0EMXFN0C00000BJ) Inverter Board (RTK0EM0000B12020BJ) Brushless DC Motor (R42BLD30L3) Communication board (RTK0EMXC90Z00000BJ) Communication cable USB cable

Product number for ordering included item	Included item
RTK0EMXFN0C00000BJ (MCB-RX74M)	CPU Board (RTK0EMXFN0C00000BJ)
RTK0EM0000S04020BJ (MCI-LV-1)	Inverter Board (RTK0EM0000B12020BJ) Brushless DC Motor (R42BLD30L3)
RTK0EMXC90S00000BJ (MC-COM)	Communication board (RTK0EMXC90Z00000BJ) Communication cable USB cable

4. Hardware Configuration and Default Setting

4.1 Hardware configuration

MCK-RX74M consists of the inverter board, the CPU board and the communication board. Specifications as a kit and for the relevant boards are listed below.

Table 4-1 MCK-RX74M specification (1/4)

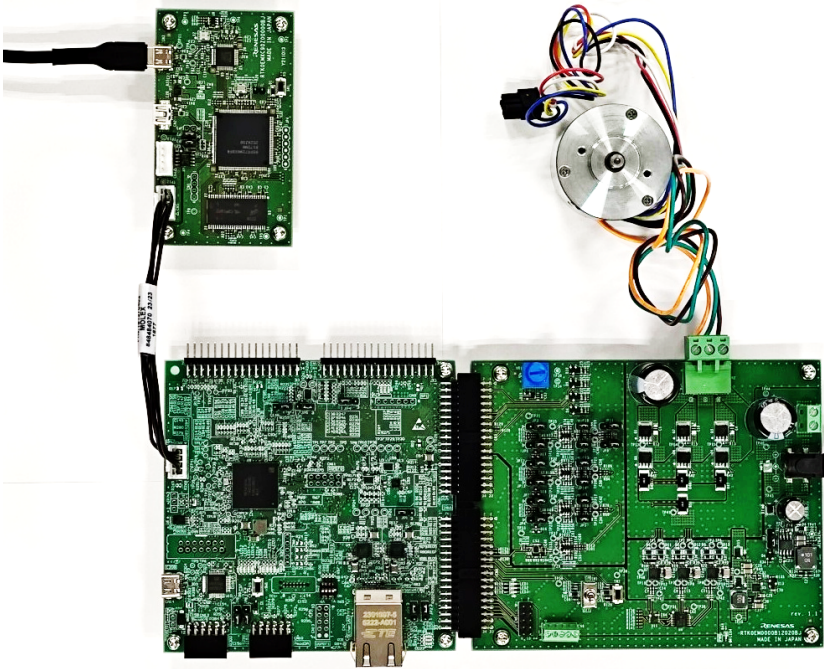
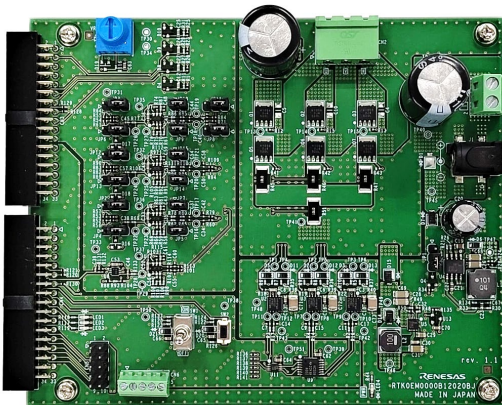
Item	Specification	
Kit product name	MCK-RX74M	
Kit product No.	RTK0EMXFN0S00020BJ	
Kit configuration	Inverter Board	RTK0EM0000B12020BJ
	CPU Board	RTK0EMXFN0C00000BJ
	Communication board	RTK0EMXC90Z00000BJ
	Brushless DC Motor	R42BLD30L3 (MOONS') Rated voltage : 36[V] Rated current : 1.13[A]
Isolation	Inverter board - CPU board : Non-isolated Communication board – CPU board : isolated (up to 1kV _{RMS})	
External view	 Note: The actual product may differ from this photo.	
Board size	Inverter board : 133mm (W) x 109mm (L) CPU board : 109mm (W) x 109mm (L) Communication board : 89mm(W) × 52mm (L)	
Operating temperature	Room temperature	
Operating humidity	No condensation allowed	
EMC Directive	EN61326-1:2021 EMI : Class A EMS : Industrial electromagnetic environment	

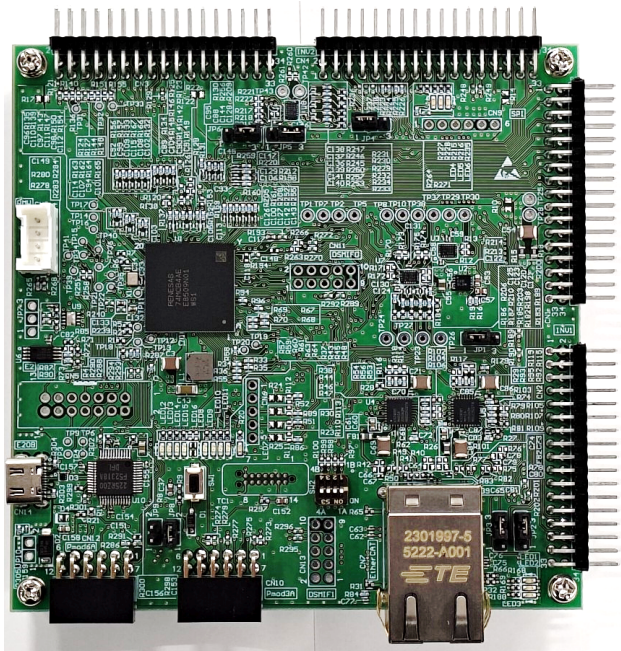
Table 4-2 MCK-RX74M specification (2/4)

Item	Specification
Product name	Inverter board
Board part No.	RTK0EM0000B12020BJ
External view	 Note: The actual product may differ from this photo.
Power supply	2 ways - From DC jack or Power supply connector (DC 12~48V) *1 - From CPU board (DC 5V)
Rated output current	AC 10A (RMS value) *2
Switching frequency	20kHz (typical)
Current sensing method	1- / 2- / 3-shunt sensing
Shunt resistor	10mΩ
PWM logic	High-side, Low-side : Positive logic
DC bus voltage detection (bus voltage detection)	Detection by resistance division (0 V ~ 48 V)
3-phase output voltage detection	Detection by resistance division (0 V ~ 48 V)
3-phase output current sensing	With shunt resistor
Overcurrent detection function	21.4A for AVCC = 3.3V 32.4A for AVCC = 5.0V
Supporting sensor	HALL sensor, Encoder, Inductive Position Sensors
Connector	- CPU card connector - Motor connector - Power input connector - HALL sensor connector - Encoder/Inductive position sensor connector
Switch	- Toggle switch x1 - Push switch x1
LED	- LED x3 - Power LED

*1 The polarity of the DC jack (J1) is center positive. The compatible plug has an inner diameter of 2.1 mm and an outer diameter of 5.5 mm.

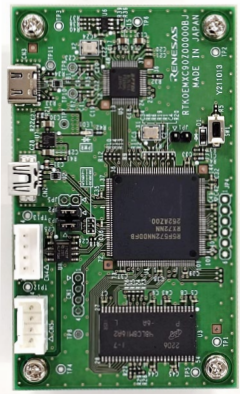
*2 It is strongly recommended to attach a heat sink to the MOSFET when using over 5A.

Table 4-3 MCK-RX74M specification (3/4)

item		Specification
Product name		CPU Board
Board part No.		RTK0EMXFN0C00000BJ
Compatible inverter board		RTK0EM0000B12020BJ
External view		 Note: The actual product may differ from this photo.
Mounted MCU	Product group	RX74M group
	Product No.	R5K574MCB4AE
	CPU maximum operating frequency	600MHz
	Bit count	32bit
	Package / Pin count	LFBGA / 320pin
	ROM / RAM	1MB / 1.5MB
MCU input clock		24MHz (Generate with external crystal oscillator)
Power supply		DC 5V Select one way automatically from the below - Power is supplied from compatible inverter board - Power is supplied from USB connector
Debugger		E2OB (Onboard debugger circuit)
Connector		- USB connector for E2OB and power supply (Type-C) - External emulator connector * - Inverter board connector (2 pair) - SCI connector (for Renesas Motor Workbench communication) - SPI connector * - Pmod 3A connector - Pmod 6A connector - CAN connector * - DSMIF connector (2 pair) * - EtherCAT connector / EtherCAT test connector *
Switch		Reset switch
LED		User controllable LED x6, Power LED x1, EtherCAT LED x9 (include DNF)

* Not implemented

Table 4-4 MCK-RX74M specification (4/4)

item		Specification
Product name		Communication Board
Board part No.		RTK0EMXC90Z00000BJ
External view		 Note: The actual product may differ from this photo.
Mounted MCU	Product group	RX72N group
	Product No.	R5F572NNDDFB
	CPU maximum operating frequency	240MHz
	Bit count	32bit
	Package / Pin count	LFQFP / 144pin
	RAM	1Mbyte
MCU input clock		20MHz (Generate with external crystal oscillator)
Power supply		DC 5V Power is supplied from USB connector
Connector		- USB Type-C connector for PC - SCI connector for CPU board - USB miniB connector (not available for users)
Isolation		- Between SCI connector and MCU - Up to 1kV_{RMS}
Switch		MCU external reset switch

4.2 Block diagram

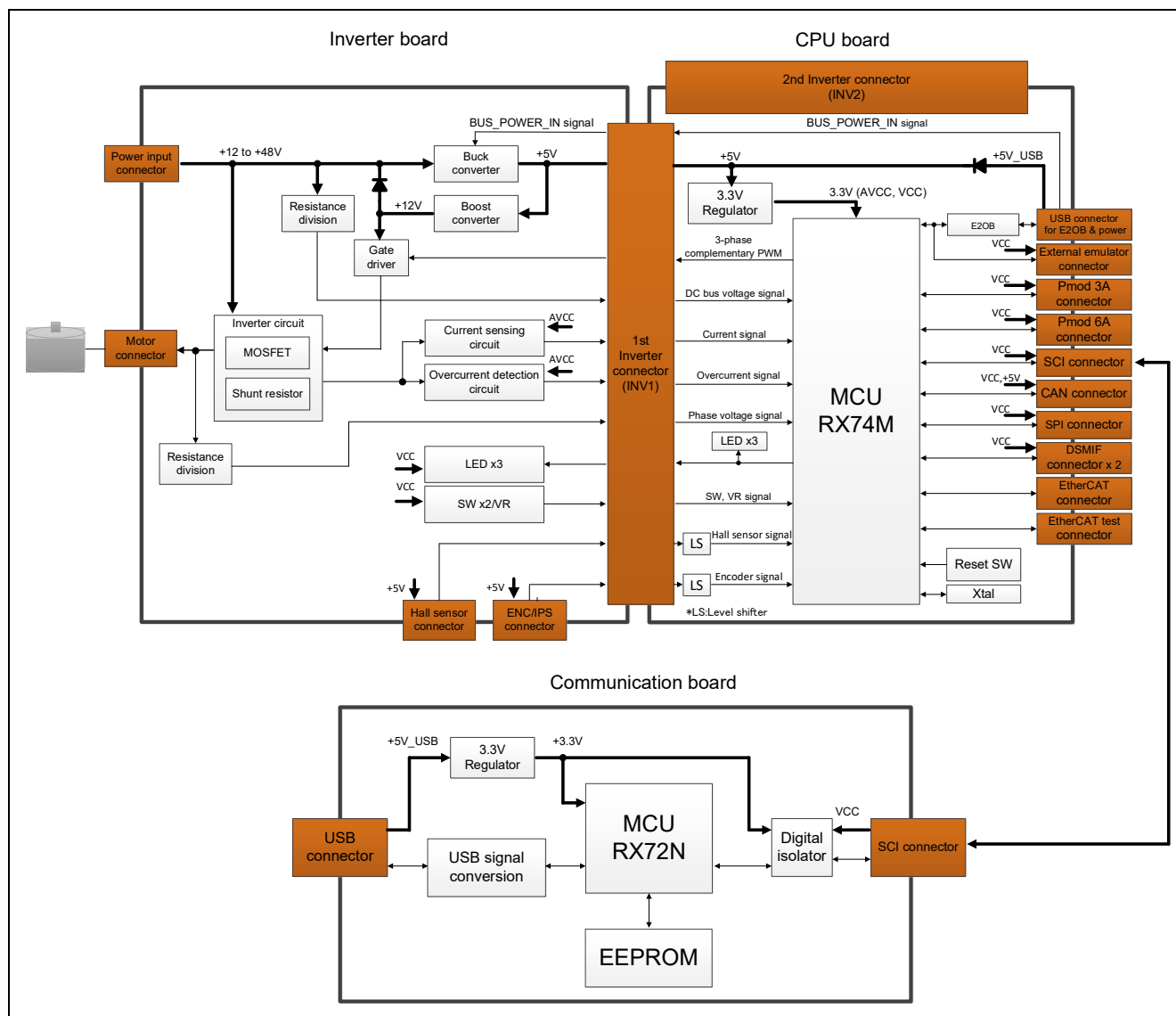


Figure 4-1 MCK-RX74M block diagram

4.3 Board Layout

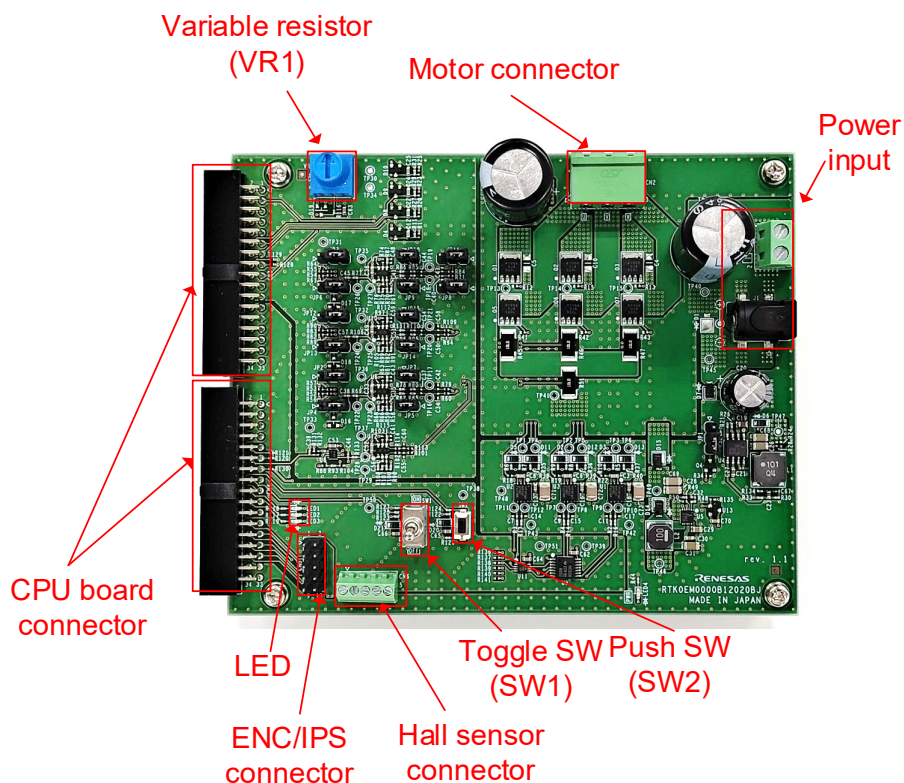


Figure 4-2 Inverter board Layout

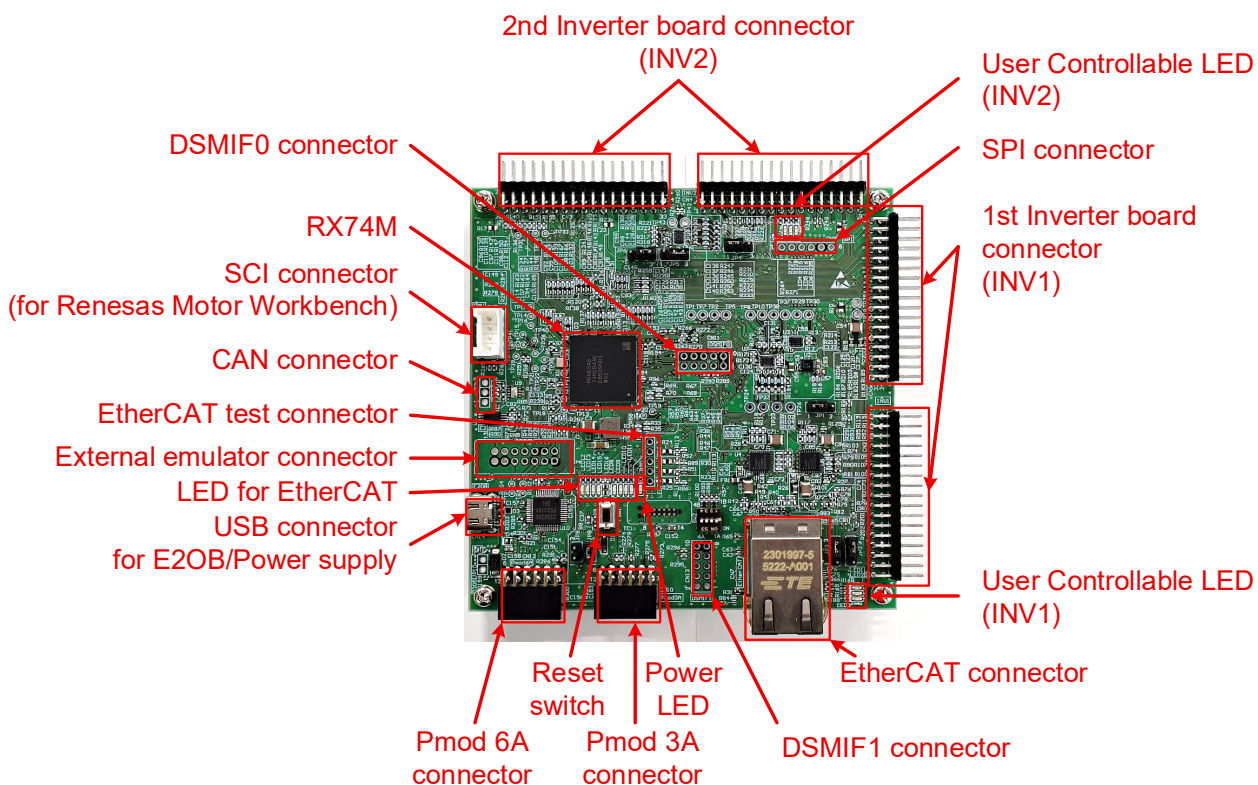


Figure 4-3 CPU Board Layout

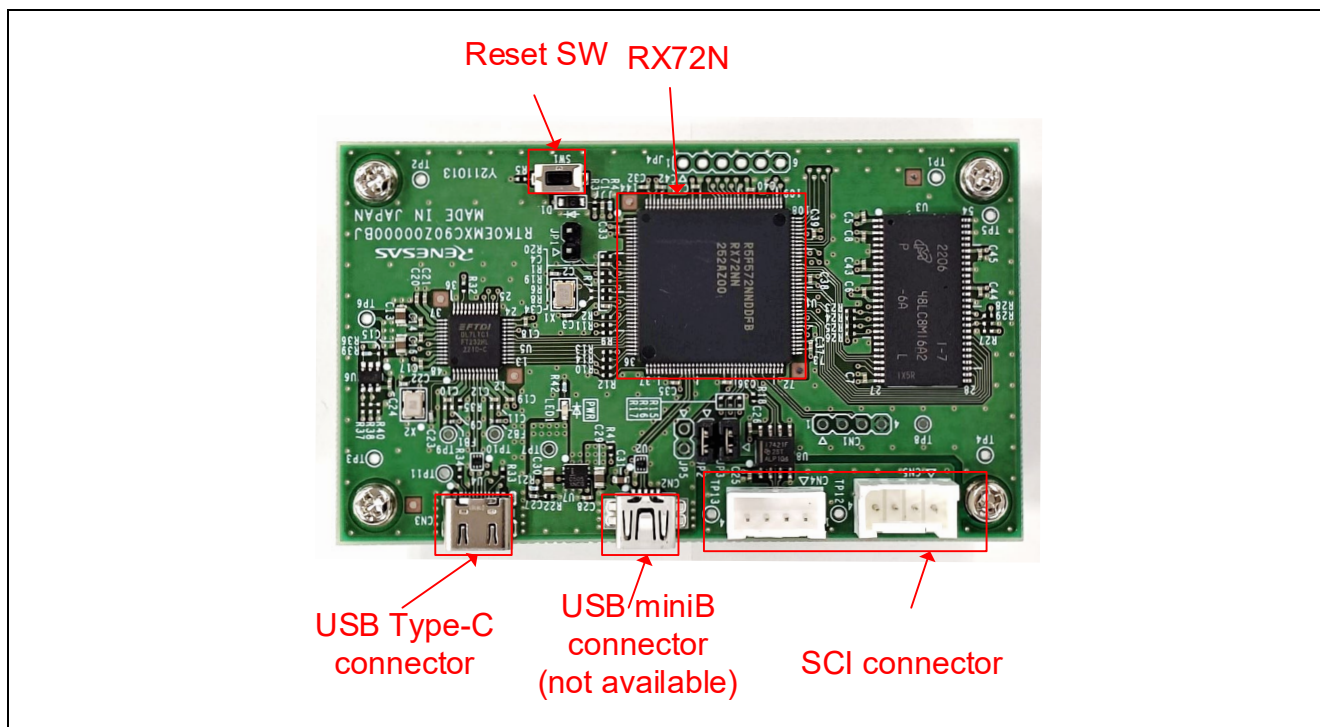


Figure 4-4 Communication board Layout

4.4 Standoffs and Screws

Before using this product, assemble the included standoffs and screws as shown below.

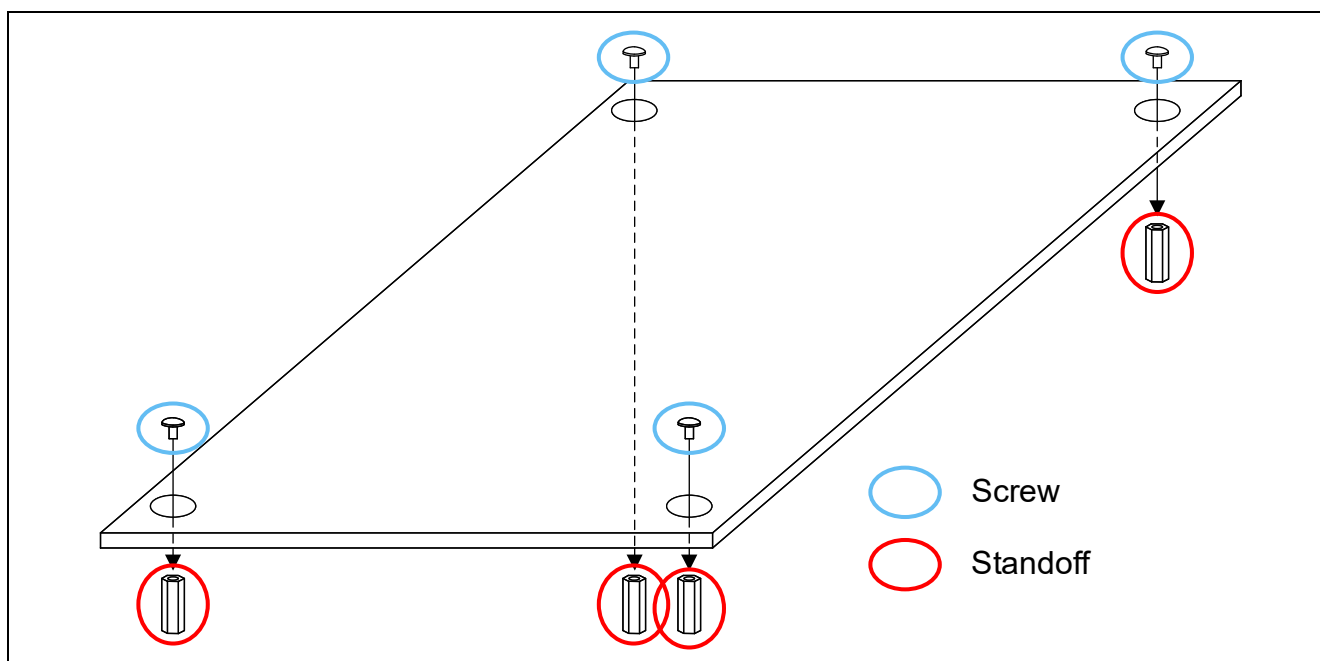


Figure 4-5 Standoffs and Screws assembly

4.5 Jumper pin setting

4.5.1 Inverter board

Default settings and functions of the jumper pins (JP1~JP15) are as follows.

Table 4-5 Jumper pin setting of Inverter board

JP No.	Default setting	Function
JP1	2-3pin short	1-2pin short : Disable 5V regulator 2-3pin short : Enable 5V regulator
JP2, JP3, JP4, JP6, JP12, JP13	2-3pin short	1-2pin short : Disable current sensing amplifiers 2-3pin short : Enable current sensing amplifiers
JP5, JP7, JP9, JP10, JP14, JP15	1-2pin short	1-2pin short : Current sensing amplifier gain = 20 1-2pin open : Current sensing amplifier gain = 10
JP8, JP11	1-2pin short	1-2pin short : 2-/3-shunt current sensing 2-3pin short : 1-shunt current sensing

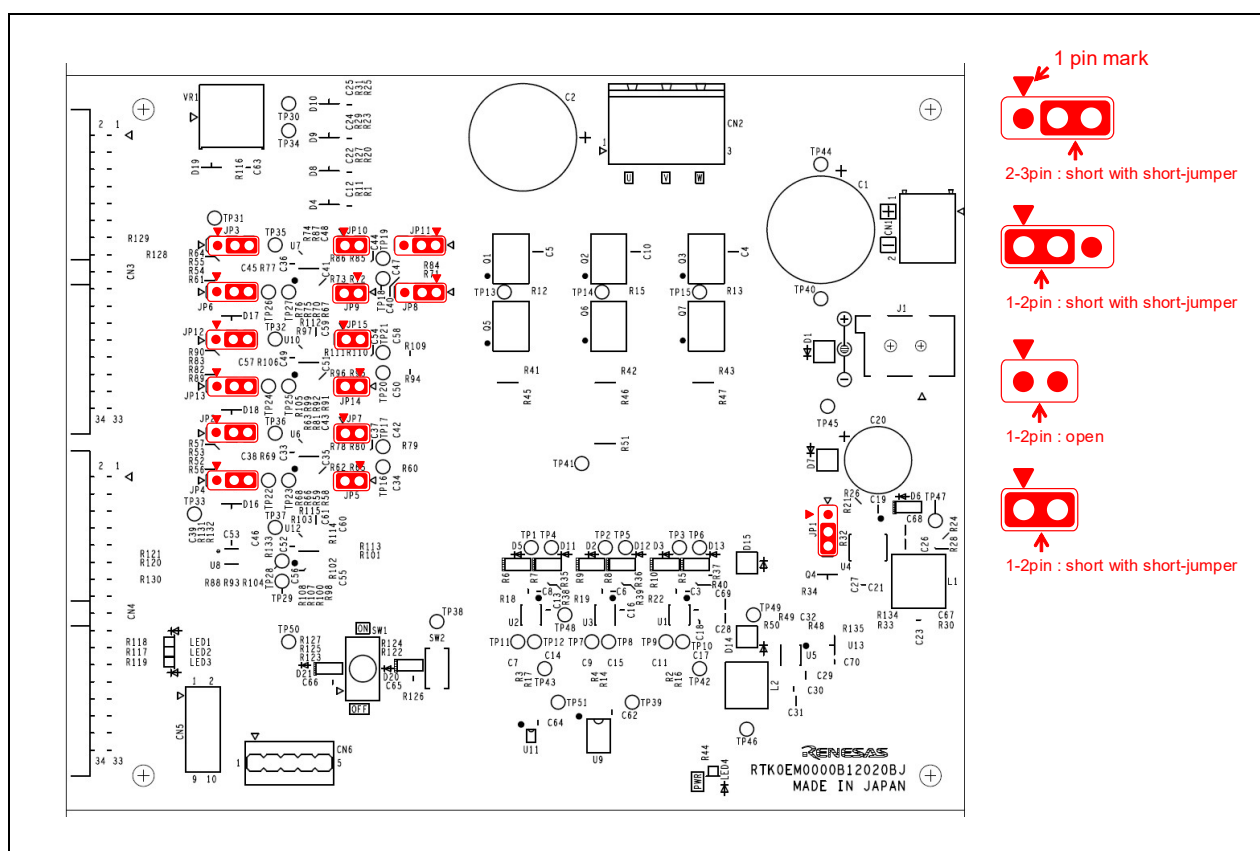


Figure 4-6 Default jumper pin setting of Inverter board

4.5.2 CPU board

Default settings and functions of the jumper pins are as follows.

Table 4-6 Jumper pins setting of CPU board

Jumper pin	Default setting	Function
JP1	1-2 pin short	1-2 pin short : INV1 PFC_PWM2 2-3 pin short : INV1 MOSI
JP2	2-3 pin short	1-2 pin short : INV1 IPS_A 2-3 pin short : INV1 ENC_A
JP3	2-3 pin short	1-2 pin short : INV1 IPS_B 2-3 pin short : INV1 ENC_B
JP4	1-2 pin short	1-2 pin short : INV2 PFC_PWM2 2-3 pin short : INV2 MOSI
JP5	2-3 pin short	1-2 pin short : INV2 IPS_A 2-3 pin short : INV2 ENC_A
JP6	2-3 pin short	1-2 pin short : INV2 IPS_B 2-3 pin short : INV2 ENC_B
JP8	1-2 pin short	1-2 pin open : On board debugger valid 1-2 pin short : On board debugger invalid *1

*1 For use in single-chip mode, or for use with emulators other than E2OB.

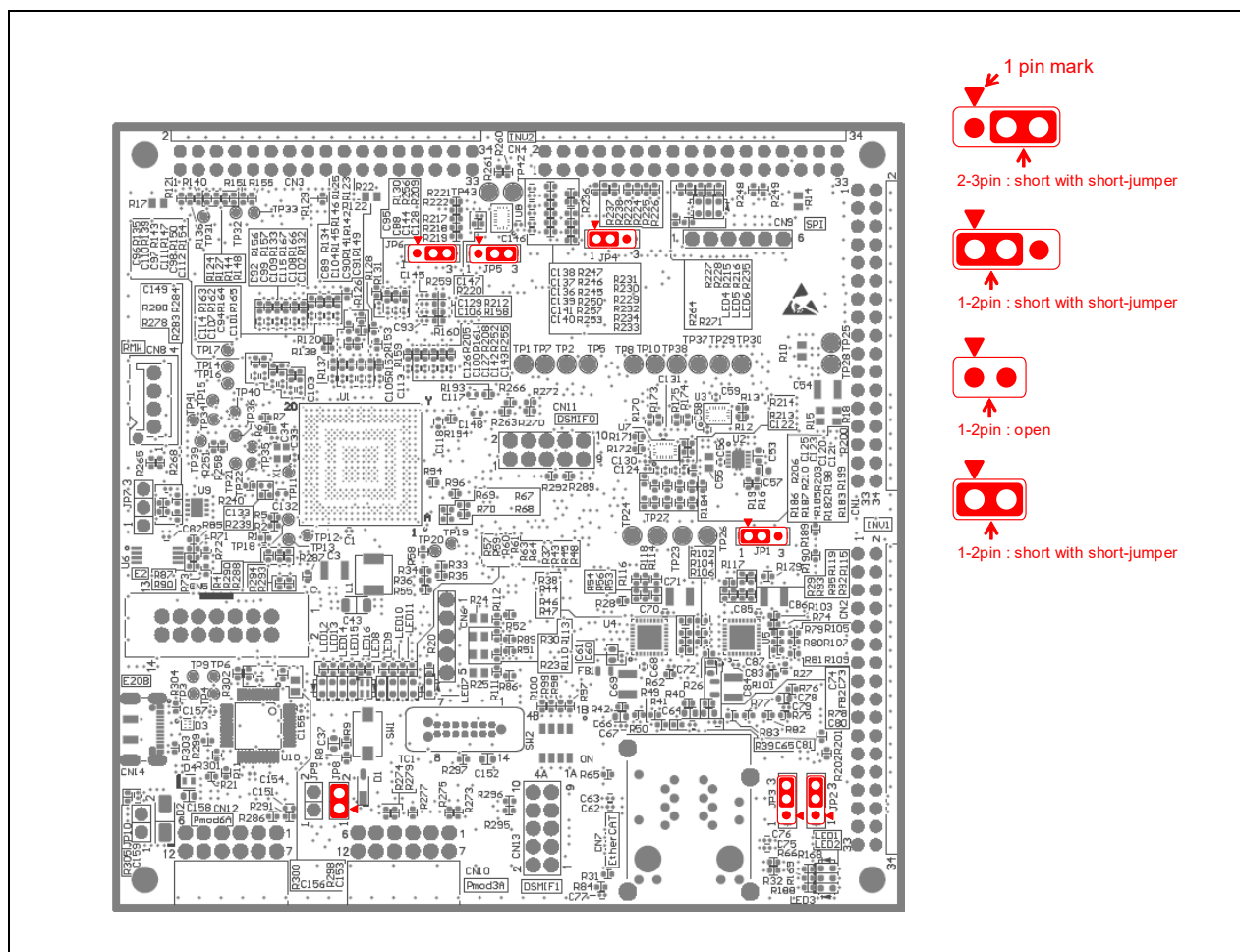


Figure 4-7 Default jumper pin setting of CPU board

4.5.3 Communication board

Default settings and functions of the jumper pins (JP1~JP3) are as follows.

Table 4-7 Jumper pin setting of Communication board

Jumper pin	Default setting	Function
JP1	1-2pin open	1-2pin short : Enable pull-up for MD port (Not available) 1-2pin open : Enable pull-up for MD port
JP2	1-2pin short	1-2pin short : Disable pull-up for GPIO (PC6) 1-2pin open : Enable pull-up for GPIO (PC6)
JP3	1-2pin short	1-2pin short : Disable pull-up for GPIO (PC5) 1-2pin open : Enable pull-up for GPIO (PC5)

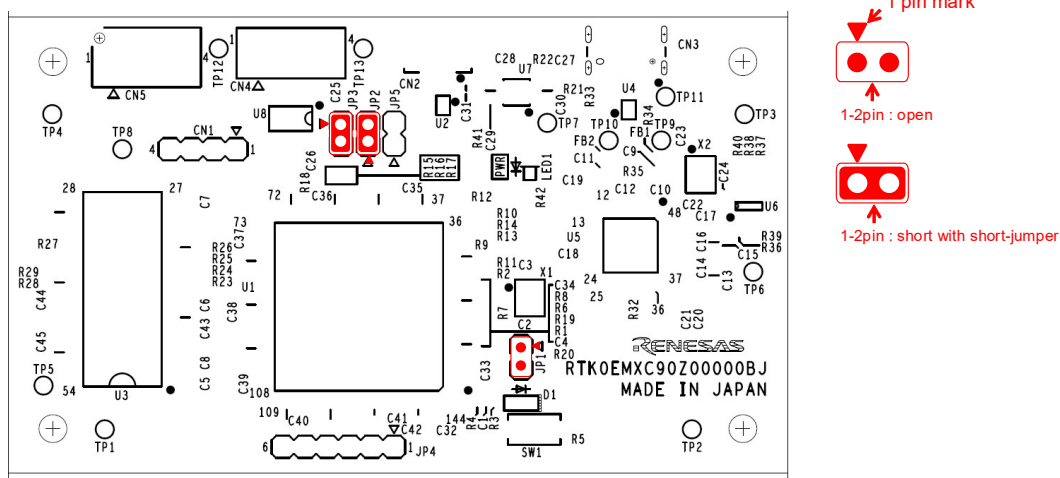


Figure 4-8 Default jumper pin setting of Communication board

4.6 Hardware Setup

4.6.1 Board Connection

When using this product for motor control evaluation, connect the boards as shown in Figure 4-9. Note that the connector between the CPU board and the inverter board is a tight fit, so be careful not to bend the pins when connecting or disconnecting.

Refer to 4.6.2 for the power supply method. In Figure 4-9 the power is supplied from the AC adaptor.

The RX74M CPU board supports a maximum of two-motor control and can be connected as shown in Figure 6-2 shown later, if you prepare an additional inverter board and motor.

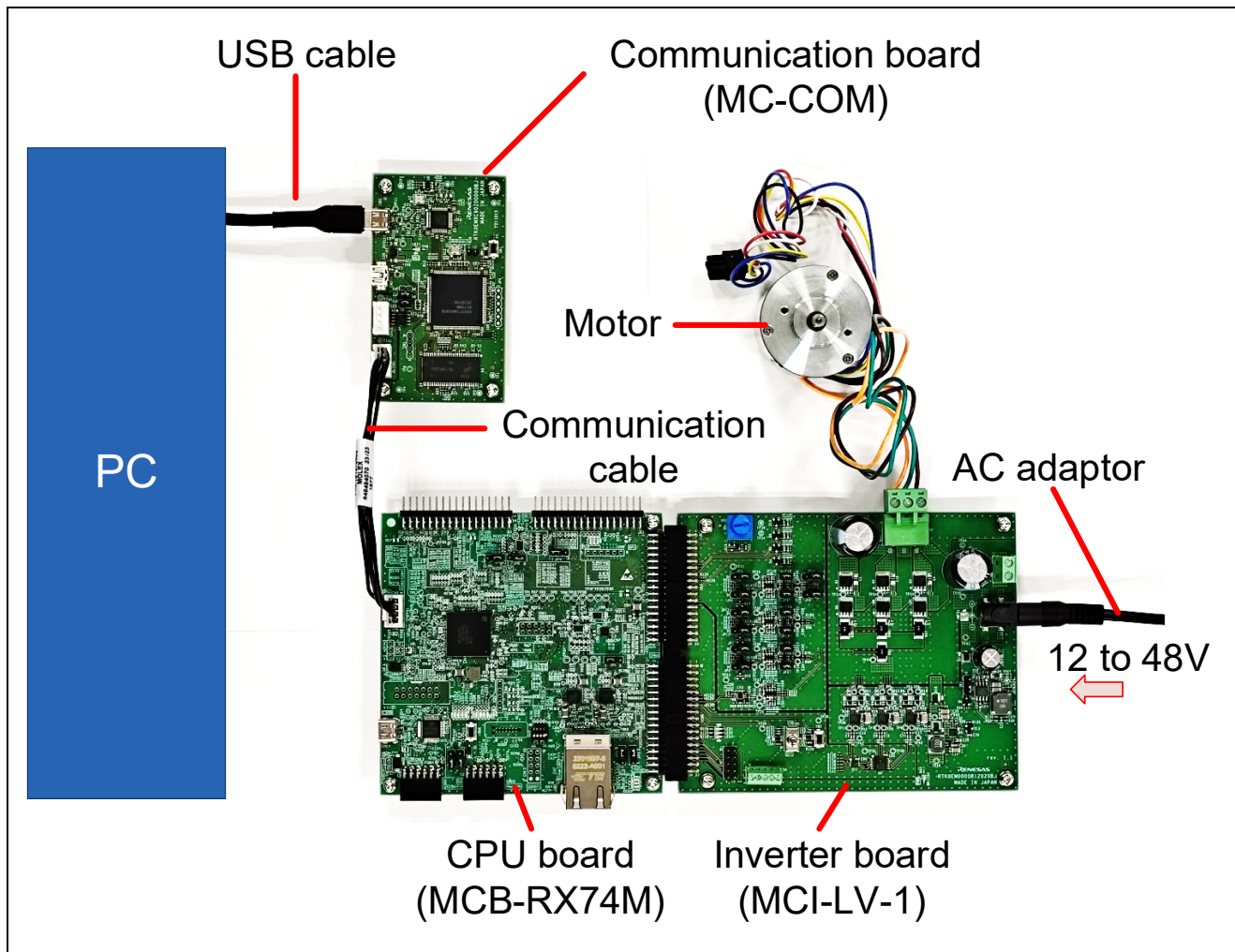


Figure 4-9 Board connection

4.6.2 Power Supply

There are three ways to supply power to the CPU board and inverter board, and the power supply for the communication board is independent of the CPU board and inverter board and is supplied at 5V from the USB connector.

(1) From DC jack

Use an AC adapter or something similar to supply power from the DC jack (J1) on the inverter board. The compatible plug has an outer diameter of 5.5 mm, an inner diameter of 2.1 mm, and a polarity of center positive. The input voltage range is 12 to 48V.

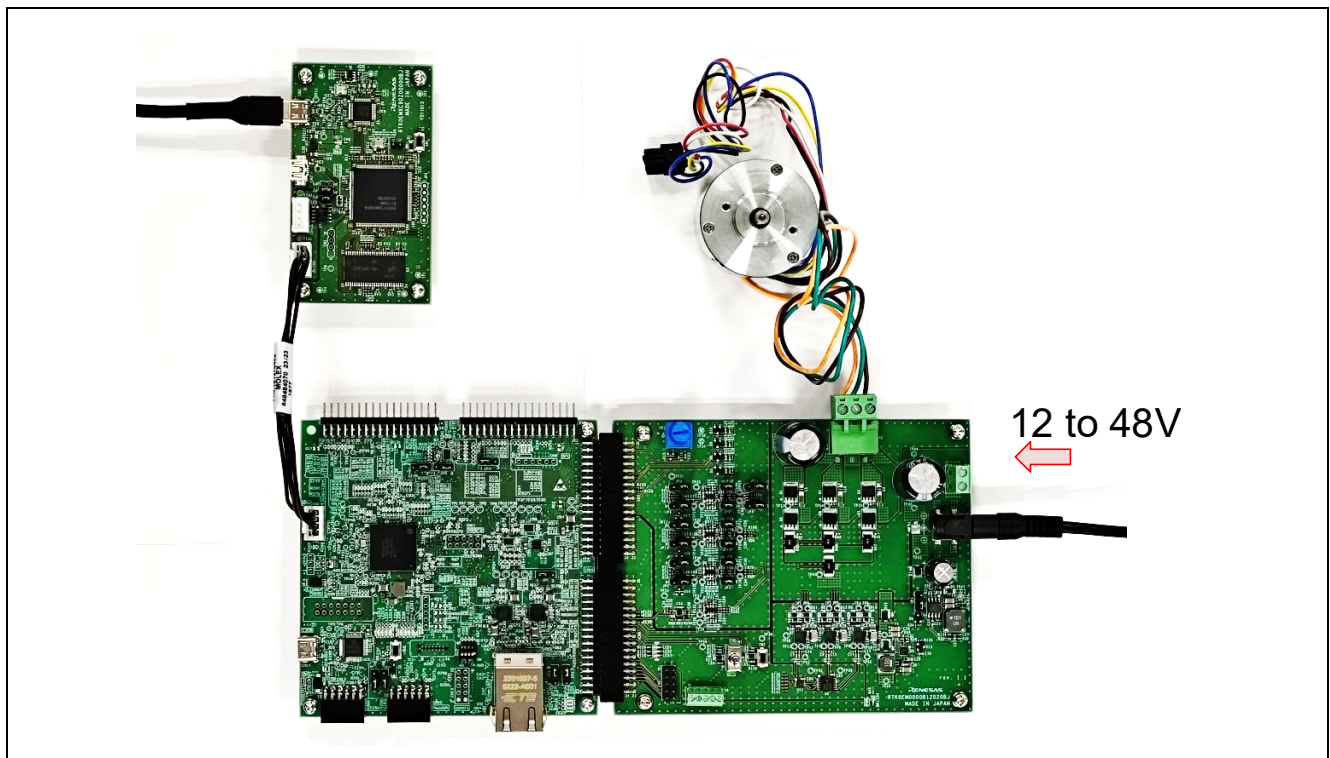


Figure 4-10 Power supply from DC jack

(2) From terminal block

Supply power from the terminal block (CN1) of the inverter board using a DC stabilized power supply or the like. The polarity should follow the silk indication (“+”, “-”) on the board. The input voltage range is 12 to 48V.

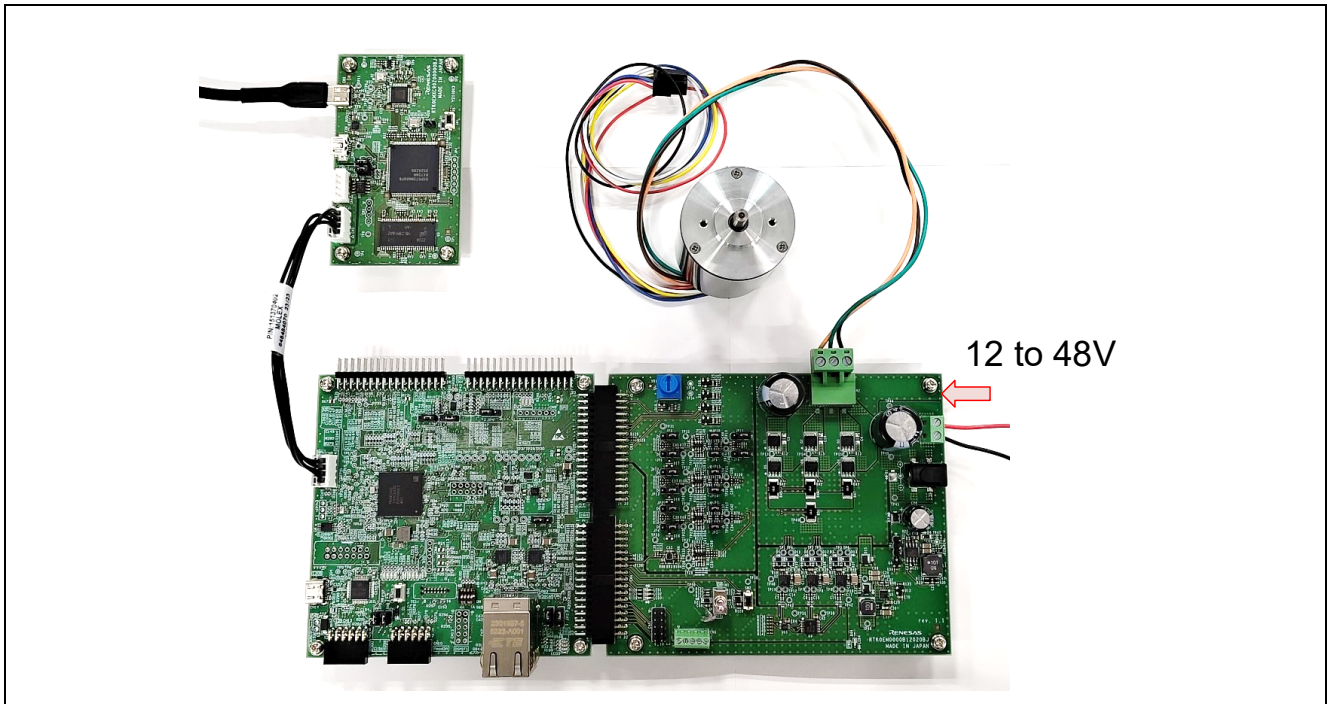


Figure 4-11 Power supply from terminal block

(3) From USB connector

5V power is supplied from the USB connector (Type-C) on the CPU board. Use a USB adapter capable of outputting 1A or more so that the motor can be driven sufficiently.

When supplying the power from USB port, use a USB adapter capable of providing at least 1A output to ensure sufficient motor drive capability.

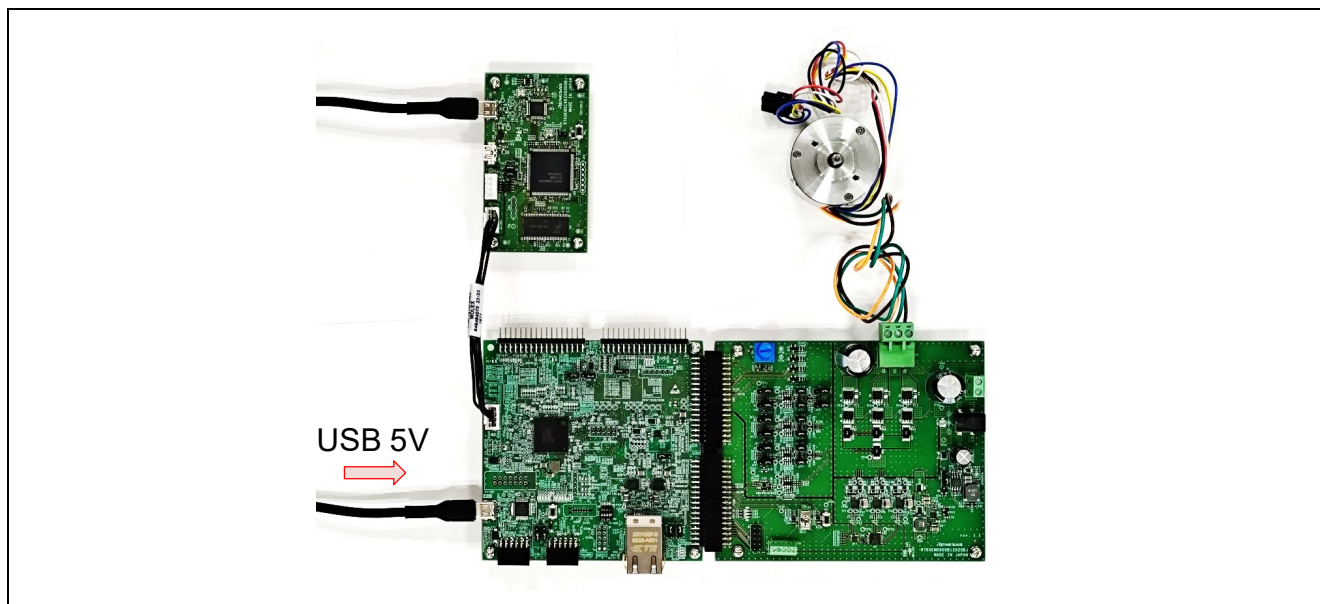


Figure 4-12 Power supply from USB connector

It is also possible to supply power from the USB connector on the CPU board and the DC jack or terminal block on the inverter board at the same time. In this case, the MCU drive voltage of 3.3V and the gate driver drive voltage are generated from the USB supply of 5V, while the motor drive voltage is supplied from the DC jack or terminal block. Refer to Table 4-8 for the power supply conditions and each voltage generation.

Table 4-8 Power supply and driving voltage generation

			Case 1	Case 2	Case 3
Power supply condition	CPU board	USB 5V *1	✓	–	✓
	Inverter board	External power (12~48V) *2	–	✓	✓
Power source for	CPU board	I/O (VCC/AVCC, 3.3V)	[A]	[B]	[A]
		BUS (+5V)	[A]	[B]	[A]
	Inverter board	I/O (VCC/AVCC, 3.3V)	[A]	[B]	[A]
		BUS (5V)	[A]	[B]	[A]
		Gate driver (11.4V)	[A]	[B]	[A]
		Motor drive (11.4V or 12~48V)	[A]	[B]	[B]

Power supply condition:

✓ : supplied

– : not supplied

Driving voltage generation

[A]: generated from USB 5V on CPU board

[B]: generated from external power on inverter board

*1 Motor drive current of 1 A or more may be required for each inverter board.

*2 When connecting two inverter boards to the CPU board and also inputting an external power supply to INV2, it is necessary to supply an external power supply (which can be different from INV2) to INV1 as well, or to supply USB 5V to the CPU board.

5. Inverter Board Specification

This section describes inverter board specification.

5.1 Functions

5.1.1 Inverter control circuit block

The inverter board has the inverter control circuit block which controls the motor with 6 POWER MOSFETs. POWER MOSFET is controlled with 6-phase timer output of MCU.

The inverter control circuit block outputs DC bus voltage, U, V and W phase voltage and shunt current to the connectors (CN3, CN4). By inputting these output voltages to A/D of MCU on the CPU card, analog values of the voltage and the shunt current of each phase can be measured. Refer to

5.1.2 for the current sensing and refer to 5.1.4 for the voltage detection, respectively. Also function to detect overcurrent from the input current is available. Refer to 5.1.3 for details.

An illustration of the inverter control circuit block is shown in Figure 5-1. In the actual circuit, some inputs on the A/D pins are via voltage dividers and offsets and so on. Refer to the circuit diagram for details.

If the gate drive PWM signals output from the MCU enter a high-impedance (Hi-Z) state, such as during reset or low-voltage conditions, all gate driver outputs become LOW, and all MOSFETs remain in the OFF state.

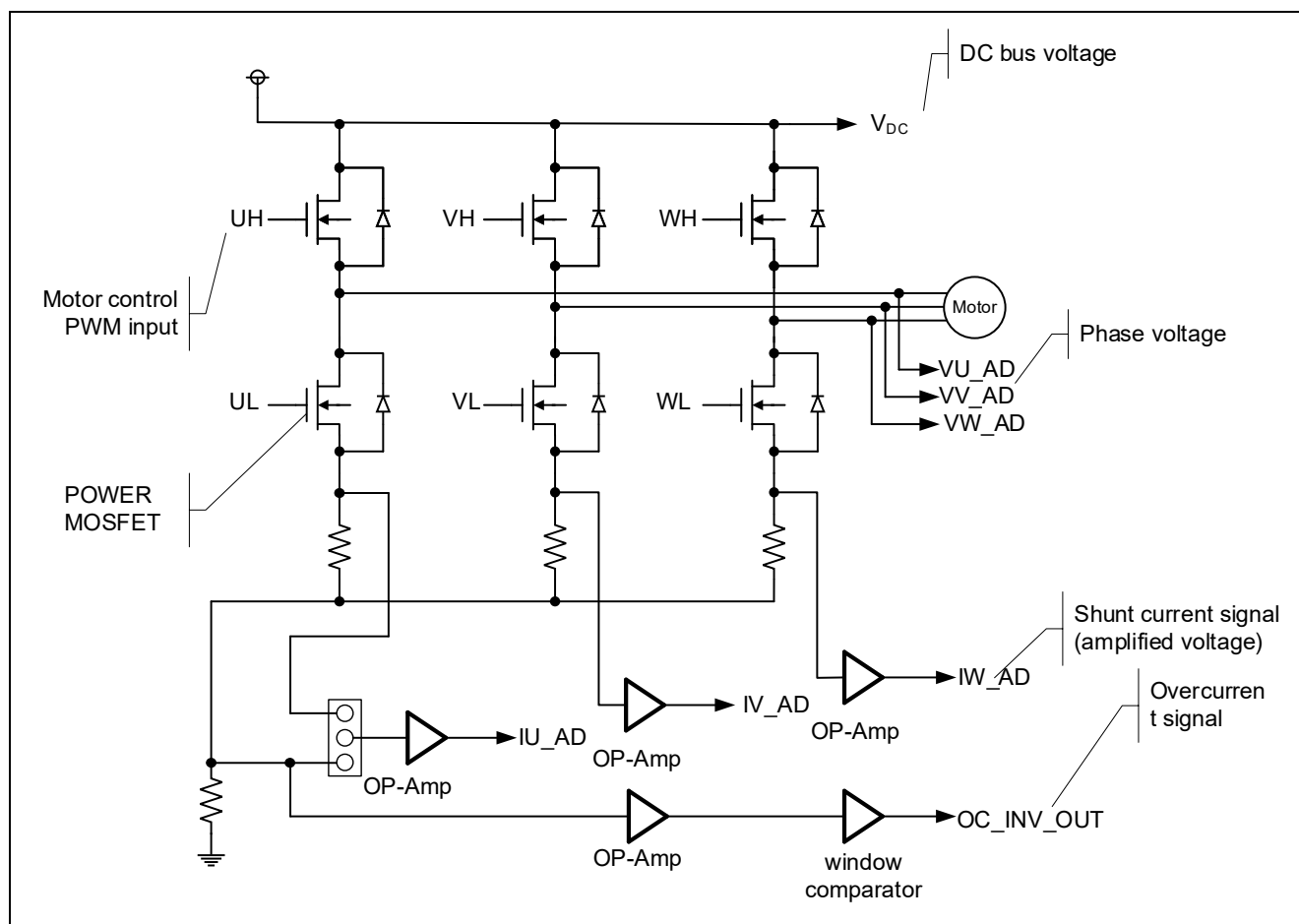


Figure 5-1 Illustration of inverter control circuit block

5.1.2 Current sensing circuit

The inverter board has the current sensing circuit to measure the current at the U, V and W phase. The current sensing circuit uses shunt resistor at each phase. Voltage drop caused by the current flowing through the shunt resistor is amplified by the current sensing amplifier to output. The default gain of the current sensing amplifier is set to 20x, but the gain can be changed to 10x by setting JP5, JP7, JP9, JP10, JP14, and JP15 to open. The relationship between the current I_{in} flowing through the shunt resistor and the voltage V_{out} output from the current sensing circuit is shown in equations (1) and (2). In addition, by switching JP8 and JP11 to 2-3 pin short circuit, one shunt current sensing can be supported.

$$\text{Amplifier gain 10x : } V_{out}[V] = I_{in}[A] \times R_s[\Omega] \times 10 + AVCC/2 \quad (1)$$

$$\text{Amplifier gain 20x : } V_{out}[V] = I_{in}[A] \times R_s[\Omega] \times 20 + AVCC/2 \quad (2)$$

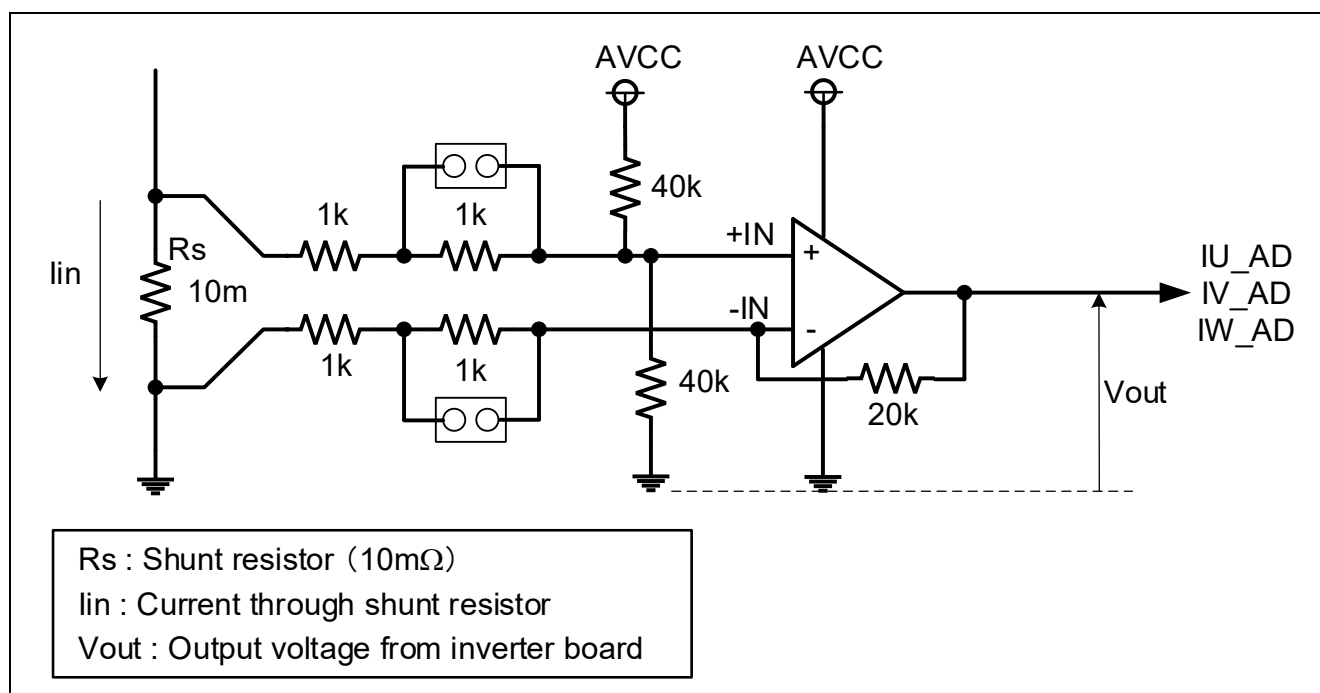


Figure 5-2 Current sensing circuit

5.1.3 Overcurrent detection circuit

Detect the overcurrent from the input current, using the overcurrent detection circuit illustrated in Figure 5-3. If the current value is within the range of threshold, OC_INV_OUT is HIGH, and this changes to LOW if overcurrent is detected. Therefore, you can protect the board and motor by monitoring the over current detection signal and setting PWM signals for gate driver to LOW or Hi-Z if the over current detection signal changes to LOW. As the value of the shunt resistor R_s is $10\text{m}\Omega$, the threshold current for over current detection is 21.4A for $AVCC=3.3\text{V}$ and 32.4A for $AVCC=5.0\text{V}$, respectively.

This circuit detects overcurrent and outputs a detection signal; however, it does not provide any protection function. Appropriate protection must be implemented by the MCU software.

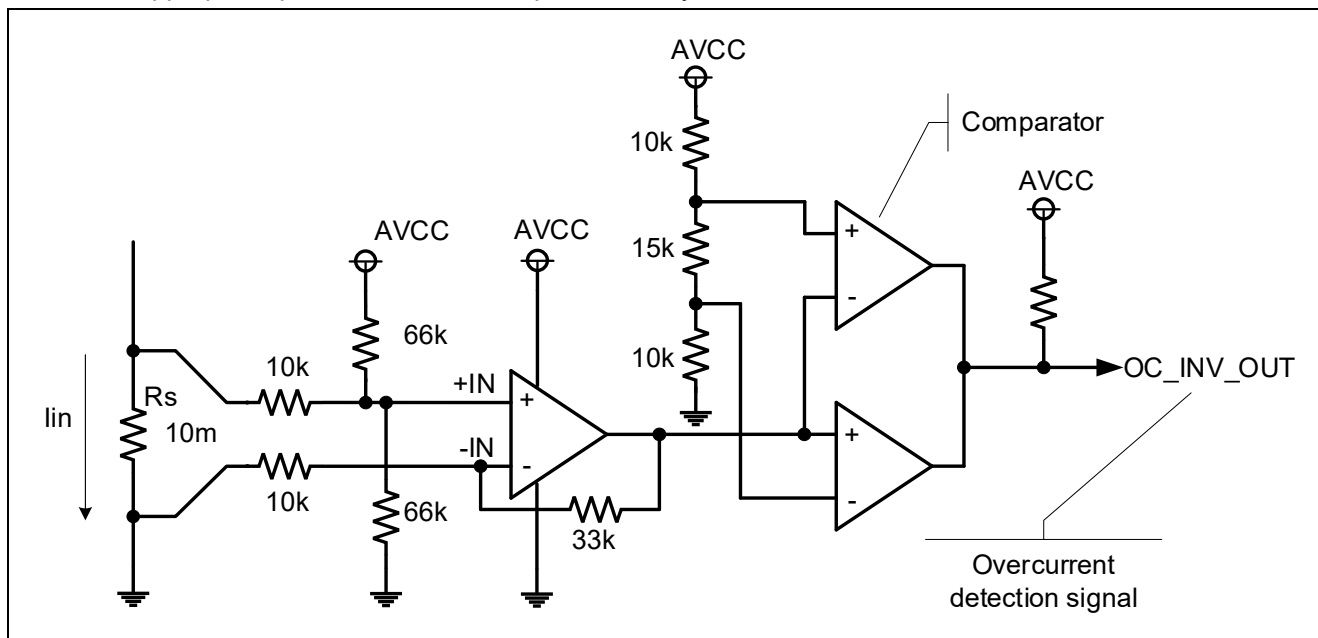


Figure 5-3 Overcurrent detection circuit

5.1.4 Output voltage detection circuit

The inverter board has the circuit that inputs bus voltage and three-phase output voltage (U, V and W phase) into the AD pin of the microcontroller through resistive voltage divider. Relation between the three-phase output voltage, the bus voltage and the detection voltage is described by the below equation (3).

$$V_{out}[V] = \frac{470}{10 \times 10^3 + 470} \times V_{in}[V] \quad (3)$$

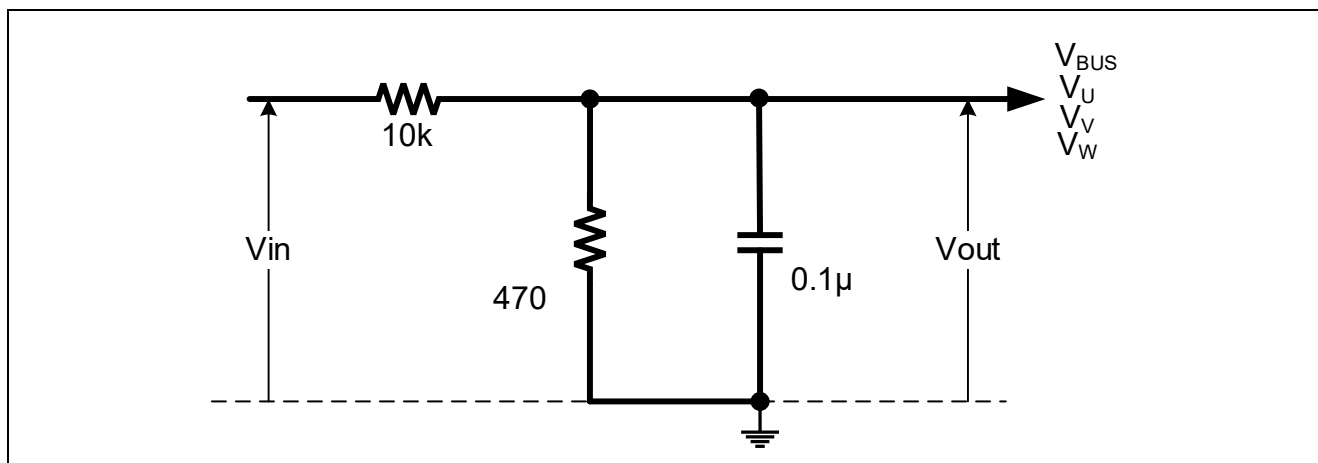


Figure 5-4 Output voltage detection circuit

5.1.5 Voltage generation circuit

On the inverter board, the gate driver voltage (" +12V" in the schematic) is generated from the 5V power supply (" +5V" in the schematic) with the boost converter. If 5V is not supplied from the CPU board, the buck converter on the inverter board generates 5V from the voltage input (12 to 48V) from the DC jack or terminal block.

Table 5-1 Voltage generation

Item	Input voltage [V]	Output voltage (TYP.) [V]	Output current (Max) [A]
5V generation	12~48	5	0.6
Gate driver voltage generation	5	11.4	-

5.1.6 LED

The inverter board has three LEDs which the user can control. The LED ON/OFF is controlled by the pin state.

Table 5-2 LED

Connector pin		LED1	LED2	LED3
CN4-18	HIGH	OFF	-	-
	LOW	ON	-	-
CN4-19	HIGH	-	OFF	-
	LOW	-	ON	-
CN4-20	HIGH	-	-	OFF
	LOW	-	-	ON

5.1.7 Toggle switch and push switch

The inverter board has toggle switch (SW1) and push switch (SW2). The pin voltage is controlled by the state of them.

Table 5-3 Toggle switch and push switch

Connector pin		SW1	SW2
CN4-16	HIGH	ON	-
	LOW	OFF	-
CN4-17	HIGH	-	RELEASE
	LOW	-	PUSH

5.1.8 Variable resistor

The inverter board has a variable resistor (VR1). When turning the variable resistor clockwise, terminal voltage of the variable resistor (CN3-17) becomes low. When turning it counterclockwise, the voltage becomes high.

Table 5-4 Variable resistance specification

Item	Specification
Input voltage range	0~AVCC
Variable resistor range	0~10kΩ

5.1.9 Test points

The inverter board has several test points to monitor various signals. They are marked in Figure 5-5 and summarized in Table 5-5.

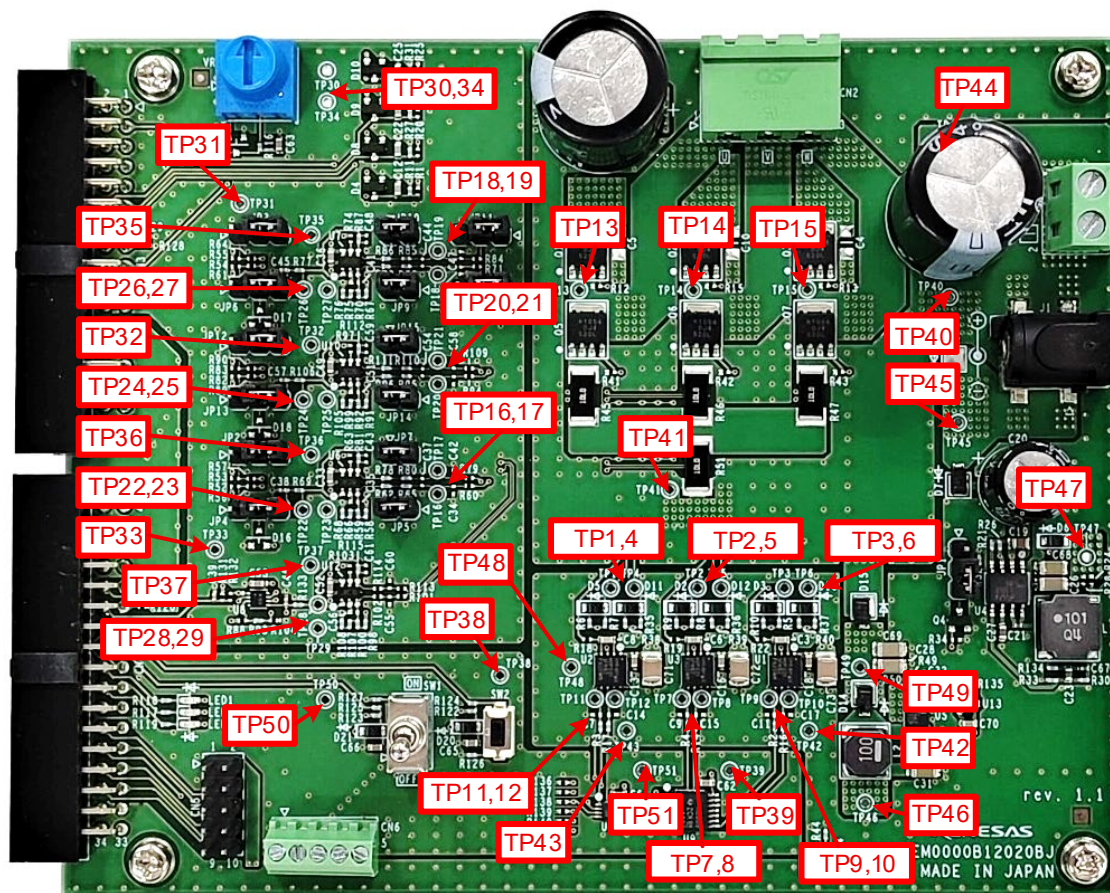


Figure 5-5 Test points

Table 5-5 Test points

No.	Signal name	Description
TP1	-	Gate of U-phase high-side MOSFET
TP2	-	Gate of V-phase high-side MOSFET
TP3	-	Gate of W-phase high-side MOSFET
TP4	-	Gate of U-phase low-side MOSFET
TP5	-	Gate of V-phase low-side MOSFET
TP6	-	Gate of W-phase low-side MOSFET
TP7	-	Gate driver input, V-phase, high-side
TP8	-	Gate driver input, V-phase, low-side
TP9	-	Gate driver input, W-phase, high-side
TP10	-	Gate driver input, W-phase, low-side
TP11	-	Gate driver input, U-phase, high-side
TP12	-	Gate driver input, U-phase, low-side
TP13	-	Motor output, U-phase
TP14	-	Motor output, V-phase
TP15	-	Motor output, W-phase
TP16	IWP	Current sensing, W-phase
TP17	IWN	Current sensing, W-phase
TP18	IUP	Current sensing, U-phase
TP19	IUN	Current sensing, U-phase
TP20	IVP	Current sensing, V-phase
TP21	IVN	Current sensing, V-phase
TP22	-	Current sensing amplifier output, W-phase
TP23	AGND	Analog Ground
TP24	-	Current sensing amplifier output, V-phase
TP25	AGND	Analog Ground
TP26	-	Current sensing amplifier output, U-phase
TP27	AGND	Analog Ground
TP28	-	Overcurrent detection amplifier output
TP29	AGND	Analog Ground
TP30~33	AVCC	Analog Power
TP34~37	AGND	Analog Ground
TP38,39	DGND	Digital Ground
TP40~43	GND	Power Ground
TP44,45	VDC	External power supply / bus voltage
TP46,47	+5V	+5V power supply
TP48,49	+12V	+12V power supply / gate driver operation voltage
TP50,51	VCC	Digital Power

5.2 Pin assignment

5.2.1 CPU board connector

Table 5-6 CPU board connector (CN3)

Pin No.	Output direction	Signal
1	-	SPARE1
2	-	AGND
3	To CPU	DC bus voltage detection
4	-	AGND
5	To CPU	U-phase current sensing
6	To CPU	U-phase current sensing (PGAVSS)
7	To CPU	V-phase current sensing
8	To CPU	V-phase current sensing (PGAVSS)
9	To CPU	W-phase current sensing
10	To CPU	W-phase current sensing (PGAVSS)
11	To CPU	U-phase voltage detection
12	To CPU	V-phase voltage detection
13	To CPU	W-phase voltage detection
14	-	AGND
15	To CPU	VPFC_AD
16	To CPU	IPFC_AD
17	To CPU	VR1
18	-	AGND
19	-	AVCC
20	-	AVCC
21	-	AGND
22	-	AGND
23	-	VCC
24	-	VCC
25	-	DGND
26	-	DGND
27	To INV	PWM UL (Lower)
28	-	DGND
29	To INV	PWM UH (Upper)
30	-	DGND
31	To INV	PWM VL (Lower)
32	-	DGND
33	To INV	PWM VH (Upper)
34	-	DGND

Table 5-7 CPU board connector (CN4)

Pin No.	Output direction	Signal
1	To INV	PWM WL (Lower)
2	-	DGND
3	To INV	PWM WH (Upper)
4	-	DGND
5	-	SPARE2
6	-	SPARE3
7	-	SPARE4
8	-	SPARE5
9	To INV	Bus power signal from CPU board
10	To CPU	Inverter connected signal
11	To CPU	Save interlock signal
12	To CPU	Over current detection
13	To CPU	OC_PFC_OUT
14	To INV	PWM_IN
15	To INV	RELAY_IN
16	To CPU	SW1
17	To CPU	SW2
18	To INV	LED1
19	To INV	LED2
20	To INV	LED3
21	To CPU	HALL U
22	To CPU	HALL V
23	To CPU	HALL W
24	To CPU	IPS_SIO_SDA
25	To CPU	IPS_SCK_SCL
26	To CPU	IPS_CSN_IRQN/Encoder Z
27	To CPU	IPS_A/ Encoder A
28	To CPU	IPS_A#/ Encoder A#
29	To CPU	IPS_B/ Encoder B#
30	To CPU	IPS_B#/ Encoder B#
31	-	DGND
32	-	DGND
33	-	+5V
34	-	+5V

5.2.2 Hall sensor signal input

This product has connector for hall sensor signal input. Pin assignment of it is listed in Table 5-8.

Table 5-8 Connector for hall sensor signal input (CN6) pin assignment

Pin No.	Pin Function
1	DGND
2	+5V
3	HALL_W
4	HALL_V
5	HALL_U

5.2.3 Encoder/Inductive position sensor signal input

This product has pins for encoder/inductive position sensor signal input. Pin assignment for them is listed in Table 5-9.

Table 5-9 Pins for encoder/inductive position sensor signal input (CN5) pin assignment

Pin No.	Pin function
1	VCC
2	+5V
3	CSN_IRQN/ENC_Z
4	SIO_SDA
5	SCK_SCL
6	IPS_A/ENC_A
7	IPS_A#/ENC_A#
8	IPS_B/ENC_B
9	IPS_B#/ENC_B#
10	DGND

6. CPU Board Specification

This section describes the specification of the CPU Board.

6.1 Functions

6.1.1 Power supply

When not connected to the inverter board, power should be supplied from the USB connector (CN14). When connecting to the inverter board, power supply from the USB connector or from the inverter board will be automatically selected. USB power supply has priority. By supplying power, the 3.3V regulator operates, supplying 3.3V to VCC, and turns on LED7.

6.1.2 Onboard debugger

This product has the on-board debugger circuit, E2 On-Board (hereinafter called "E2OB"). You can write a program (firmware) of RX74M with it. When you write a program, open (remove) JP8 and connect the CPU board to PC with USB cable. E2OB operates as debugger equivalent to E2 emulator Lite. After writing a program, short JP8 for CPU board operation. E2OB become invalid, and it operates in single-chip mode. When using from Integrated Development Environment (e.g. e²studio) or flash programming tool (e.g. Renesas Flash Programmer), set the type of debugger (tool) to "E2 emulator Lite". After writing a program, short JP8 for CPU board operation.

*When using the debugger, please make sure to carefully review the contents of "1.1 Presupposition and precautions of this document".

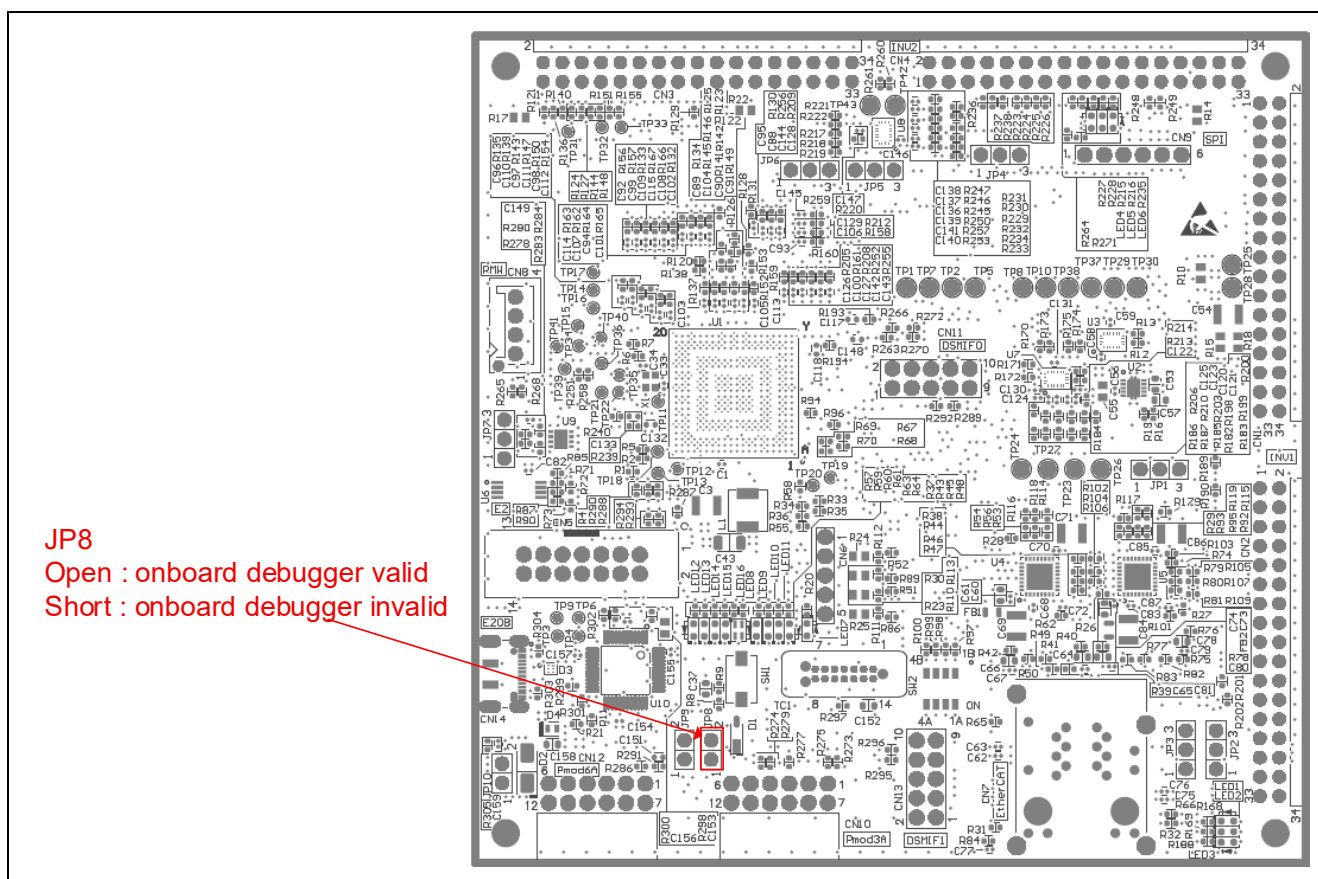


Figure 6-1 Jumper setting of JP8

6.1.3 External emulator connector

This product E2OB circuit, but external emulators such as E2 or E2-Lite can also be used.

The pin assignments of the external emulator connector are shown in Table 6-1.

**When using the debugger, please make sure to carefully review the contents of "1.1 Presupposition and precautions of this document".*

Table 6-1 External emulator connector (CN5) pin assignment

Pin No.	Emulator	RX74M pin	Pin No.	Emulator	RX74Mpin
1	TCK	P301/TCK	2	GND	- (VSS)
3	TRST#	- (NC)	4	EMLE	- (NC)
5	TDO	P302/TDO/TXD001-A	6	NC	- (NC)
7	MD/FINED	P315/MD/FINED	8	VCC	- (VCC)
9	TMS	P300/TMS	10	UD	- (NC)
11	TDI	P303/TDI/RXD001-A	12	GND	- (VSS)
13	RES#	RES#	14	GND	- (VSS)

6.1.4 Reset circuit

This product has a reset circuit to enable power-on reset or external reset on MCU. Push the tact switch (SW1) to externally reset MCU.

6.1.5 Inverter board connector

Max 2 inverter boards can be connected to this product. 1st inverter board is connected with CN1 and CN2, and 2nd inverter board is connected with CN3 and CN4. The pin assignments of the inverter board connectors are shown in Table 6-2, Table 6-3, Table 6-4, Table 6-5.

Table 6-2 1st Inverter board connector (CN1) pin assignment

Pin No.	Pin Function	RX74M Pin	Pin No.	Pin Function	RX74M Pin
1	TEMPERATURE	P406/AN006	2	AGND	- (AVSS)
3	VDC (BUS_VOLTAGE)	P408/AN008	4	AGND	- (AVSS)
5	IU (U_CURRENT/ 1-SHUNT_CURRENT)	P400/AN000	6	IU (U_CURRENT (G)/ 1-SHUNT_CURRENT (G))	(P401/AN001) *1
7	IV (V_CURRENT)	P402/AN002	8	IV (V_CURRENT (G))	(P403/AN003) *1
9	IW (W_CURRENT)	P404/AN004	10	IW (W_CURRENT (G))	(P405/AN005) *1
11	VU (U_VOLTAGE)	P006/AN022	12	VV (V_VOLTAGE)	PD00/AN024
13	VW (W_VOLTAGE)	PD02/AN026	14	AGND	- (AVSS)
15	VAC (VPFC)	P410/AN010	16	IPFC1	P412/AN012
17	VR/IPFC2	P414/AN014	18	AGND	- (AVSS)
19	AVCC	- (AVCC)	20	AVCC	- (AVCC)
21	AGND	- (AVSS)	22	AGND	- (AVSS)
23	VCC	- (VCC)	24	VCC	- (VCC)
25	DGND	- (VSS)	26	DGND	- (VSS)
27	PWM_UL	PE01/GTIOC7B	28	DGND	- (VSS)
29	PWM_UH	PE00/GTIOC7A	30	DGND	- (VSS)
31	PWM_VL	PE03/GTIOC8B	32	DGND	- (VSS)
33	PWM_VH	PE02/GTIOC8A	34	DGND	- (VSS)

*1 Exclusively assigned by jumper register setting

*2 Available functions are limited depending on the inverter board type.

IPS : Inductive Position Sensor

PFC : Power Factor Correction

ENC : Encoder

Table 6-3 1st Inverter board connector (CN2) pin assignment

Pin No.	Pin Function	RX74M Pin	Pin No.	Pin Function	RX74M Pin
1	PWM_WL	PE05/GTIOC9B	2	DGND	- (VSS)
3	PWM_WH	PE04/GTIOC9A	4	DGND	- (VSS)
5	DRV_SCK (SCK)	P601/RSPCK0-D	6	PWM_PFC2	P607/GTIOC15B *1
				DRV_RXD (MOSI)	(P602/MOSI0-D) *1
7	DRV_TXD (MISO)	P603/MISO0-D	8	DRV_CS (CS)	PF07/SSL03-C
9	BUS_POWER_IN1	-	10	INV_CONNECTED	P904/GPIO/IRQ12
11	SAFE_INTERLOCK	P905/GPIO/IRQ13	12	OC (INVERTER)	P711/GTETRGRB/IRQ19
13	OC (PFC)	PG15/GPIO/IRQ31	14	PWM_PFC1/ DRV_EN	PE06/GTIOC10A
15	RELAY_IN/ CON_MOT_SEL	P907/GPIO	16	SW1	P803/GPIO/IRQ3
17	SW2/ OVER_TEMPERATURE	P506/GPIO/IRQ6	18	LED1	P508/GPIO
19	LED2	P510/GPIO	20	LED3	P511/GPIO
21	HALL_U	P714/GPIO/IRQ22	22	HALL_V	P715/GPIO/IRQ23
23	HALL_W	PG08/GPIO/IRQ24	24	IPS_SIO_SDA (SDA)	P107/SDAHS2-A
25	IPS_SCK_SCL (SCL)	P106/SCLHS2-A	26	IPS_CSN_IRQN/ ENC_Z	P708/GTETRGC/IRQ16 *1
				(PCIF Z Phase)	(P210/PCINZ1) *1
27	IPS_A	(PD04/AN028) *1	28	IPS_A#	PD06/AN030
	ENC_A	P712/GTIOC14A *1			
	(PCIF_A)	(P208/PCINA1) *1			
29	IPS_B	(P900/AN032) *1	30	IPS_B#	P902/AN034
	ENC_B	P713/GTIOC14B *1			
	(PCIF_B)	(P209/PCINB1) *1			
31	5V_GND	- (VSS)	32	5V_GND	- (VSS)
33	+5V	-	34	+5V	-

*1 Exclusively assigned by jumper register setting

*2 Available functions are limited depending on the inverter board type.

Table 6-4 2nd Inverter board connector (CN3) pin assignment

Pin No.	Pin Function	RX74M Pin	Pin No.	Pin Function	RX74M Pin
1	TEMPERATURE	P407/AN007	2	AGND	- (AVSS)
3	VDC (BUS_VOLTAGE)	P409/AN009	4	AGND	- (AVSS)
5	IU (U_CURRENT/ 1-SHUNT_CURRENT)	P401/AN001 *1 (P000/AN016) *1 (P402/AN002) *1	6	IU (U_CURRENT (G)/ 1-SHUNT_CURRENT (G))	(P001/AN017) *1 (P403/AN003) *1
7	IV (V_CURRENT)	P403/AN003 *1 (P002/AN018) *1	8	IV (V_CURRENT (G))	(P003/AN019)
9	IW (W_CURRENT)	P405/AN005 *1 (P004/AN020) *1	10	IW (W_CURRENT (G))	(P005/AN021)
11	VU (U_VOLTAGE)	P007/AN023	12	VV (V_VOLTAGE)	PD01/AN025
13	VW (W_VOLTAGE)	PD03/AN027	14	AGND	- (AVSS)
15	VAC (VPFC)	P411/AN011	16	IPFC1	P413/AN013
17	VR/IPFC2	P415/AN015	18	AGND	- (AVSS)
19	AVCC	- (AVCC)	20	AVCC	- (AVCC)
21	AGND	- (AVSS)	22	AGND	- (AVSS)
23	VCC	- (VCC)	24	VCC	- (VCC)
25	DGND	- (VSS)	26	DGND	- (VSS)
27	PWM_UL	P203/GTIOC4B	28	DGND	- (VSS)
29	PWM_UH	P202/GTIOC4A	30	DGND	- (VSS)
31	PWM_VL	P205/GTIOC5B	32	DGND	- (VSS)
33	PWM_VH	P204/GTIOC5A	34	DGND	- (VSS)

*1 Exclusively assigned by jumper register setting

*2 Available functions are limited depending on the inverter board type.

Table 6-5 2nd Inverter board connector (CN4) pin assignment

Pin No.	Pin Function	RX74M Pin	Pin No.	Pin Function	RX74M Pin
1	PWM_WL	P207/GTIOC6B	2	DGND	- (VSS)
3	PWM_WH	P206/GTIOC6A	4	DGND	- (VSS)
5	DRV_SCK (SCK)	P601/RSPCK0-D	6	PWM_PFC2	P606/GTIOC15A *1
				DRV_RXD (MOSI)	(P602/MOSI0-D) *1
7	DRV_TXD (MISO)	P603/MISO0-D	8	DRV_CS (CS)	PF06/SSL02-C
9	BUS_POWER_IN2	-	10	INV_CONNECTED	PF00/GPIO/IRQ0
11	SAFE_INTERLOCK	PF01/GPIO/IRQ1	12	OC (INVERTER)	P709/GTETRGD/IRQ17
13	OC (PFC)	P211/GPIO/IRQ27	14	PWM_PFC1/ DRV_EN	PE07/GTIOC10B
15	RELAY_IN/ CON_MOT_SEL	P906/GPIO	16	SW1	P010/GPIO/IRQ26
17	SW2/ OVER_TEMPERATURE	P009/GPIO/IRQ25	18	LED1	P011/GPIO
19	LED2	P212/GPIO	20	LED3	P310/GPIO
21	HALL_U	P012/IRQ28	22	HALL_V	P013/IRQ29
23	HALL_W	P014/IRQ30	24	IPS_SIO_SDA (SDA)	P107/SDAHS2-A
25	IPS_SCK_SCL (SCL)	P106/SCLHS2-A	26	IPS_CSN_IRQN/ ENC_Z	P710/GTETRGA/IRQ18 *1
				(PCIF Z Phase)	(P215/PCINZ0) *1
27	IPS_A	(PD05/AN029) *1	28	IPS_A#	PD07/AN031
	ENC_A	PG06/GTIOC13A *1			
	(PCIF_A)	(P213/PCINA0) *1			
29	IPS_B	(P901/AN033) *1	30	IPS_B#	P903/AN035
	ENC_B	PG07/GTIOC13B *1			
	(PCIF_B)	(P214/PCINB0) *1			
31	5V_GND	- (VSS)	32	5V_GND	- (VSS)
33	+5V	-	34	+5V	-

*1 Exclusively assigned by jumper register setting

*2 Available functions are limited depending on the inverter board type.

Figure 6-2 show a connection example when using this product with the inverter board and the communication board. And also see 4.6.2 for power supply.

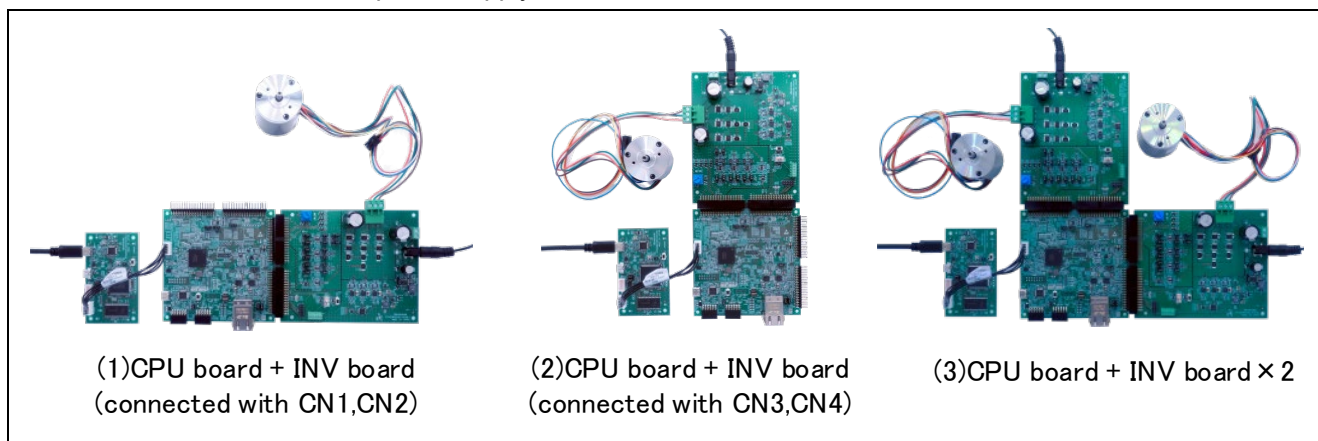


Figure 6-2 Board connection of CPU board, INV board and COM board

6.1.6 Serial communication

For serial communication using Renesas Motor Workbench, the CPU board has SCI connector. Pin assignment for SCI connector is listed in Table 6-6.

Table 6-6 SCI connector (CN8) pin assignment

Pin No.	Pin Function	RX74M Connection Pin
1	DGND	- (VSS)
2	MCU RXD	P605/RXD009-B
3	MCU TXD	P604/TXD009-B
4	VCC	- (VCC)

6.1.7 SPI Communication

This product has through holes for SPI communication. Pin assignment for SPI communication connector is listed in Table 6-7.

Table 6-7 SPI connector (CN9) pin assignment

Pin No.	Pin Function	RX74M Connection Pin
1	CS	P600/SSL00-D
2	MOSI	P602/MOSI0-D
3	MISO	P603/MISO0-D
4	SCK	P601/RSPCK0-D
5	DGND	- (VSS)
6	VCC	- (VCC)

6.1.8 Pmod

This product has two connectors for connecting Pmod modules. The pin assignments are listed in Table 6-8, Table 6-9.

Table 6-8 Pmod 3A connector (CN10) pin assignment

Pin No.	Pin Function	RX74M Connection Pin	Pin No.	Pin Function	RX74M Connection Pin
1	CTS	P501/CTS002#	7	GPIO/IRQ	P507/GPIO/IRQ7
2	TXD	P502/TXD002-C	8	GPIO	PF04/GPIO
3	RXD	P503/RXD002-C	9	GPIO	PF02/GPIO
4	RTS	P500/CTS002#/RTS002#	10	GPIO	P101/GPIO
5	DGND	- (VSS)	11	DGND	- (VSS)
6	VCC	- (VCC)	12	VCC	- (VCC)

Table 6-9 Pmod 6A connector (CN12) pin assignment

Pin No.	Pin Function	RX74M Connection Pin	Pin No.	Pin Function	RX74M Connection Pin
1	NC/INT	P505/GPIO/IRQ10	7	GPIO	P108/GPIO
2	NC/RESET	P504/GPIO	8	GPIO	P109/GPIO
3	SCL (I2C)	P106/SCLHS2-A	9	GPIO	P110/GPIO
4	SDA (I2C)	P107/SDAHS2-A	10	GPIO	P111/GPIO
5	DGND	- (VSS)	11	DGND	- (VSS)
6	VCC	- (VCC)	12	VCC	- (VCC)

6.1.9 CAN communication

This product has through holes for CAN communication. The pin assignments for the CAN connector is listed in Table 6-10. Note that the CAN connector and CAN driver are not mounted, so users need to mount them when using this circuit.

Table 6-10 CAN connector (CN15), CAN Driver (U9) pin assignment

CAN driver		CAN connector		RX74M Connection Pin
Pin No.	Pin Function	Pin No.	Pin Function	
1	TXD	-	-	P804/CTX0
2	DGND	2	VSS	- (VSS)
3	VCC (+5V)	-	-	-
4	RXD	-	-	P805/CRX0
5	VIO	-	-	- (VCC)
6	CANL	3	CANL	-
7	CANH	1	CANH	-
8	STB	-	-	P115/GPIO
9	DGND	2	VSS	- (VSS)

6.1.10 User controllable LED

This product has several LEDs for use in program debugging and system applications. Pin assignment for user controllable LEDs are listed in Table 6-11, Table 6-12.

Table 6-11 User controllable LED (INV1) pin assignment

RX74M Pin		LED1 (Orange)	LED2 (Orange)	LED3 (Orange)
P508/GPIO	Output HIGH	OFF	-	-
	Output LOW	ON	-	-
P510/GPIO	Output HIGH	-	OFF	-
	Output LOW	-	ON	-
P511/GPIO	Output HIGH	-	-	OFF
	Output LOW	-	-	ON

Table 6-12 User controllable LED (INV2) pin assignment

RX74M Pin		LED4 (Orange)	LED5 (Orange)	LED6 (Orange)
P011/GPIO	Output HIGH	OFF	-	-
	Output LOW	ON	-	-
P212/GPIO	Output HIGH	-	OFF	-
	Output LOW	-	ON	-
P310/GPIO	Output HIGH	-	-	OFF
	Output LOW	-	-	ON

6.1.11 DSMIF circuit

This product has 2 connectors for DSMIF communication. The pin assignments for the DSMIF connectors are listed in Table 6-13, Table 6-14.

Note that the connectors are not mounted, so users need to mount them when using this circuit.

Table 6-13 DSMIF0 connector (CN11) pin assignment

Pin No.	Pin Function	RX74M Connection Pin	Pin No.	Pin Function	RX74M Connection Pin
1	NC	-	2	VCC	- (VCC)
3	NC	-	4	NC	-
5	NC	-	6	NC	-
7	DSMCLK0	P514/DSM0_CLK0-G	8	DSMDAT0	P515/DSM0_DAT0-G
9	DGND	- (VSS)	10	DGND	- (VSS)

Table 6-14 DSMIF1 connector (CN13) pin assignment

Pin No.	Pin Function	RX74M Connection Pin	Pin No.	Pin Function	RX74M Connection Pin
1	NC	-	2	VCC	- (VCC)
3	NC	-	4	NC	-
5	NC	-	6	NC	-
7	DSMCLK1	P807/DSM0_CLK1-F	8	DSMDAT1	P806/DSM0_DAT1-F
9	DGND	- (VSS)	10	DGND	- (VSS)

6.1.12 EtherCAT circuit

This product has EtherCAT connector (CN7) and EtherCAT test connector (CN6). When using EtherCAT, connect to the target with CN7. The upper side of CN7 is connected to Ch1, and the lower side is connected to Ch0. The connector configuration is shown in Figure 6-3 The test connector connections are listed in Table 6-15.

Additionally, it has LEDs for checking the EtherCAT control status. The pin assignments of each LEDs are listed in Table 6-16, Table 6-17, Table 6-18.

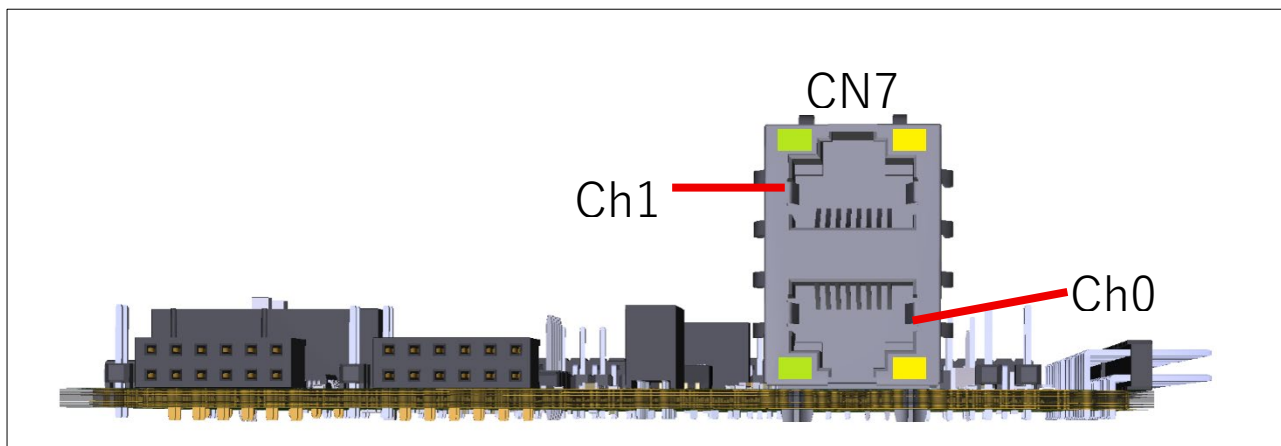


Figure 6-3 EtherCAT Connector (CN7) Configuration

Table 6-15 EtherCAT test connector (CN6) pin assignment

Pin No.	RX74M Connection Pin
1	PA00/CATIRQ
2	P801/CATSYNC0
3	P702/CATSYNC1
4	P800/CATLATCH0
5	P701/CATLATCH1

Table 6-16 LED for EtherCAT (GPIO control) pin assignment

RX74M Pin		LED8 (Module1/ Green)	LED9 (Module2/ Red)	LED10 (Network1/ Green)	LED11 (Network2/ Red)
PF08/GPIO	Output HIGH	ON	-	-	-
	Output LOW	OFF	-	-	-
PF09/GPIO	Output HIGH	-	ON	-	-
	Output LOW	-	OFF	-	-
P509/GPIO	Output HIGH	-	-	ON	-
	Output LOW	-	-	OFF	-
PF03/GPIO	Output HIGH	-	-	-	ON
	Output LOW	-	-	-	OFF

Table 6-17 LED for EtherCAT (IP control) pin assignment

RX74M Pin		LED12 (L/A IN/ Green)	LED13 (L/A OUT/ Green)	LED14 (RUN/ Green)	LED15 (ERR/ Red)	LED16-R (DNF)	LED16-YG (DNF)
PC06/ CATLINKACT0	Output HIGH	ON	-	-	-	-	-
	Output LOW	OFF	-	-	-	-	-
PA07/ CATLINKACT1	Output HIGH	-	ON	-	-	-	-
	Output LOW	-	OFF	-	-	-	-
P105/ CATLED RUN	Output HIGH	-	-	ON	-	-	-
	Output LOW	-	-	OFF	-	-	-
P100/ CATLEDERR	Output HIGH	-	-	-	ON	ON	-
	Output LOW	-	-	-	OFF	OFF	-
P104/ CATLEDSTER	Output HIGH	-	-	-	-	-	ON
	Output LOW	-	-	-	-	-	OFF

Table 6-18 LED for EtherCAT (EtherCAT connector) pin assignment

PHY Driver (KSZ8081MNXCA)		Lower/ Yellow	Lower/ Green	Upper/ Yellow	Upper/ Green
CH0(U4)/LED0	Output HIGH	OFF	-	-	-
	Output LOW	ON	-	-	-
CH0(U4)/LED1	Output HIGH	-	OFF	-	-
	Output LOW	-	ON	-	-
CH1(U5)/LED0	Output HIGH	-	-	OFF	-
	Output LOW	-	-	ON	-
CH1(U5)/LED1	Output HIGH	-	-	-	OFF
	Output LOW	-	-	-	ON

6.1.13 Test points

The CPU board has several test points to monitor various signals. They are marked in Figure 6-4 and summarized in Table 6-19 .

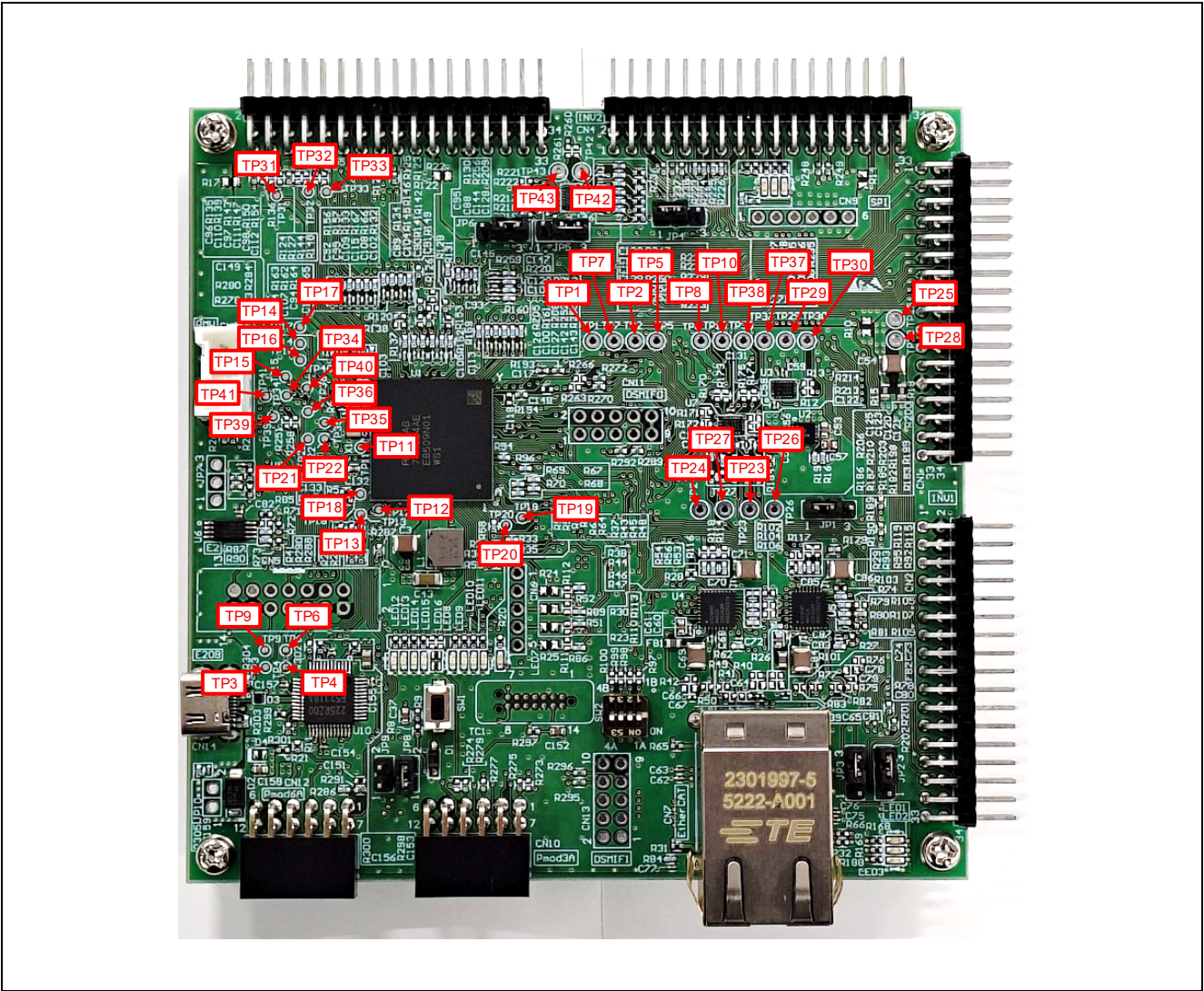


Figure 6-4 Test points

Table 6-19 Test points

No.	Signal name	Description	No.	Signal name	Description
TP1	PG14/PCOUTZ0	PG14/PCOUTZ0	TP26	5V_GND	Ground for +5V
TP2	PG12/PCOUTA0	PG12/PCOUTA0	TP27	VSS	Digital Ground
TP3	-	TRST#(NC)	TP28	AVSS	Analog Ground
TP4	-	EMLE(NC)	TP29	SCL_U3	U3 SCL
TP5	PG13/PCOUTB0	PG13/PCOUTB0	TP30	SDA_U3	U3 SDA
TP6	-	CN5-6pin	TP31	INV2 U CURRENT (G)	P001/AN017
TP7	PG11/PCOUTZ1	PG11/PCOUTZ1	TP32	INV2 V CURRENT (G)	P003/AN019
TP8	PG09/PCOUTA1	PG09/PCOUTA1	TP33	INV2 W CURRENT (G)	P005/AN021
TP9	-	CN5-10pin	TP34	P210/PCINZ1	P210/PCINZ1
TP10	PG10/PCOUTB1	PG10/PCOUTB1	TP35	P208/PCINA1	P208/PCINA1
TP11	P305/NMI	P305/NMI	TP36	P209/PCINB1	P209/PCINB1
TP12	P200/GPIO/IRQ8	P200/GPIO/IRQ8	TP37	SCL_U7	U7 SCL
TP13	P201/GPIO/IRQ9	P201/GPIO/IRQ9	TP38	SDA_U7	U7 SDA
TP14	P309/XCIN/EXCIN	P309/XCIN/EXCIN	TP39	P215/PCINZ0	P215/PCINZ0
TP15	P008/GPIO	P008/GPIO	TP40	P213/PCINA0	P213/PCINA0
TP16	P308/XCOUT	P308/XCOUT	TP41	P214/PCINB0	P214/PCINB0
TP17	P015/GPIO	P015/GPIO	TP42	SCL_U8	U8 SCL
TP18	P304/GPIO	P304/GPIO	TP43	SDA_U8	U8 SDA
TP19	P512/GPIO	P512/GPIO			
TP20	P513/GPIO	P513/GPIO			
TP21	P311/GPIO/DSM0_CLK2-C	P311/GPIO/DSM0_CLK2-C			
TP22	P312/GPIO/DSM0_DAT2-C	P312/GPIO/DSM0_DAT2-C			
TP23	+5V	+5V supply			
TP24	VCC	Digital Power			
TP25	AVCC	Analog Power			

6.1.14 Pin function list

Table 6-20 RX74M pin function list

Pin number	Pin function	Board function
A01	VSS	VSS
A02	P514/DSM0_CLK0_G	DSMIF0_PIN7_DSM0_CLK0
A03	P515/DSM0_DAT0_G	DSMIF0_PIN8_DSM0_DAT0
A04	PC01/ESC0_CAT0_ETXD0_A	ESC0_CAT0_ETXD0
A05	PC03/ESC0_CAT0_ETXD2_A	ESC0_CAT0_ETXD2
A06	PC04/ESC0_CAT0_ETXD3_A	ESC0_CAT0_ETXD3
A07	PC07/CATRESTOUT_A	ESC_CATRESTOUT
A08	P801/ESC0_CATSYNCO_A	ESC0_CATSYNCO
A09	P800/ESC0_CATLATCH0_A	ESC0_CATLATCH0
A10	VLO	VLO
A11	VSS_DCDC	VSS_DCDC
A12	VSS_DCDC	VSS_DCDC
A13	VCC_DCDC	VCC_DCDC
A14	P504/GPIO	PMOD6A_PIN2_RST_GPIO
A15	P103/CATI2CDATA_A	ESC_CATI2CDATA
A16	P104/CATLEDSTER_C	ESC_CATLEDSTER_LED
A17	P106/SCL2_A	PMOD6A_PIN3_IIC_SCL
A18	P200/GPIO/IRQ8	TP12_GPIO_IRQ
A19	P300/TMS	DEBUGGER_TMS
A20	VSS	VSS
B01	PG03/GPIO/IRQ11	CAT0_INTRP
B02	P512/GPIO	TP19_GPIO
B03	P513/GPIO	TP20_GPIO
B04	PC00/ESC0_CAT0_TX_CLK_A	ESC0_CAT0_TX_CLK
B05	PC02/ESC0_CAT0_ETXD1_A	ESC0_CAT0_ETXD1
B06	VSS	VSS
B07	PC05/ESC0_CAT0_TX_EN_A	ESC0_CAT0_TX_EN
B08	PC06/ESC0_CATLINKACT0_A	ESC0_CATLINKACT0_LED
B09	P802/GPIO/IRQ2	CAT1_INTRP
B10	VLO	VLO
B11	VSS_DCDC	VSS_DCDC
B12	VSS_DCDC	VSS_DCDC
B13	VCC_DCDC	VCC_DCDC
B14	P505/GPIO/IRQ10	PMOD6A_PIN1_INT
B15	P102/CATI2CCLK_A	ESC_CATI2CCLK
B16	VSS	VSS
B17	P107/SDA2_A	PMOD6A_PIN4_IIC_SDA
B18	P201/GPIO/IRQ9	TP13_GPIO_IRQ
B19	P301/TCK	DEBUGGER_TCK
B20	P302/TDO	DEBUGGER_TDO
C01	PG02/ESC1_CAT1_RX_ER_A	ESC1_CAT1_RX_ER
C02	PG01/ESC1_CAT1_RX_DV_A	ESC1_CAT1_RX_DV
C03	PB01/ESC0_CAT0_ERXD1_A	ESC0_CAT0_ERXD1
C04	PB03/ESC0_CAT0_ERXD3_A	ESC0_CAT0_ERXD3
C05	PB04/ESC0_CAT0_RX_CLK_A	ESC0_CAT0_RX_CLK
C06	PB06/ESC0_CAT0_RX_ER_A	ESC0_CAT0_RX_ER
C07	PB07/ESC0_CAT0_LINKSTA_A	ESC0_CAT0_LINKSTA
C08	PB05/ESC0_CAT0_RX_DV_A	ESC0_CAT0_RX_DV
C09	P803/GPIO/IRQ3	INV1_SW1
C10	VLO	VLO
C11	VLO	VLO
C12	VCC_DCDC	VCC_DCDC
C13	VCC_DCDC	VCC_DCDC
C14	P506/GPIO/IRQ6	INV1_SW2/THERMAL_ERROR
C15	P105/CATLEDRUN_C	ESC_CATLEDRUN_LED
C16	P805/CRX0_D	CAN_DRIVER_PIN4_CRX

Pin number	Pin function	Board function
C17	P806/DSM0_CLK1_F	DSMIF1_PIN7_DSM0_CLK1
C18	P807/DSM0_DAT1_F	DSMIF1_PIN8_DSM0_DAT1
C19	P304/GPIO	TP18_GPIO
C20	P303/TDI	DEBUGGER_TDI
D01	PA06/ESC1_CAT1_ETXD3_A	ESC1_CAT1_ETXD3
D02	PG00/ESC1_CAT1_RX_CLK_A	ESC1_CAT1_RX_CLK
D03	P511/GPIO	INV1_LED3
D18	P108/GPIO	PMOD6A_PIN7_GPIO
D19	P109/GPIO	PMOD6A_PIN8_GPIO
D20	P110/GPIO	PMOD6A_PIN9_GPIO
E01	PA05/ESC1_CAT1_ETXD2_A	ESC1_CAT1_ETXD2
E02	PA07/ESC1_CATLINKACT1_A	ESC1_CATLINKACT1_LED
E03	P510/GPIO	INV1_LED2
E05	PG05/ESC0_CAT0_MDIO_A	ESC0_CAT0_MDIO
E06	PB02/ESC0_CAT0_ERXD2_A	ESC0_CAT0_ERXD2
E07	PB00/ESC0_CAT0_ERXD0_A	ESC0_CAT0_ERXD0
E08	VCC	VCC
E09	VCC	VCC
E10	P500/CTS_RTS2#_C	PMOD3A_PIN4_UART_CTS_RTS#
E11	P502/TXD2_C	PMOD3A_PIN2_UART_TXD
E12	P100/CATLEDERR_C	ESC_CATLEDERR_LED
E13	VCC	VCC
E14	VCC	VCC
E15	P507/GPIO/IRQ7	PMOD3A_PIN7_INT
E16	P804/CTX0_D	CAN_DRIVER_PIN1_CTX
E18	P111/GPIO	PMOD6A_PIN10_GPIO
E19	P112/GPIO	CAT_ID_1
E20	P113/GPIO	CAT_ID_2
F01	PA03/ESC1_CAT1_ETXD0_A	ESC1_CAT1_ETXD0
F02	PA04/ESC1_CAT1_ETXD1_A	ESC1_CAT1_ETXD1
F03	P509/GPIO	CAT_NETWORK1_LED
F05	PG07/GTIOC13B/IRQ15	INV2_ENC_B
F06	PG06/GTIOC13A/IRQ14	INV2_ENC_A
F07	PG04/ESC0_CAT0_MDC_A	ESC0_CAT0_MDC
F08	VSS	VSS
F09	VSS	VSS
F10	P501/CTS2#_C	PMOD3A_PIN1_UART_CTS#
F11	P503/RXD2_C	PMOD3A_PIN3_UART_RXD
F12	P101/GPIO	PMOD3A_PIN10_GPIO
F13	VSS	VSS
F14	VSS	VSS
F15	PF02/GPIO	PMOD3A_PIN9_GPIO
F16	P115/GPIO	CAN_DRIVER_PIN8_STB
F18	P114/GPIO	CAT_ID_3
F19	VSS	VSS
F20	VCC	VCC
G01	PA02/ESC1_CAT1_TX_CLK_A	ESC1_CAT1_TX_CLK
G02	PA01/ESC1_CAT1_TX_EN_A	ESC1_CAT1_TX_EN
G03	P508/GPIO	INV1_LED1
G05	VCC	VCC
G06	VSS	VSS
G15	PF03/GPIO	CAT_NETWORK2_LED
G16	PF04/GPIO/IRQ4	PMOD3A_PIN8_RST_GPIO
G18	VSS_USB	VSS_USB
G19	PF00/GPIO/IRQ0	INV2_INV_CONNECTED
G20	PF01/GPIO/IRQ1	INV2_SAFE_LOCK
H01	PA00/ESC_CATIRQ_A	ESC_CATIRQ
H02	PG13/PCOUTB0	TP5_PCOOUTB0
H03	PG15/GPIO/IRQ31	INV1_OC(PFC)

Pin number	Pin function	Board function
H05	VCC	VCC
H06	VSS	VSS
H08	VSS	VSS
H09	VCL	VCL
H10	VSS	VSS
H11	VSS	VSS
H12	VCL	VCL
H13	VSS	VSS
H15	P204/GTIOC5A	INV2 PWM_VH
H16	P203/GTIOC4B	INV2 PWM_UL
H18	PF05/GPIO/IRQ5	CAT_ID_4
H19	VCC_USB	VCC_USB
H20	VCC_USB	VCC_USB
J01	PG11/PCOUTZ1	TP7_PCOUTZ1
J02	PG14/PCOUTZ0	TP1_PCOUTZ0
J03	PG12/PCOUTA0	TP2_PCOUTA0
J05	VCC	VCC
J06	VSS	VSS
J08	VSS	VSS
J09	VCL	VCL
J10	VSS	VSS
J11	VSS	VSS
J12	VSS	VSS
J13	VCL	VCL
J15	P205/GTIOC5B	INV2 PWM_VL
J16	P202/GTIOC4A	INV2 PWM_UH
J18	AVSS_USBHS	AVSS_USBHS
J19	USBHS_DP	USBHS_DP
J20	USBHS_DM	USBHS_DM
K01	PG09/PCOUTA1	TP8_PCOUTA1
K02	PG10/PCOUTB1	TP10_PCOUTB1
K03	P705/ESC1_CAT1_ERXD1_A	ESC1_CAT1_ERXD1
K05	P704/ESC1_CAT1_ERXD0_A	ESC1_CAT1_ERXD0
K06	P702/ESC1_CATS1YNC1_A	ESC1_CATS1YNC1
K08	VCL	VCL
K09	VSS	VSS
K10	VCL	VCL
K11	VCL	VCL
K12	VCC	VCC
K13	VSS	VSS
K15	P207/GTIOC6B	INV2 PWM_WL
K16	P206/GTIOC6A	INV2 PWM_WH
K18	USBHS_RREF	USBHS_RREF
K19	VSS_USBHS	VSS_USBHS
K20	VCC_USBHS	VCC_USBHS
L01	PG08/GPIO/IRQ24	INV1_HALL_W
L02	VSS	VSS
L03	P707/ESC1_CAT1_ERXD3_A	ESC1_CAT1_ERXD3
L05	P706/ESC1_CAT1_ERXD2_A	ESC1_CAT1_ERXD2
L06	P703/ESC1_CAT1_LINKSTA_A	ESC1_CAT1_LINKSTA
L08	VSS	VSS
L09	VSS	VSS
L10	VCL	VCL
L11	VCL	VCL
L12	VSS	VSS
L13	VCC	VCC
L15	P310/GPIO	INV2_LED3
L16	P305/NMI	TP11_GPI_NMI
L18	AVCC_USBHS	AVCC_USBHS

Pin number	Pin function	Board function
L19	VCC	VCC
L20	VCC	VCC
M01	P701/ESC1_CATLATCH1_A	ESC1_CATLATCH1
M02	P715/GPIO/IRQ23	INV1_HALL_V
M03	P714/GPIO/IRQ22	INV1_HALL_U
M05	VCC	VCC
M06	VSS	VSS
M08	VCL	VCL
M09	VCL	VCL
M10	VSS	VSS
M11	VSS	VSS
M12	VSS	VSS
M13	VCL	VCL
M15	P312	TP22_GPIO_DSM0_DAT2
M16	P311	TP21_GPIO_DSM0_CLK2
M18	VSS	VSS
M19	P306/EXTAL	EXTAL
M20	P307/XTAL	XTAL
N01	P700/CLKOUT_ETHPHYCLK	CLKOUT_ETHPHYCLK
N02	P712/GTIOC14A/IRQ20	INV1_ENC_A
N03	P713/GTIOC14B/IRQ21	INV1_ENC_B
N05	VCC	VCC
N06	VSS	VSS
N08	VSS	VSS
N09	VCL	VCL
N10	VSS	VSS
N11	VSS	VSS
N12	VCL	VCL
N13	VSS	VSS
N15	P209/PCINB1	INV1_PCINB1
N16	P208/PCINA1	INV1_PCINA1
N18	P315/MD/FINED	DEBUGGER_MD
N19	VSS	VSS
N20	VSS	VSS
P01	PE06/GTIOC10A	INV1_PWM_PFC1/DRV_EN
P02	PE07/GTIOC10B	INV2_PWM_PFC1
P03	PF09/GPIO	CAT_MODULE2_LED
P05	VCC	VCC
P06	VSS	VSS
P15	P211/GPIO/IRQ27	INV2_OC(PFC)
P16	P210/PCINZ1	INV1_PCINZ1
P18	VSS	VSS
P19	P309/XCIN/EXCIN	TP14_GPI
P20	P308/XCOUT	TP16_GPI
R01	PE03/GTIOC8B	INV1_PWM_VL
R02	PE05/GTIOC9B	INV1_PWM_WL
R03	PF08/GPIO	CAT_MODULE1_LED
R05	P607/GTIOC15B	INV1_PWM_PFC2
R06	P606/GTIOC15A	INV2_PWM_PFC2
R07	VSS	VSS
R08	VSS	VSS
R09	VSS	VSS
R10	P906/GPIO	INV2_RELAY_IN/CON_MOT_SEL
R11	P405/AN005	INV2_IW(W_CURRENT)
R12	P404/AN004	INV1_IW(W_CURRENT)
R13	VSS	VSS
R14	VSS	VSS
R15	P215/PCINZ0	INV2_PCINZ0
R16	P214/PCINB0	INV2_PCINB0

Pin number	Pin function	Board function
R18	RES#	RES#
R19	VCC	VCC
R20	VBATT/VCC	VBATT/VCC
T01	PE01/GTIOC7B	INV1 PWM_UL
T02	PE04/GTIOC9A	INV1 PWM_WH
T03	PF07/SSLA3_C	INV1 SPI_CS
T05	P604/TXD9_B	RMW_PIN3_UART_TXD
T06	P602/MOSIA_D	SPI_PIN2_SPI_MOSI
T07	VCC2	VCC2
T08	VCC2	VCC2
T09	VCC2	VCC2
T10	P907/GPIO	INV1_RELAY_IN/CON_MOT_SEL
T11	P406/AN006	INV1_TEMPERATURE
T12	P407/AN007	INV2_TEMPERATURE
T13	VCC	VCC
T14	VCC	VCC
T15	P013/GPIO/IRQ29	INV2_HALL_V
T16	P012/GPIO/IRQ28	INV2_HALL_U
T18	P213/PCINA0	INV2_PCINA0
T19	P212/GPIO	INV2_LED2
T20	P000/AN016	ADC_AN016
U01	PE00/GTIOC7A	INV1_PWM_UH
U02	PE02/GTIOC8A	INV1_PWM_VH
U03	PF06/SSLA2_C	INV2_SPI_CS
U18	P008/GPIO	TP15_GPIO
U19	P009/GPIO/IRQ25	INV2_SW2/THERMAL_ERROR
U20	P001/AN017	ADC_AN017
V01	P709/GTETRGD/IRQ17	INV2_OC(INV)
V02	P711/GTETRGA/IRQ19	INV1_OC(INV)
V03	P605/RXD9_B	RMW_PIN2_UART_RXD
V04	P603/MISOA_D	SPI_PIN3_SPI_MISO
V05	P600/SSLA0_D	SPI_PIN1_SPI_CS
V06	P601/RSPCKA_D	SPI_PIN4_SPI_SCLK
V07	P905/GPIO/IRQ13	INV1_SAFE_LOCK
V08	P904/GPIO/IRQ12	INV1_INV_CONNECTED
V09	AVCC0	AVCC0
V10	AVSS0	AVSS0
V11	VREFL00 for ADC0	VREFL00 for ADC0
V12	VREFH00 for ADC0	VREFH00 for ADC0
V13	P412/AN012	INV1_IPFC1
V14	P413/AN013	INV2_IPFC1
V15	P414/AN014	INV1_VR/IPFC2
V16	P415/AN015	INV2_VR/IPFC2
V17	P014/GPIO/IRQ30	INV2_HALL_W
V18	P011/GPIO	INV2_LED1
V19	P010/GPIO/IRQ26	INV2_SW1
V20	P002/AN018	ADC_AN018
W01	P708/GTETRGC/IRQ16	INV1_ENC_Z/IPS_CSN
W02	P710/GTETRGA/IRQ18	INV2_ENC_Z/IPS_CSN
W03	PD05/AN029	INV2_IPS_A
W04	PD03/AN027	INV2_VW(W_VOLTAGE)
W05	VSS	VSS
W06	PD02/AN026	INV1_VW(W_VOLTAGE)
W07	P902/AN034	INV1_IPS_B#
W08	P900/AN032	INV1_IPS_B
W09	AVCC0	AVCC0
W10	AVSS0	AVSS0
W11	VREFL01 for ADC1	VREFL01 for ADC1
W12	VREFH01 for ADC1	VREFH01 for ADC1

Pin number	Pin function	Board function
W13	P403/AN003	INV2 IV(V_CURRENT)
W14	P401/AN001	INV2 IU(U_CURRENT)
W15	P411/AN011	INV2 VAC(VPFC)
W16	P409/AN009	INV2 VDC(BUS_VOLTAGE)
W17	VSS	VSS
W18	P006/AN022	INV1 VU(U_VOLTAGE)
W19	P004/AN020	ADC_AN020
W20	P003/AN019	ADC_AN019
Y01	VSS	VSS
Y02	PD07/AN031	INV2 IPS_A#
Y03	PD06/AN030	INV1 IPS_A#
Y04	PD04/AN028	INV1 IPS_A
Y05	PD01/AN025	INV2 VV(V_VOLTAGE)
Y06	PD00/AN024	INV1 VV(V_VOLTAGE)
Y07	P903/AN035	INV2 IPS_B#
Y08	P901/AN033	INV2 IPS_B
Y09	AVCC0	AVCC0
Y10	AVSS0	AVSS0
Y11	VREFL02 for ADC2	VREFL02 for ADC2
Y12	VREFH02 for ADC2	VREFH02 for ADC2
Y13	P402/AN002	INV1 IV(V_CURRENT)
Y14	P400/AN000	INV1 IU(U_CURRENT)
Y15	P410/AN010	INV1 VAC(VPFC)
Y16	P408/AN008	INV1 VDC(BUS_VOLTAGE)
Y17	P015/GPIO	TP17_GPIO
Y18	P007/AN023	INV2 VU(U_VOLTAGE)
Y19	P005/AN021	ADC_AN021
Y20	VSS	VSS

7. Communication Board Specification

This section describes the specification of the communication board.

7.1 Functions

7.1.1 Power supply

Power of this product is supplied at 5V from USB connector.

7.1.2 USB communication

This product is equipped with a USB Type-C connector for communication with a PC when using Renesas Motor Workbench, etc.

7.1.3 Serial communication

This board has two SCI connectors for serial communication with the target MCU when using Renesas Motor Workbench, etc. The pin assignments are shown in Table 7-1 and Table 7-2. When using the communication cable bundled with this product, use CN5.

The serial communication connector and the MCU (RX72N) are connected via a digital isolator, so the communication board and the CPU board with the target MCU are isolated.

Table 7-1 SCI connector (CN5) pin assignment

Pin No.	Function	Note
1	VCC	
2	RXD	Connect to TXD of target MCU
3	TXD	Connect to RXD of target MCU
4	GND	

Table 7-2 SCI connector (CN4) pin assignment

Pin No.	Function	Note
1	VCC	
2	RXD	Connect to TXD of target MCU
3	TXD	Connect to RXD of target MCU
4	GND	

8. Regulatory information

This product meets the following standards. Refer to “General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products” of this Evaluation Kit Manual for the precautions.

8.1 EMC Directive



This product is herewith confirmed to comply with the requirements set out in the Council Directives on the Approximation of the laws of the Member States relating to Electromagnetic Compatibility Directive 2014/30/EU.



This product is in conformity with the following relevant UK Statutory Instrument(s) (and its amendments): 2016 No. 1091 Electromagnetic Compatibility Regulations 2016.

Warning – This is a Class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures to correct this interference.

8.2 Material Selection, Waste, Recycling and Disposal Standards

China SJ/T 11364-2014, 10-year environmental protection use period.

WEEE Directive (2012/19/EU) & The Waste Electrical and Electronic Equipment Regulations 2013



The WEEE (Waste Electrical and Electronic Equipment) regulations put responsibilities on producers for the collection and recycling or disposal of electrical and electronic waste. Return of WEEE under these regulations is applicable in the UK and European Union.

This product (including all accessories) is not intended for household use. After use the product cannot be disposed of as household waste, and the WEEE must be treated, recycled and disposed of in an environmentally sound manner.

Renesas Electronics Europe GmbH can take back end of life product. Register for this service at; <https://www.renesas.com/form/weee-questionnaire>

9. Design and Manufacture Information

The design resources for the MCK-RX74M (schematics, PCB layout data, BOM, etc.) and software development tools (Renesas Motor Workbench, QE for Motor, sample code, application note, etc.) are available for download from the following website: [renesas.com/mck-rx74m](https://www.renesas.com/mck-rx74m)

10. Website and Support

In order to learn, download tools and documents, apply technical support for RX family MCU and its kit, visit the below Web site.

- RX Product Information [renesas.com/rx](https://www.renesas.com/rx)
- Renesas Support [renesas.com/support](https://www.renesas.com/support)

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		Page	Summary
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