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April 1st, 2010 Renesas Electronics Corporation

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Evaluation Board Information

EC-NE3510M04 2.4 GHz LNA Evaluation Board

- Evaluation Board Pattern Layout
- Circuit Description
- Evaluation Board Test Results
- Gain and Isolation
- Input and Output Return Loss
- 1 dB Gain Compression Output Power
- Pin-Pout & IM3 Performance
- Stability Factor

Reference Design Data

- Frequency Feature of NF
- Relations of C1 with NF, Input Return Loss and Associated Gain

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Date Published November 2007 NS

For the purposes of maintaining up-to-date information, the contents of this document are subject to change without notice.

This document outlines general applications for this product. The application circuits and circuit constants provided in this document are simply examples and should not be used for mass production design. Be aware also that there is no intention to standardize the restrictions and characteristics of these application circuits.

The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

Customers are requested to confirm all characteristics when designing a system based in part or wholly on the information in this document.

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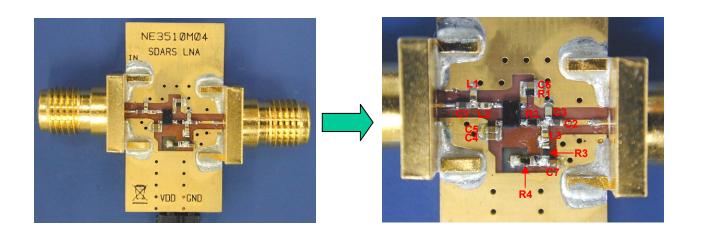
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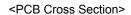
(Note)

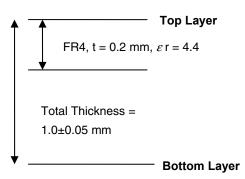
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M8E 02.11-1

Evaluation Board Pattern Layout





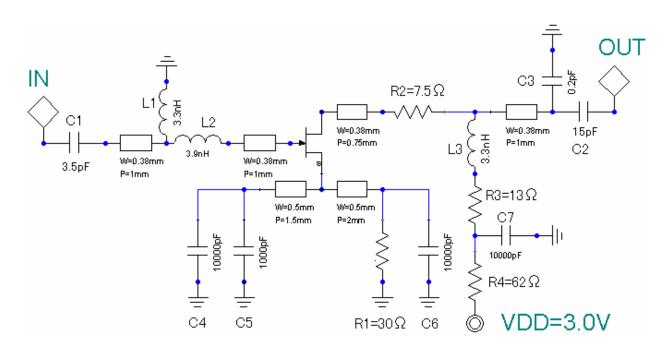


 $\underline{\text{size}} \hspace{1.5cm} 12.0 \hspace{0.1cm} \text{mm} \times 24.0 \hspace{0.1cm} \text{mm}$

 $\frac{\text{material}}{\text{material}} \quad \text{FR4 (ELC4756/Sumitomo)} \\ t = 0.2 \text{ mm, } \varepsilon \text{ r} = 4.4$

Circuit Description

 $(V_{DD} = 3.0 \text{ V}, I_D = 14.3 \text{ mA}, f = 2.4 \text{ GHz})$



COMPONENTS OF TEST CIRCUIT

Parts	Part Number	Maker	Symbol	Value	Unit
Chip Capacitor	GRM1553C1H3R5CZ01	Murata	C1	3.5	pF
Chip Capacitor	GRM1552C1H150JZ01	Murata	C2	15	pF
Chip Capacitor	GRM1554C1HR20KZ01	Murata	C3	0.2	pF
Chip Capacitor	GRP155B11E103KA01	Murata	C4, C6, C7	10 000	pF
Chip Capacitor	GRM155B11H102KA01	Murata	C5	1 000	pF
Chip Inductor	AML1005H3N3STS	FDK	L1, L3	3.3	nΗ
Chip Inductor	AML1005H3N9STS	FDK	L2	3.9	nΗ
Chip Resistor	MCR01MZPJ300	ROHM	R1	30	Ω
Chip Resistor	MCR01MZPJ7R5	ROHM	R2	7.5	Ω
Chip Resistor	MCR01MZPJ130	ROHM	R3	13	Ω
Chip Resistor	MCR01MZPJ620	ROHM	R4	62	Ω
Transistor	NE3510M04	NEC	TR		
DC Connector	A2-2PA-2.54DSA (71)	Hirose			
RF Connector	01K2266-00	WAKA			
Substrate	FR4 (t = 0.2 mm)	Sumitomo			

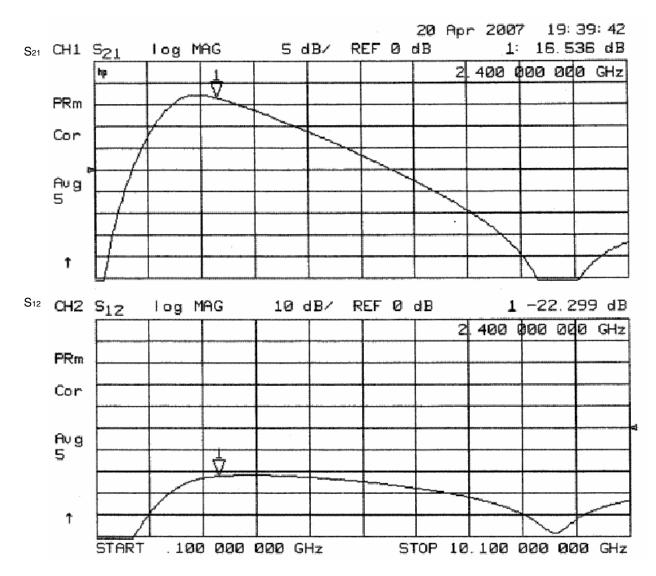
Evaluation Board Test Results

 $(V_{DD} = 3.0 \text{ V}, I_D = 14.3 \text{ mA}, f = 2.4 \text{ GHz})$

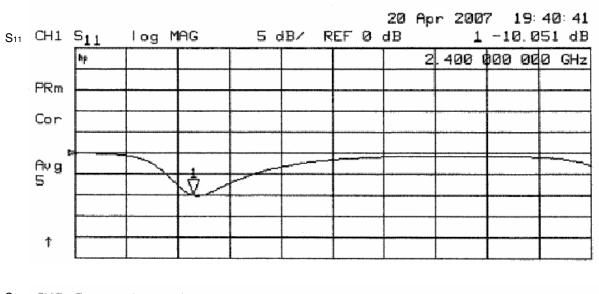
Item	Symbol	Data	Unit	
Noise Figure	NF Note	0.57	dB	
Associated Gain	Ga	16.4	dB	
Input Return Loss	RLin	10.1	dB	
Output Return Loss	RLout	13.9	dB	
Output Power at 1 dB Compression Point	Po (1 dB)	7.4	dBm	
Input 3rd Order Intercept Point	IIP₃	6.0	dBm	

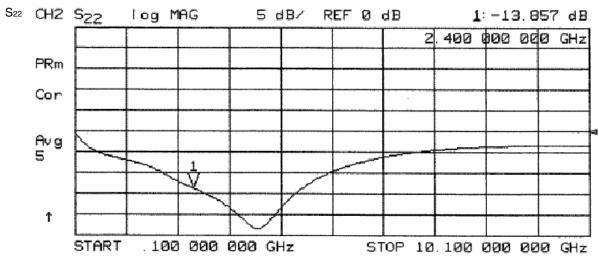
Note The loss of the substrate is included in the value of NF.

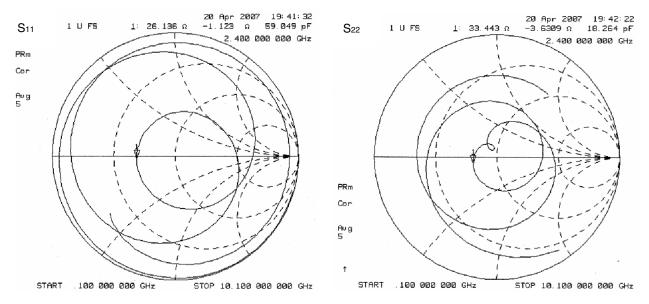
Gain and Isolation



Input and Output Return Loss

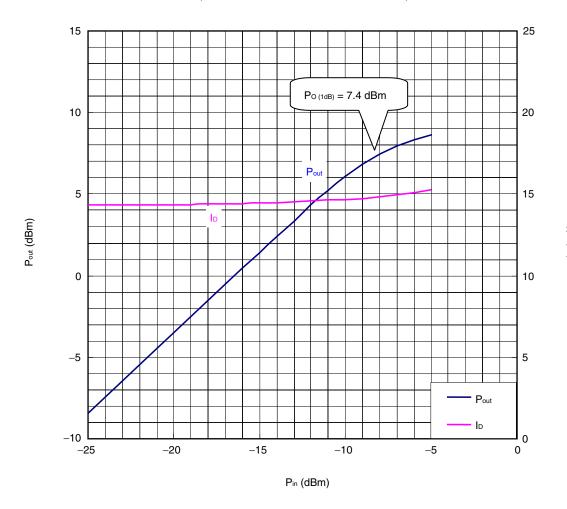






1 dB Gain Compression Output Power

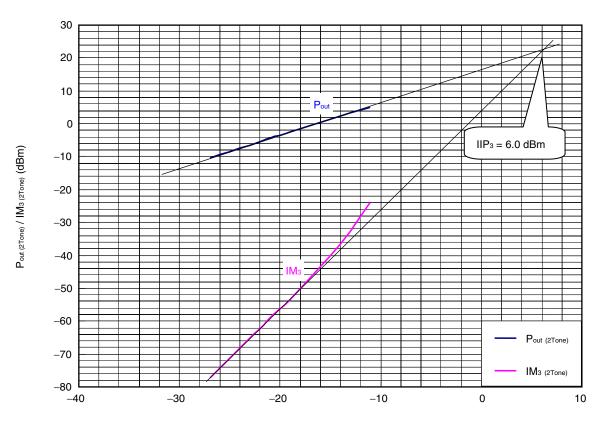
 $(V_{DD} = 3.0 \text{ V}, I_D = 14.3 \text{ mA}, f = 2.4 \text{ GHz})$



8

Pin-Pout & IM3 Performance

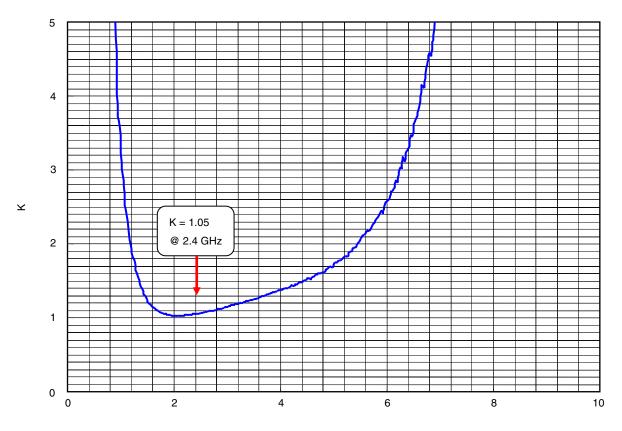
 $(V_{DD} = 3.0 \text{ V}, I_D = 14.3 \text{ mA}, f = 2.4 \text{ GHz}, 1 \text{ MHz offset})$



Pin (2Tone) (dBm)

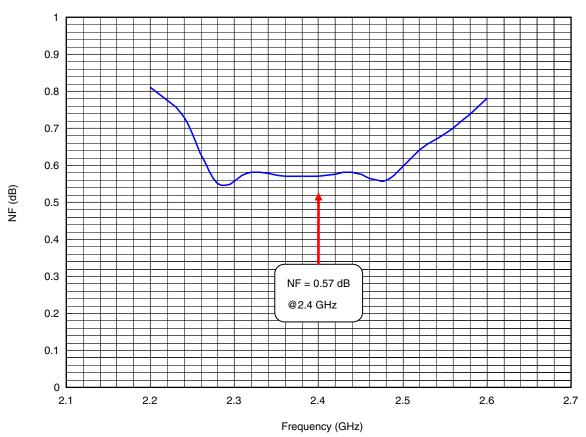
Stability Factor

K factor

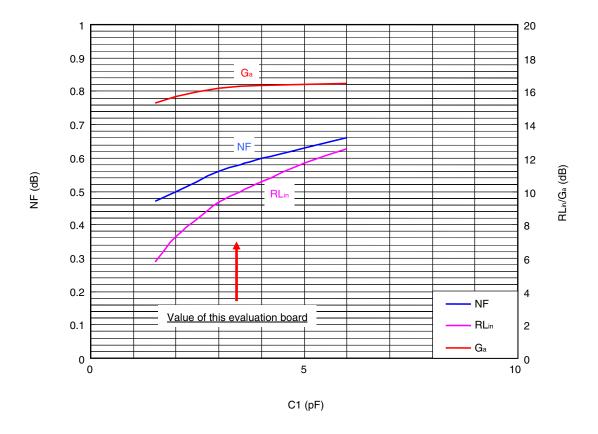


Reference Design Data

Frequency Feature of NF



Relations of C1 with NF, Input Return Loss and Associated Gain



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