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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Evaluation Board Information

EC-NE3508M04 (Ver2) 1.575 GHz LNA Evaluation Board for GPS Application (NF optimized)

- **Evaluation Board Pattern Layout**
- **Circuit Description**
- **Evaluation Board Test Results**
- **Gain and Isolation**
- **Input and Output Return Loss**
- **1 dB Gain Compression Output Power**
- **P_{in}-P_{out} & IM₃ Performance**
- **Stability Factor**

Reference Design Data

- **Frequency Feature of NF**
- **Relations of C3 with NF and Input Return Loss**

Caution	GaAs Products	<p>This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.</p> <ul style="list-style-type: none">• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.<ol style="list-style-type: none">1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.• Do not burn, destroy, cut, crush, or chemically dissolve the product.• Do not lick the product or in any way allow it to enter the mouth.
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This document outlines general applications for this product. The application circuits and circuit constants provided in this document are simply examples and should not be used for mass production design. Be aware also that there is no intention to standardize the restrictions and characteristics of these application circuits.

The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

Customers are requested to confirm all characteristics when designing a system based in part or wholly on the information in this document.

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M8E 02.11-1

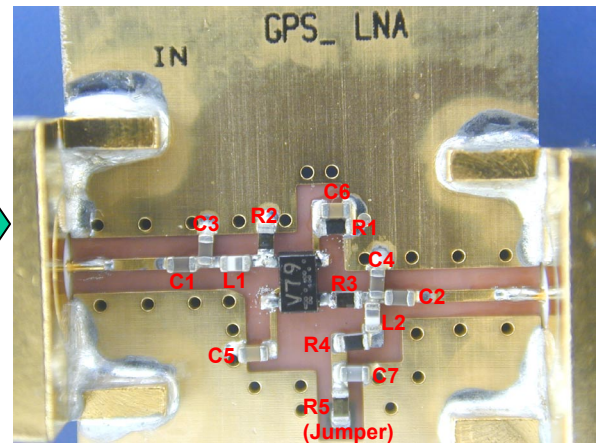
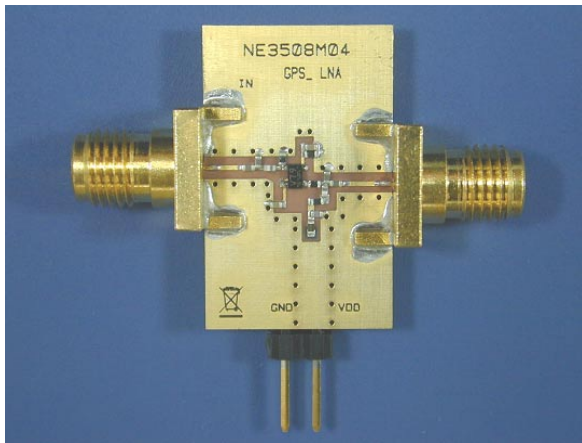
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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

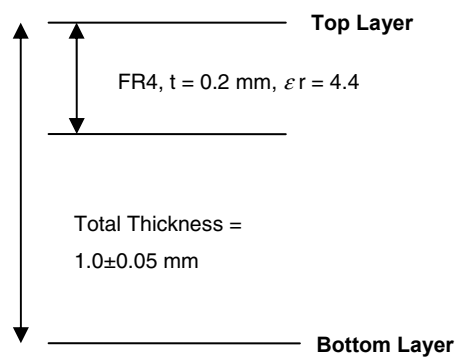
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Evaluation Board Pattern Layout

<Top View>



<PCB Cross Section>



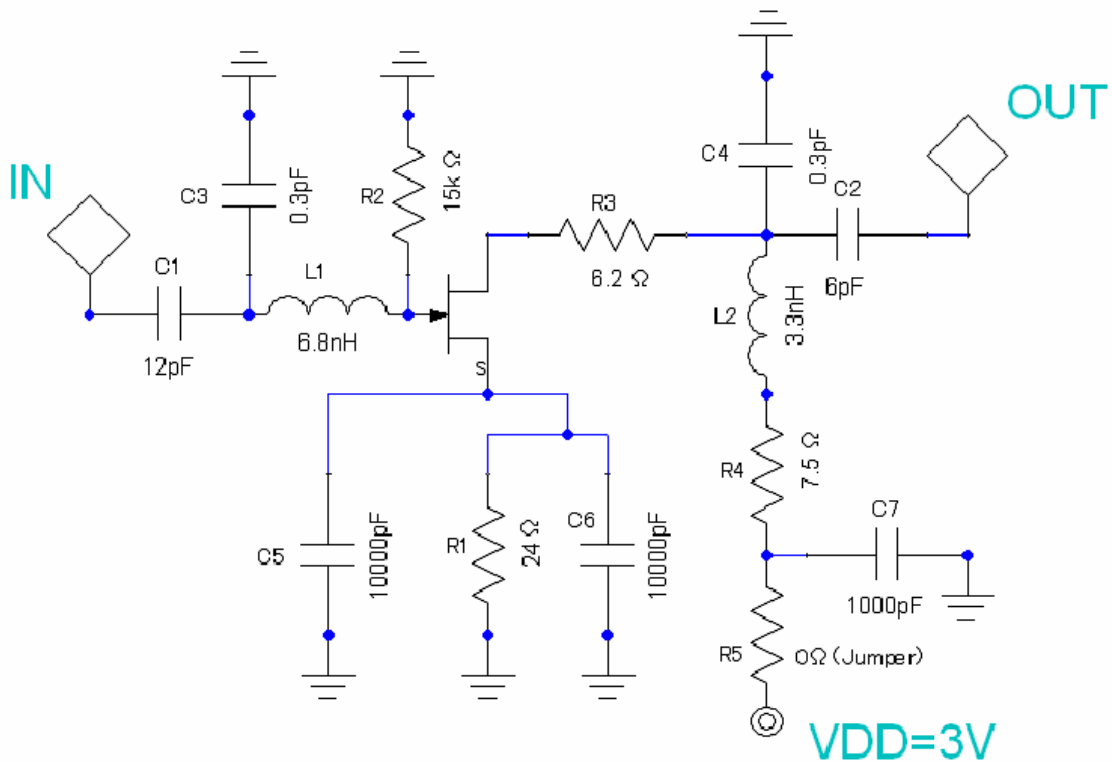
size 15.0 mm × 24.0 mm

material FR4 (ELC4756/Sumitomo)
t = 0.2 mm, εr = 4.4

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Circuit Description

(V_{DD} = 3.0 V, I_b = 11.4 mA, f = 1.575 GHz (NF optimized))



COMPONENTS OF TEST CIRCUIT

Parts	Part Number	Maker	Symbol	Value	Unit
Chip Capacitor	GRM1552C1H120JZ01	Murata	C1	12	pF
Chip Capacitor	GRM1552C1H6R0DZ01	Murata	C2	6	pF
Chip Capacitor	GRM1554C1HR30CZ01	Murata	C3, C4	0.3	pF
Chip Capacitor	GRP155B11E103KA01	Murata	C5, C6	10 000	pF
Chip Capacitor	GRM155B11H102KA01	Murata	C7	1 000	pF
Chip Inductor	AML1005H6N8JTS	FDK	L1	6.8	nH
Chip Inductor	AML1005H3N3STS	FDK	L2	3.3	nH
Chip Resistor	MCR01MZPJ240	ROHM	R1	24	Ω
Chip Resistor	MCR01MZPJ153	ROHM	R2	15	kΩ
Chip Resistor	MCR01MZPJ6R2	ROHM	R3	6.2	Ω
Chip Resistor	MCR01MZPJ7R5	ROHM	R4	7.5	Ω
Chip Resistor	MCR01MZPJ000	ROHM	R5	0	Ω (Jumper)
Transistor	NE3508M04	NEC	TR		
DC Connector	A2-2PA-2.54DSA (71)	Hirose			
RF Connector	01K2266-00	WAKA			
Substrate	FR4 (t = 0.2 mm)	Sumitomo			

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Evaluation Board Test Results

($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$, $f = 1.575\text{ GHz}$)

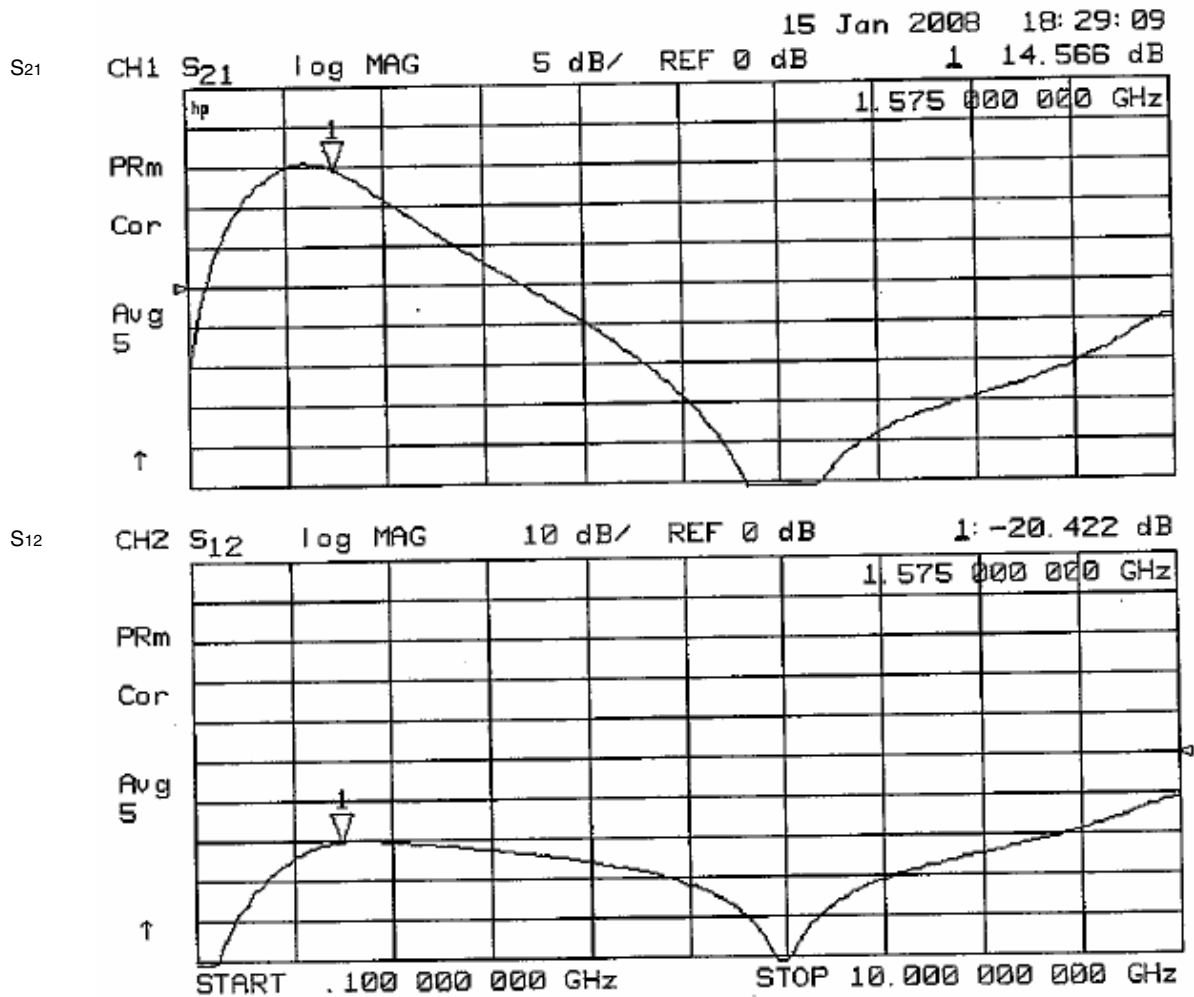
Item	Symbol	Data	Unit
Noise Figure	NF ^{Note}	0.62	dB
Associated Gain	G _a	14.6	dB
Input Return Loss	RL _{in}	8.8	dB
Output Return Loss	RL _{out}	23.3	dB
Output Power at 1 dB Compression Point	P _{O (1 dB)}	9.8	dBm

Note A substrate loss 0.08 dB is including in value of NF.

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Gain and Isolation

($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$, $f = 1.575\text{ GHz}$)

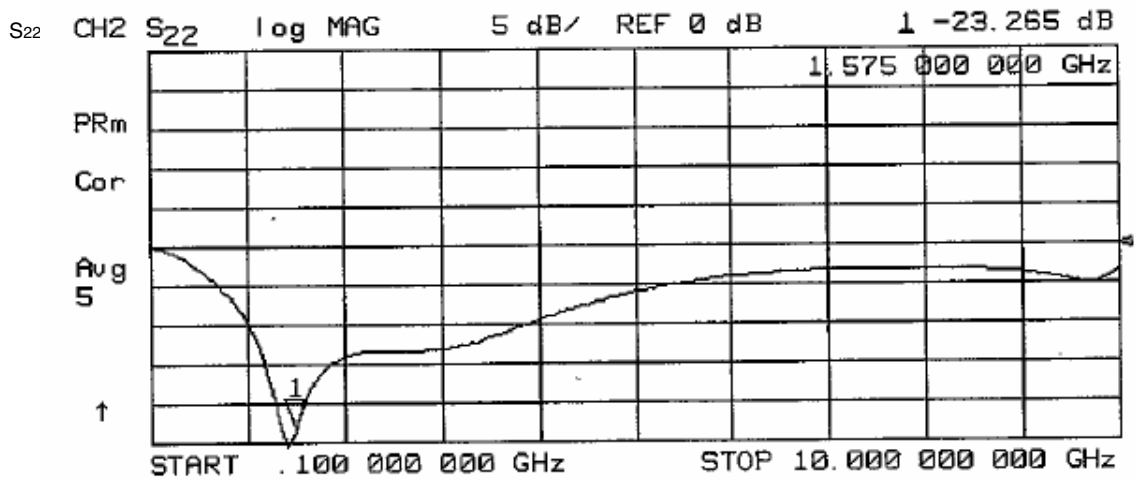
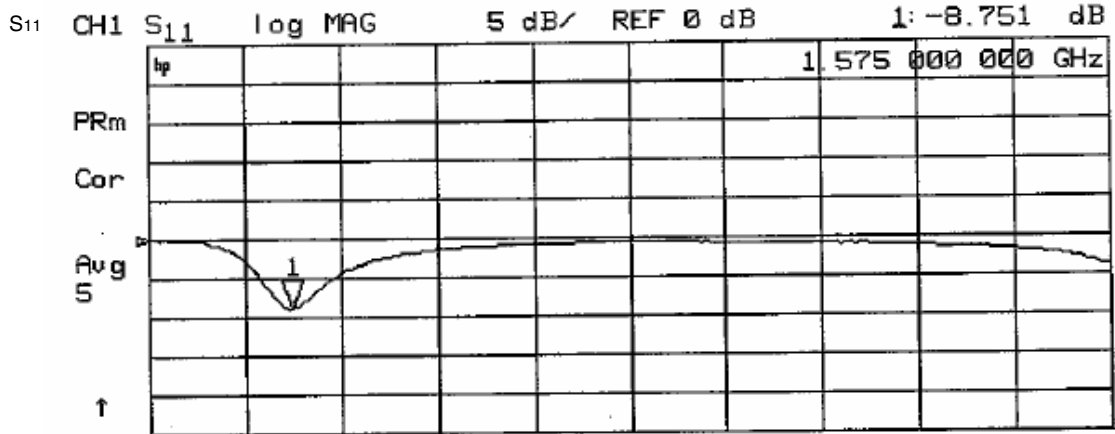


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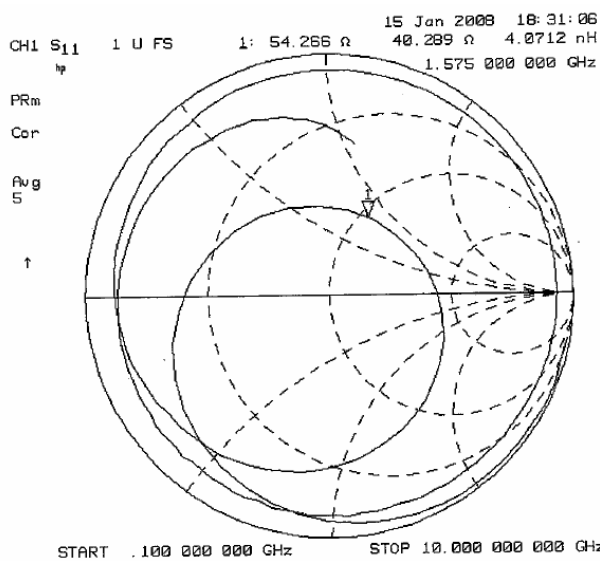
Input and Output Return Loss

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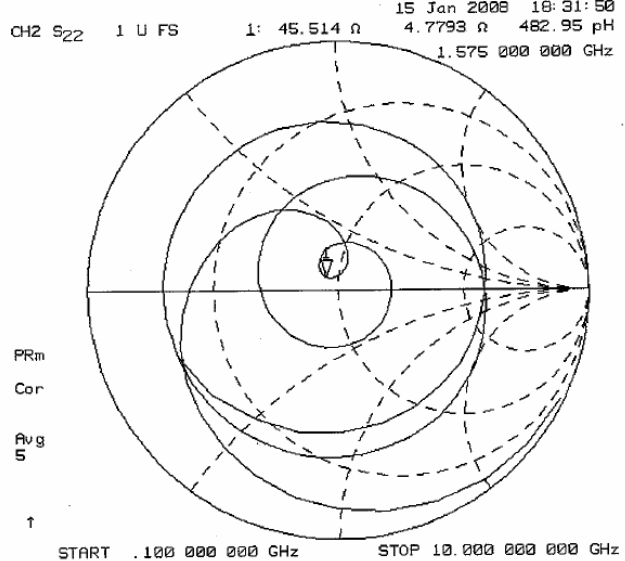
15 Jan 2008 18:30:16



S11



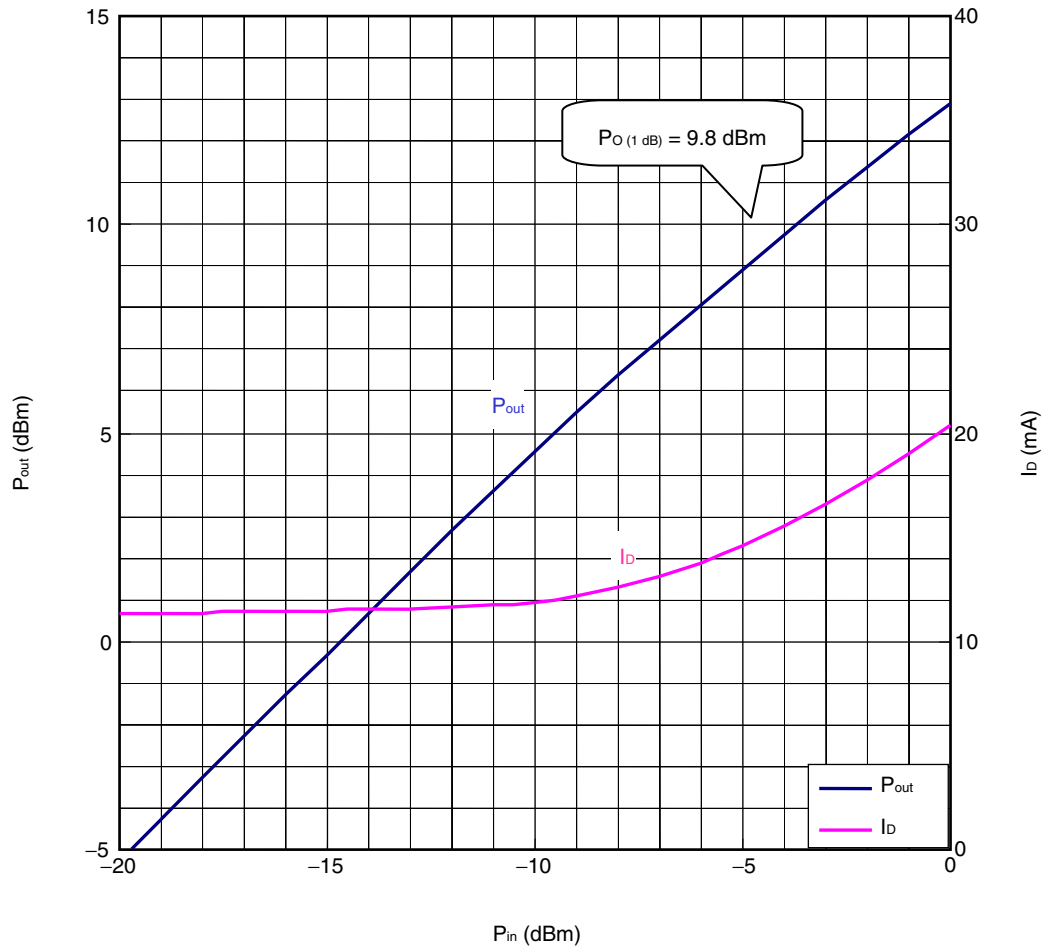
S22



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1 dB Gain Compression Output Power

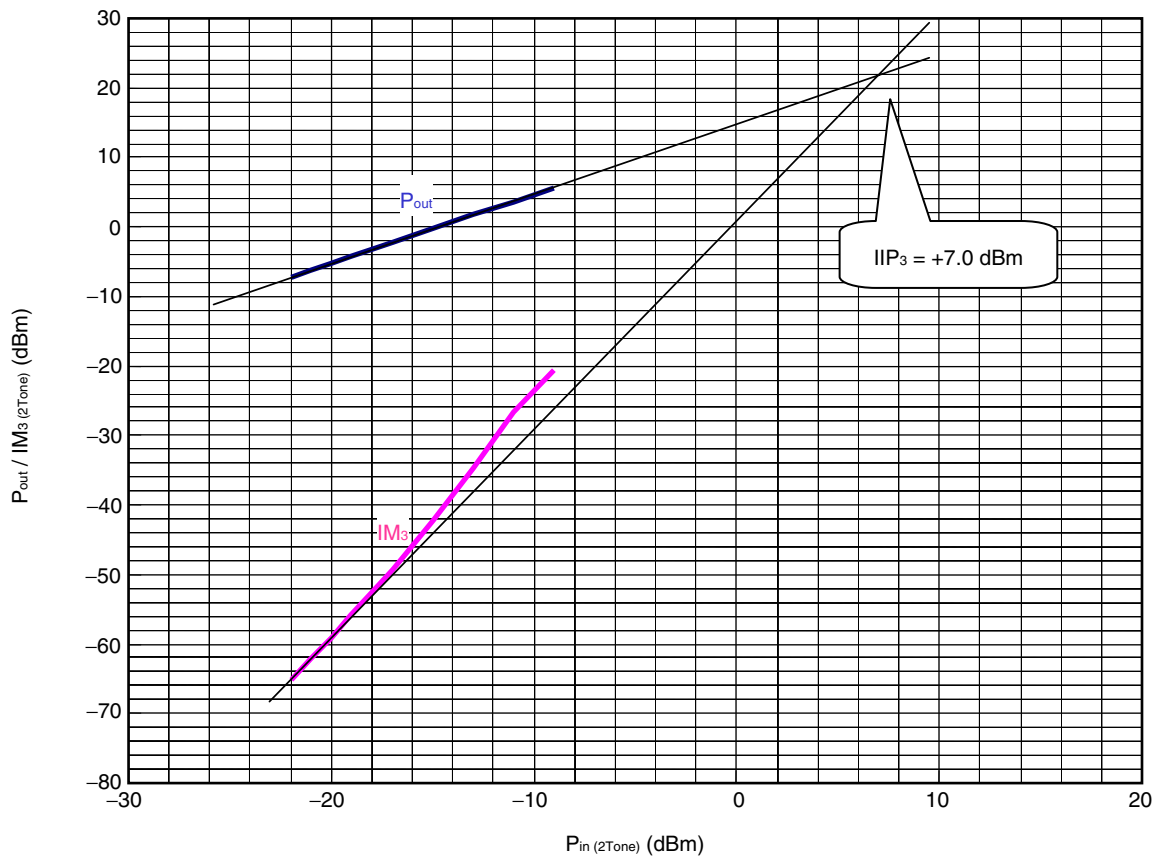
($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$, $f = 1.575\text{ GHz}$)



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Pin-Pout & IM3 Performance

(V_{DD} = 3.0 V, I_D = 11.4 mA, f = 1.575 GHz, 1 MHz offset)

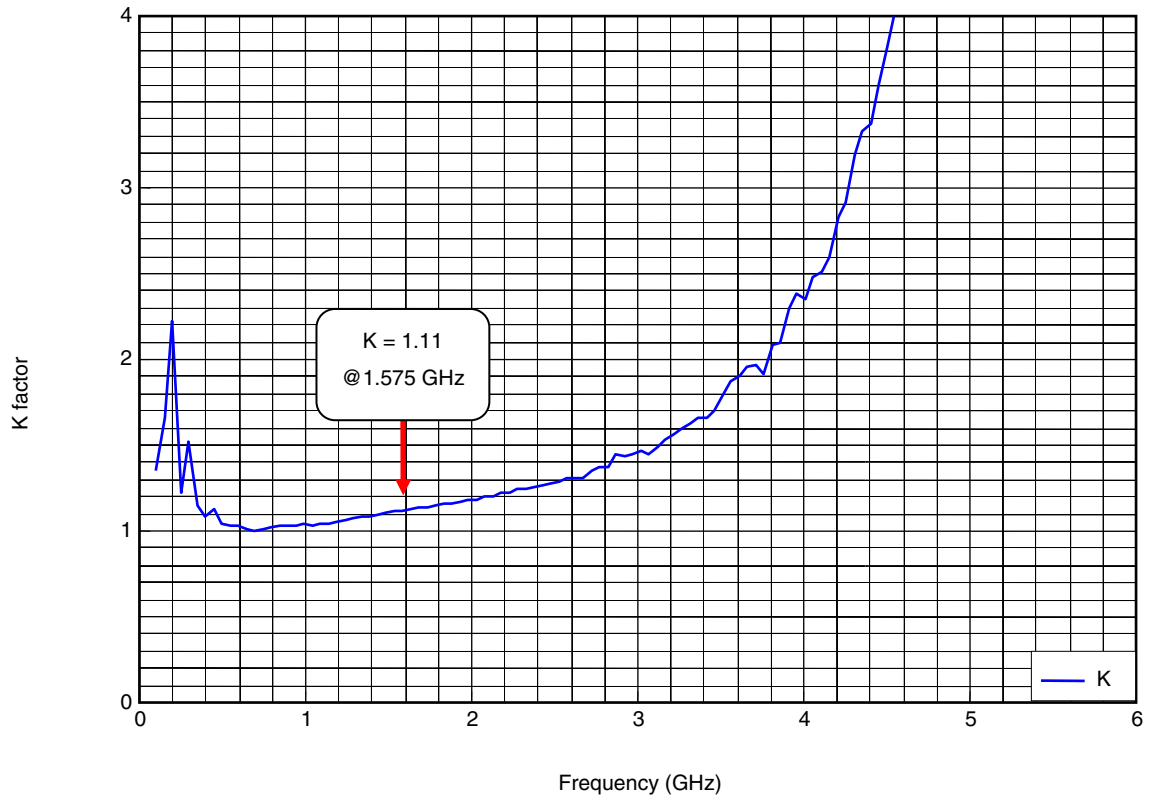


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Stability Factor

K factor

($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$)

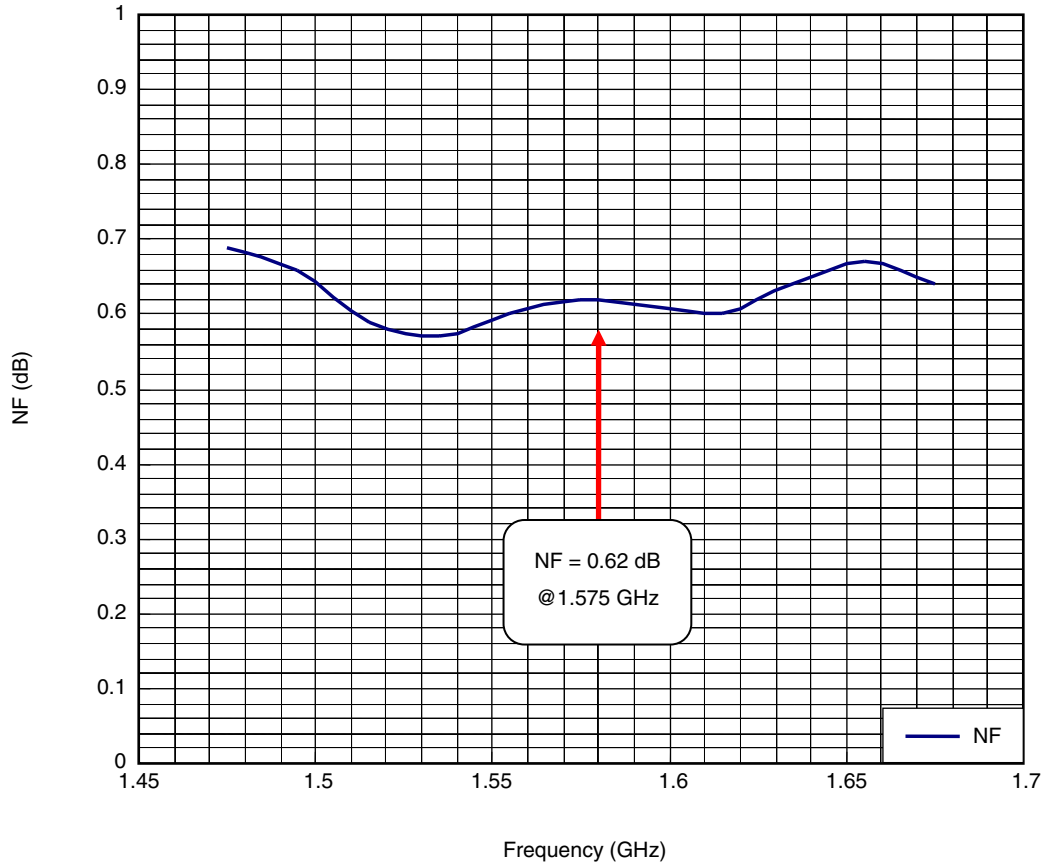


Reference Design Data

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Frequency Feature of NF

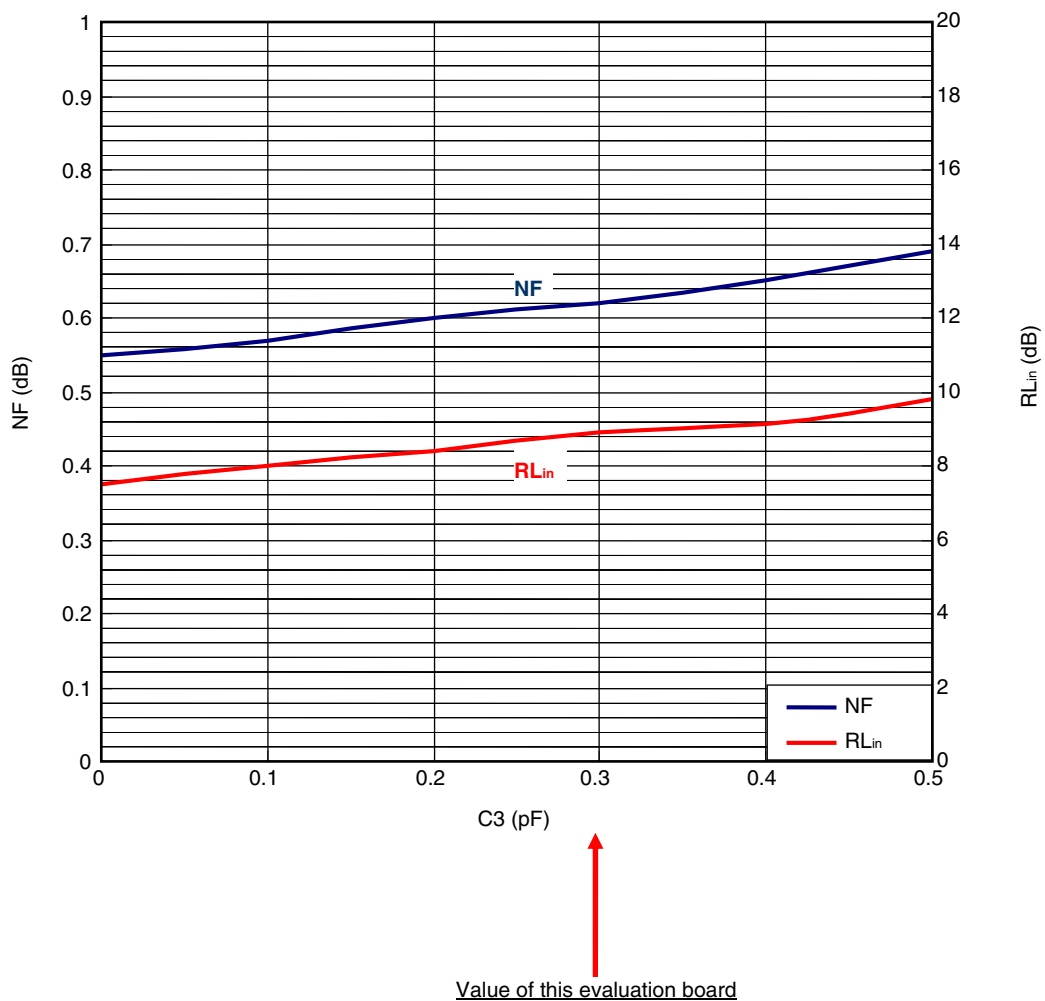
($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$)



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Relations of C3 with NF and Input Return Loss

($V_{DD} = 3.0\text{ V}$, $I_D = 11.4\text{ mA}$, $f = 1.575\text{ GHz}$)



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