

RX72T Group

Renesas Starter Kit for RX72T
Smart Configurator Tutorial Manual
For CS+

RENESAS 32-Bit MCU
RX Family / RX700 Series

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1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

¾ The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

¾ The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

¾ The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

¾ When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

¾ The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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The following precautions should be observed when operating any RSK product:

This Renesas Starter Kit is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures;

- ensure attached cables do not lie across the equipment
- reorient the receiving antenna
- increase the distance between the equipment and the receiver
- connect the equipment into an outlet on a circuit different from that which the receiver is connected
- power down the equipment when not in use
- consult the dealer or an experienced radio/TV technician for help NOTE: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken;

- The user is advised that mobile phones should not be used within 10m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Renesas Starter Kit does not represent an ideal reference design for an end product and does not fulfil the regulatory standards for an end product.

How to Use This Manual

1. Purpose and Target Readers

This manual is designed to provide the user with an understanding of how to use Application Leading Tool (Smart Configurator) for RX together with the CS+ IDE to create a working project for the RSK platform. It is intended for users designing sample code on the RSK platform, using the many different incorporated peripheral devices.

The manual comprises of step-by-step instructions to generate code and import it into CS+, but does not intend to be a complete guide to software development on the RSK platform. Further details regarding operating the RX72T microcontroller may be found in the Hardware Manual and within the provided sample code.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

In this manual, the display may differ slightly from screen shots. There is no problem in reading this manual.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the RX72T Group. Make sure to refer to the latest versions of these documents. The newest versions of the documents listed may be obtained from the Renesas Electronics Web site.

Document Type	Description	Document Title	Document No.
User's Manual	Describes the technical details of the RSK hardware.	Renesas Starter Kit for RX72T User's Manual	R20UT4272EG
Tutorial Manual	Provides a guide to setting up RSK environment, running sample code and debugging programs.	Renesas Starter Kit for RX72T Tutorial Manual	R20UT4273EG
Quick Start Guide	Provides simple instructions to setup the RSK and run the first sample.	Renesas Starter Kit for RX72T Quick Start Guide	R20UT4274EG
Smart Configurator Tutorial Manual	Provides a guide to code generation and importing into the CS+ IDE.	Renesas Starter Kit for RX72T Smart Configurator Tutorial Manual	R20UT4275EG
Schematics	Full detail circuit schematics of the RSK.	Renesas Starter Kit for RX72T Schematics	R20UT4271EG
Hardware Manual	Provides technical details of the RX72T microcontroller.	RX72T Group Hardware Manual	R01UH0803EJ

2. List of Abbreviations and Acronyms

Abbreviation	Full Form
ADC	Analog-to-Digital Converter
API	Application Programming Interface
bps	bits per second
CMT	Compare Match Timer
COM	COMmunications port referring to PC serial port
CPU	Central Processing Unit
E1/E2 Lite	Renesas On-chip Debugging Emulator
GUI	Graphical User Interface
IDE	Integrated Development Environment
IRQ	Interrupt Request
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LSB	Least Significant Bit
LVD	Low Voltage Detect
MCU	Micro-controller Unit
MSB	Most Significant Bit
PC	Personal Computer
PLL	Phase-locked Loop
Pmod™	This is a Digilent Pmod™ Compatible connector. Pmod™ is registered to Digilent Inc. Digilent-Pmod_Interface_Specification
PSU	Power Supply Unit
RAM	Random Access Memory
ROM	Read Only Memory
RSK	Renesas Starter Kit
RTC	Real Time Clock
SCI	Serial Communications Interface
SPI	Serial Peripheral Interface
TFT	Thin Film Transistor
UART	Universal Asynchronous Receiver/Transmitter
USB	Universal Serial Bus
WDT	Watchdog Timer

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Table of Contents

1. Overview.....	8
1.1 Purpose.....	8
1.2 Features.....	8
2. Introduction.....	9
3. Project Creation with CS+.....	10
3.1 Introduction.....	10
3.2 Creating the Project.....	10
4. Smart Configurator Using the CS+.....	12
4.1 Introduction.....	12
4.2 Project Configuration using Smart Configurator - Overview page.....	13
4.3 Board configuration page.....	14
4.3.1 Board configuration.....	14
4.4 Clocks configuration page.....	15
4.4.1 Clocks configuration.....	15
4.5 Component page.....	16
4.5.1 Add a software component into the project.....	16
4.5.2 Compare Match Timer.....	17
4.5.3 Interrupt Controller.....	20
4.5.4 Ports.....	22
4.5.5 SCI/SCIF Asynchronous Mode.....	26
4.5.6 SPI Clock Synchronous Mode.....	29
4.5.7 Single Scan Mode S12AD.....	32
4.6 Pins configuration page.....	35
4.6.1 Change pin assignment of a software component.....	35
5. Completing the Tutorial Project.....	39
5.1 Project Settings.....	39
5.2 Additional Folders.....	41
5.3 LCD Code Integration.....	42
5.3.1 SPI Code.....	45
5.3.2 CMT Code.....	46
5.4 Switch Code Integration.....	47
5.4.1 Interrupt Code.....	47
5.4.2 De-bounce Timer Code.....	50
5.4.3 Main Switch and ADC Code.....	51
5.5 Debug Code Integration.....	56
5.6 UART Code Integration.....	56
5.6.1 SCI Code.....	56
5.6.2 Main UART code.....	58
5.7 LED Code Integration.....	60
6. Debugging the Project.....	63
7. Running the Smart Configurator Tutorial.....	64
7.1 Running the Tutorial.....	64
8. Additional Information.....	65

1. Overview

1.1 Purpose

This RSK is an evaluation tool for Renesas microcontrollers. This manual describes how to use the CS+ IDE Smart Configurator to create a working project for the RSK platform.

1.2 Features

This RSK provides an evaluation of the following features:

- Project Creation with CS+
- Code generation using the Smart Configurator.
- User circuitry such as switches, LEDs and a potentiometer

The RSK board contains all the circuitry required for microcontroller operation.

2. Introduction

This manual is designed to answer, in tutorial form, how to use the Smart Configurator for the RX family together with the CS+ IDE to create a working project for the RSK platform. The tutorials help explain the following:

- Project generation using the CS+
- Detailed use of the Smart Configurator for CS+
- Integration with custom code
- Building the project CS+

The project generator will create a tutorial project with three selectable build configurations:

- 'DefaultBuild' is a project with debug support and optimisation level set to two.
- 'Debug' is a project built with the debugger support included. Optimisation is set to zero.
- 'Release' is a project with optimised compile options (level two) and no 'Outputs debugging information' options not selected, producing code suitable for release in a product.

The tutorial examples in this manual assume that installation procedures described in the RSK Quick Start Guide have been completed. Please refer to the Quick Start Guide for details of preparing the configuration.

These tutorials are designed to show you how to use the RSK and are not intended as a comprehensive introduction to the CS+ debugger, compiler toolchains or the E2 emulator Lite. Please refer to the relevant user manuals for more in-depth information.

3. Project Creation with CS+

3.1 Introduction

In this section, the user will be guided through the steps required to create a new C project for the RX72T MCU, ready to generate peripheral driver code using Smart Configurator. This project generation step is necessary to create the MCU-specific project and debug files.

3.2 Creating the Project

To use the program, start CS+:

Windows™ 7: Start Menu > All Programs > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)

Windows™ 8.1 & 8: From Apps View , click 'CS+ for CC (RL78,RX,RH850)' icon

Windows™ 10: Start Menu > All Apps > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)

- CS+ will show the Start Page. Use the 'GO' button to Create a New Project.

Create New Project

GO

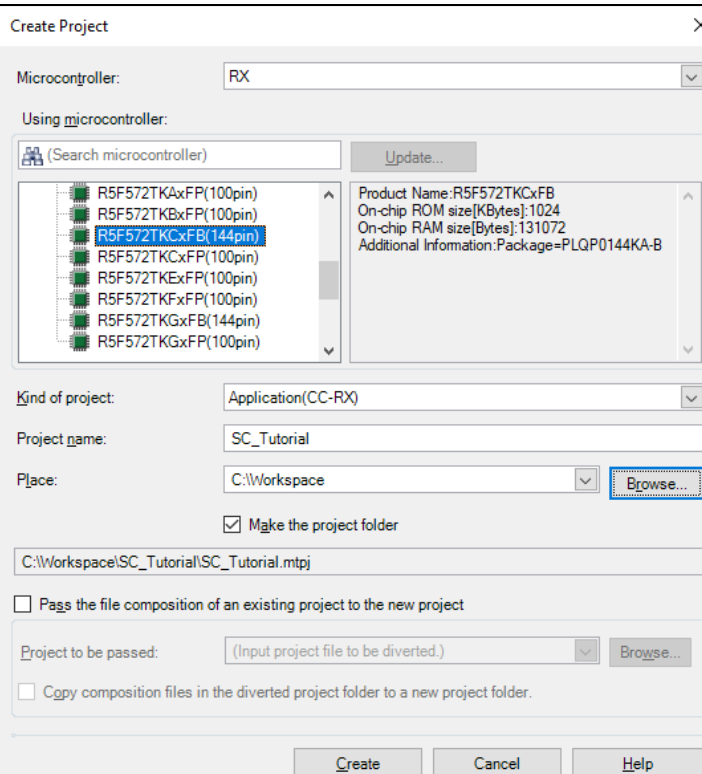
A new project can be created.
A new project can also be created by reusing the file configuration registered to an existing project.

- In the 'Create Project' dialog, select 'RX' from the 'Microcontroller' pull-down.
- In the 'Using Microcontroller' list control, scroll down to 'RX72T' and expand the tree control by clicking '+'. Select 'R5F572TKCxFB(144pin)'.

- Ensure that in the 'Kind of project' pull-down, 'Application(CC-RX)' is selected.
- Choose an appropriate name and location for the project, then click 'Create'.

Note: this tutorial assumes the project is named and located at the place shown opposite.

- If the folder entered cannot be found a 'Question' dialog will be displayed; click 'Yes'.

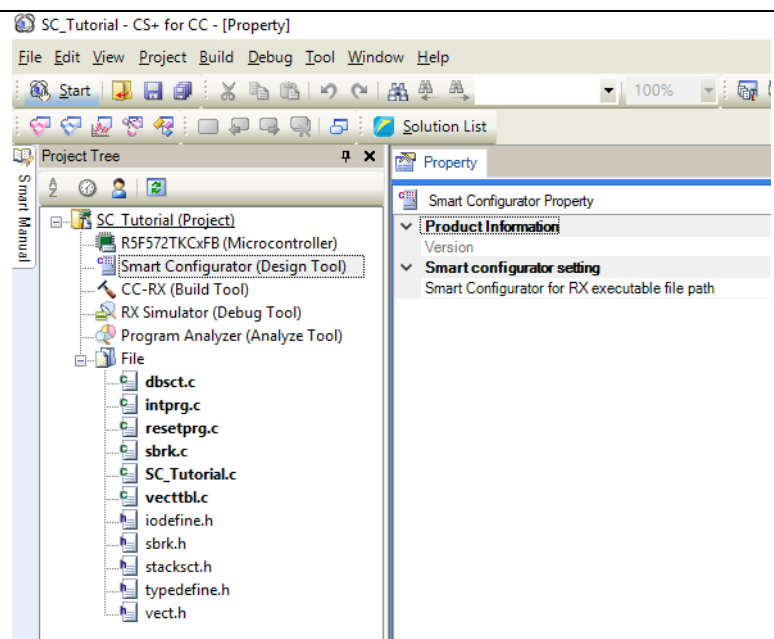


The 'Create Project' dialog box is shown with the following settings:

- Microcontroller:** RX
- Using microcontroller:** A tree view shows the selection path: RX72T > R5F572TKCxFB(144pin). The selected item is highlighted in blue.
- Product Name:** R5F572TKCxFB
- On-chip ROM size[KBytes]:** 1024
- On-chip RAM size[Bytes]:** 131072
- Additional Information:** Package=PLQP0144KA-B
- Kind of project:** Application(CC-RX)
- Project name:** SC_Tutorial
- Place:** C:\Workspace
- ☒ **Make the project folder**
- Project path:** C:\Workspace\SC_Tutorial\SC_Tutorial.mtpj
- ☐ **Pass the file composition of an existing project to the new project**
- Project to be passed:** (Input project file to be diverted.)
- ☐ **Copy composition files in the diverted project folder to a new project folder.**

Buttons at the bottom: Create, Cancel, Help.

- CS+ will create the blank project with the standard project tree. A 'Smart Configurator' node may also be shown, if previously enabled.



4. Smart Configurator Using the CS+

4.1 Introduction

The Smart Configurator for the RX72T has been used to generate the sample code discussed in this document. Smart Configurator for CS+ is a tool for generating template 'C' source code and project settings for the RX72T. When using Smart Configurator, it supports user with a visual way of configuring the target device, clocks, software components, hardware resources and interrupts for the project. Thereby bypassing the need in most cases to refer to sections of the Hardware Manual.

By following the steps detailed in this tutorial, the user will generate a CS+ project called SC_Tutorial. A fully completed Tutorial project is contained on the RSK Web Installer (<https://www.renesas.com/rskrx72t/install/cs>) and may be imported into CS+ by following the steps in the Quick Start Guide. This tutorial is intended as a learning exercise for users who wish to use the Smart Configurator to generate their own custom projects for CS+.

Once the user has configured the project, the 'Generate Code' function is used to generate three code modules for each specific MCU feature selected. These code modules are name 'Config_xxx.h', 'Config_xxx.c', and 'Config_xxx_user.c', where 'xxx' is an acronym for the relevant MCU feature, for example 'S12AD'. Within these code modules, the user is then free to add custom code to meet their specific requirement. Custom code should be added, whenever possible, in between the following comment delimiters:

```
/* Start user code for adding. Do not edit comment generated here */  
/* End user code. Do not edit comment generated here */
```

Smart Configurator will locate these comment delimiters, and preserve any custom code inside the delimiters on subsequent code generation operations. This is useful if, after adding custom code, the user needs to re-visit Smart Configurator to change any MCU operating parameters.

The SC_Tutorial project uses interrupts for switch inputs, the ADC module, the Compare Match Timer (CMT), the Serial Communications Interface (SCI) and uses these modules to perform A/D conversion and display the results via the Virtual COM port to a terminal program and also on the LCD display on the RSK.

Following a tour of the key user interface features of Smart Configurator and the reader is guided through each of the peripheral function configuration dialogs in §4.2. In §5, the reader is familiarised with the structure of the template code, as well as how to add their own code to the user code areas provided by the code generator.

4.2 Project Configuration using Smart Configurator - Overview page

In this section, a brief tour of Smart Configurator is presented. For further details of the Smart Configurator paradigm and reference, refer to the Smart Configurator Usage.

You can download the latest document from: <https://www.renesas.com/smart-configurator>.

Smart Configurator will start up by double clicking on “Smart Configurator(Design Tool)” on the project tree. The Smart Configurator initial view is displayed as illustrated in **Figure 4-1**.

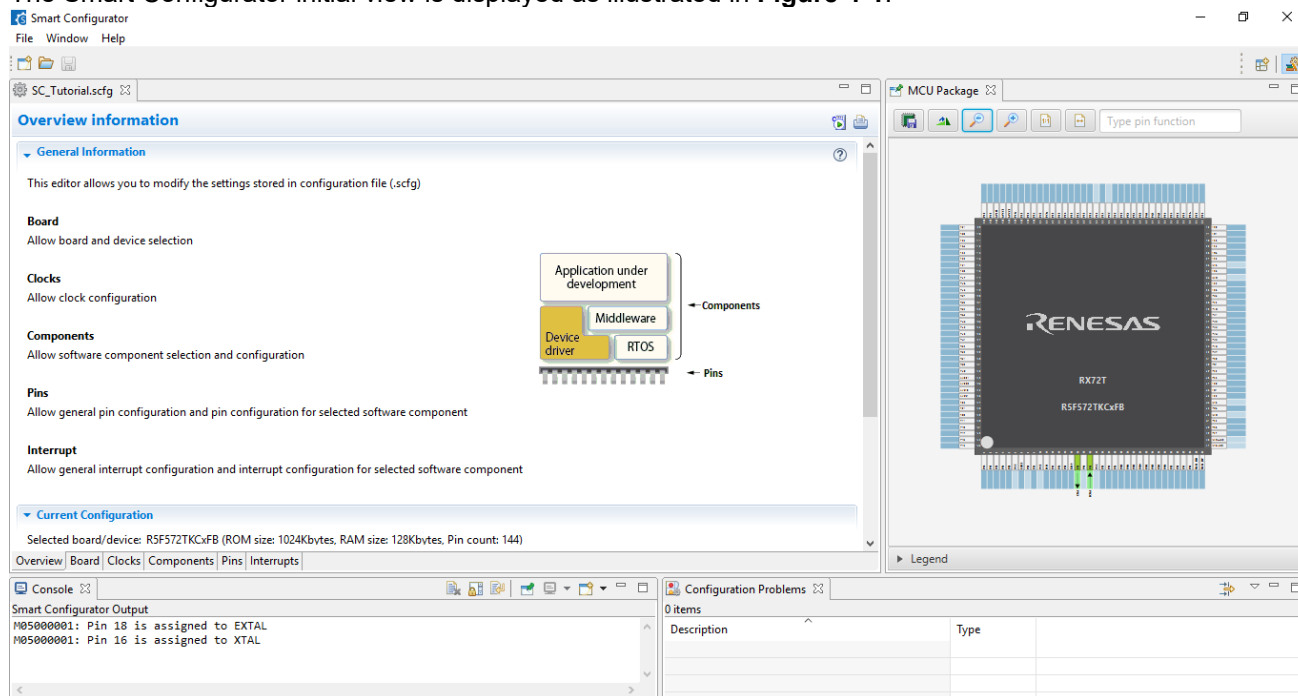


Figure 4-1 Overview page

Smart Configurator provides GUI features for configuration of MCU sub systems. Once the user has configured all required MCU sub systems and peripherals, the user can click the ‘Generate Code’ button, resulting in a fully configured CS+ project that builds and runs without error.

4.3 Board configuration page

On the board setting page, set the board type and device type.
Click the 'Board' tab and it will be displayed as shown in **Figure 4-2**.

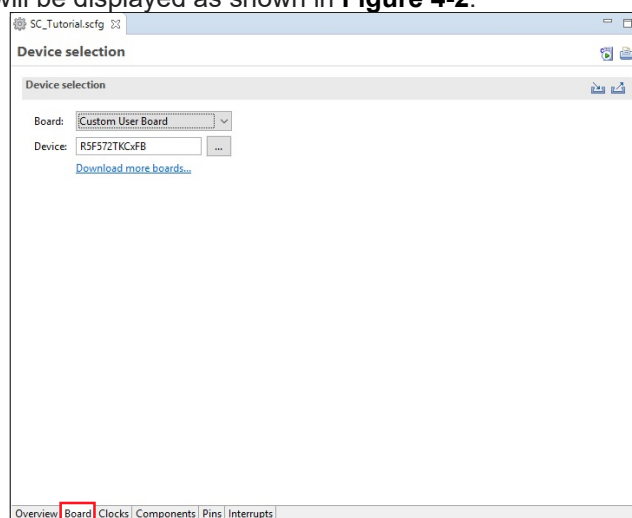


Figure 4-2 Board configuration page

4.3.1 Board configuration

From the default 'Custom User Board', select 'RSKR72T' from the pull down and change it.

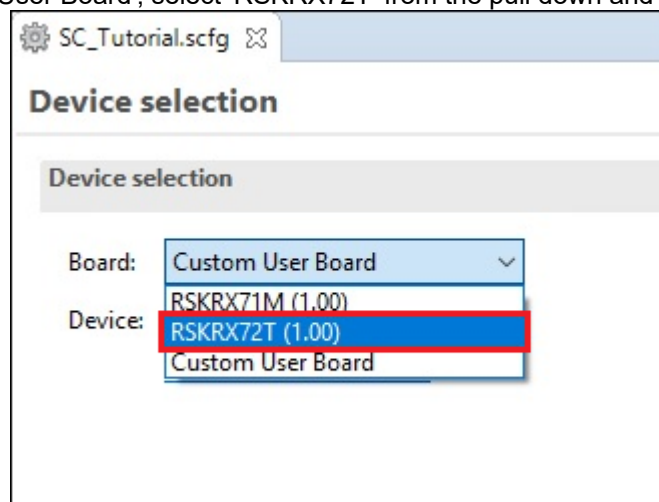


Figure 4-3 Select board

As shown in **Figure 4-4**, when the confirmation dialog of board change is displayed, please click the 'Continue' button and continue the subsequent procedure.

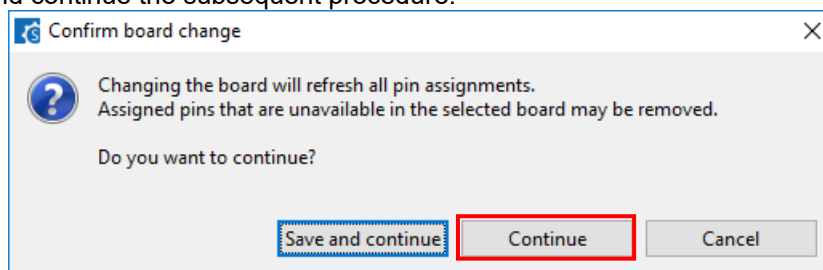


Figure 4-4 Confirm board change

4.4 Clocks configuration page

Clocks configuration page configures clocks of the device selected. Clock source, frequency, PLL settings and clock divider settings can be configured for the output clocks. Clock configurations will be reflected to 'r_bsp_config.h' file in 'Smart Configurator\r_config' of project tree.

4.4.1 Clocks configuration

Figure 4-5 shows a screenshot of Smart Configurator with the Clocks configurations. Click on the 'Clocks' tab. Configure the system clocks as shown in the figure. In this tutorial, we are using the on-board 24 MHz crystal resonator for our main clock oscillation source and the PLL circuit is in operation. The PLL output is used as the main system clock and the divisors should be set as shown in **Figure 4-5**.

Set VCC and AVCC to 3.3(V). Then, do not use Negative Voltage Input Settings of Analog Voltage Settings uncheck the check box.

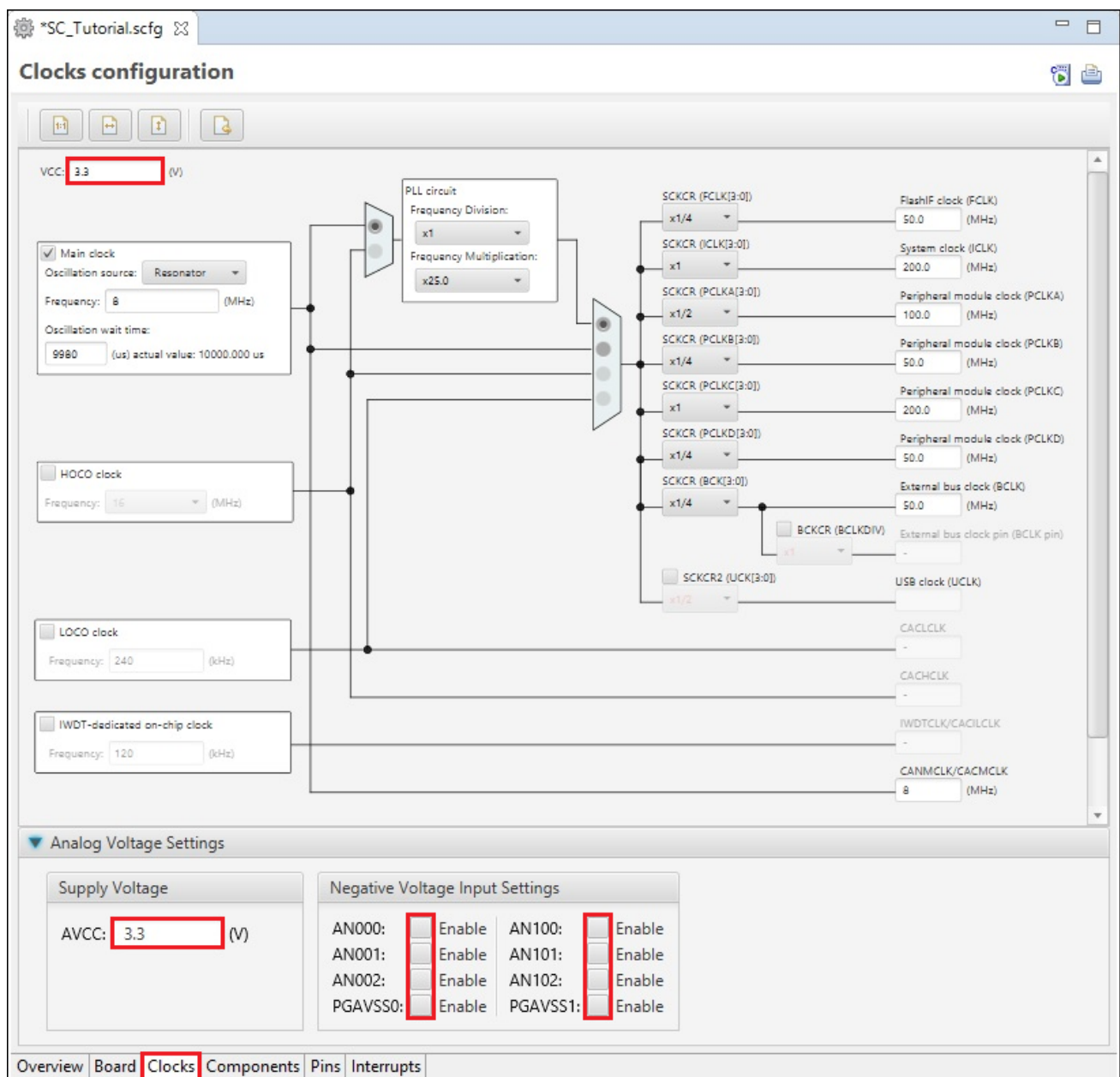


Figure 4-5 Clocks Configuration page

4.5 Component page

Drivers and middleware are handled as software components in Smart Configurator. Component page allows user to select and configure software components.

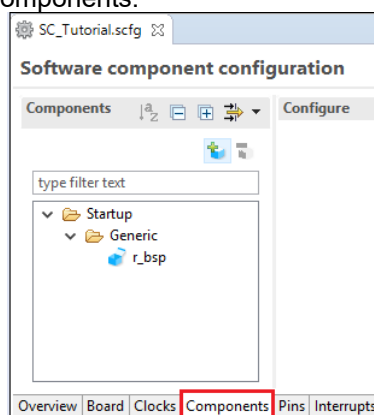


Figure 4-6 Component page

4.5.1 Add a software component into the project

Smart Configurator supports four types of software components: Startup, Drivers, Middleware and Application. In the following sub-sections, the reader is guided through the steps to configure the MCU for a simple project containing interrupts for switch inputs, timers, ADC and a SCI by component of Drivers.

Click 'Add component'  icon.

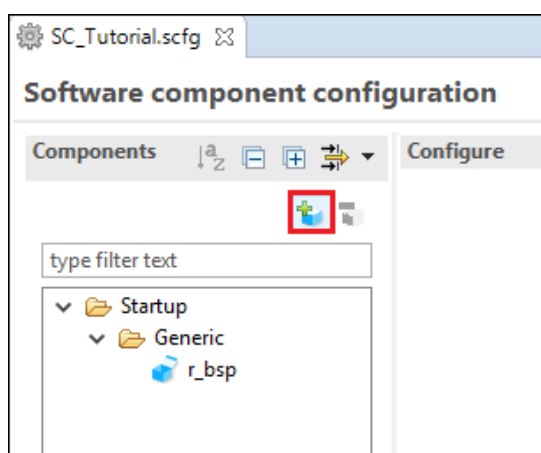


Figure 4-7 Add a Software component (1)

In 'Software Component Selection' dialog -> Type, select 'Drivers'.

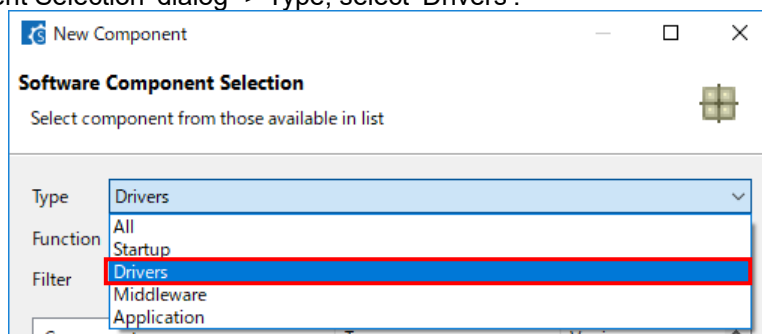


Figure 4-8 Add a Software component (2)

4.5.2 Compare Match Timer

CMT0 will be used as an interval timer for generation of accurate delays. CMT1 and CMT2 will be used as timers in de-bouncing of switch interrupts.

Select 'Compare Match Timer' as shown in **Figure 4-9** below then click 'Next'.

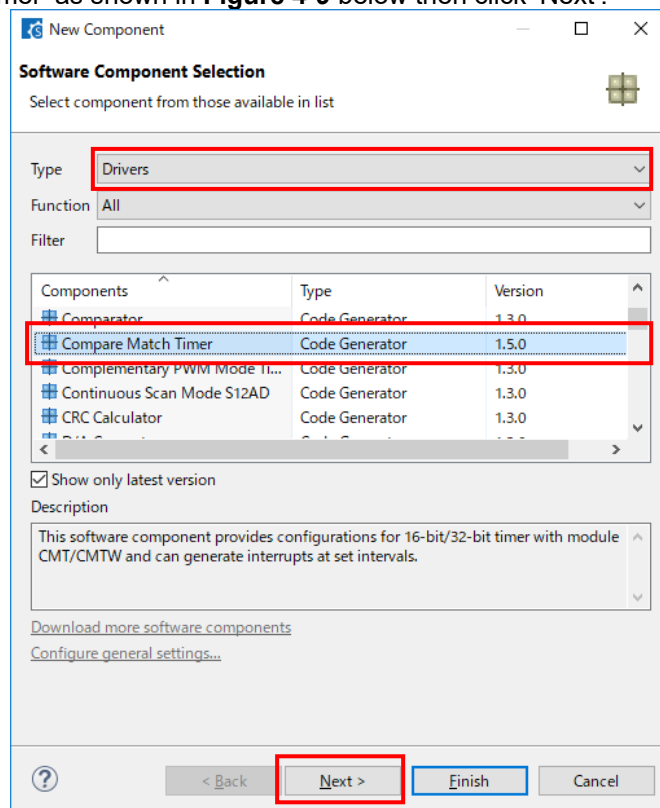


Figure 4-9 Select Compare Match Timer

In 'Add new configuration for selected component' dialog -> Resource, select 'CMT0' as shown in **Figure 4-10** below then click 'Finish'.

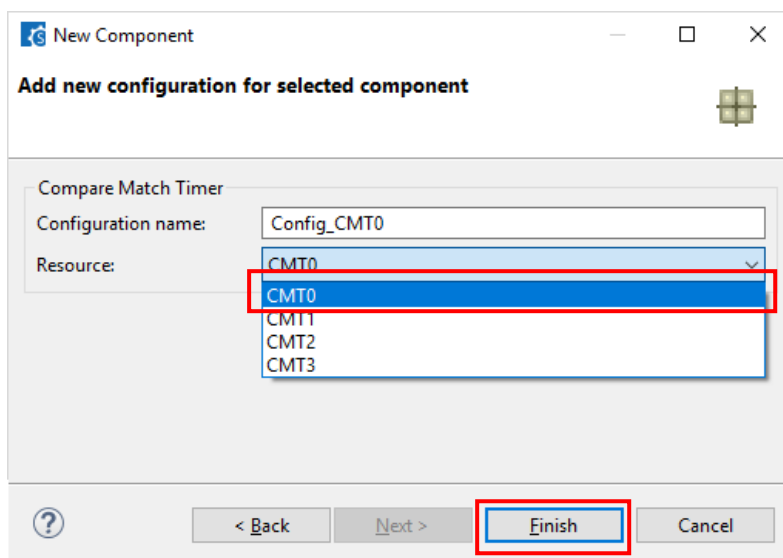


Figure 4-10 Select Resource - CMT0

In the 'Config_CMT0' configures CMT0 as shown in **Figure 4-11**. This timer is configured to generate a high priority interrupt every 1ms. We will use this interrupt later in the tutorial to provide an API for generating high accuracy delays required in our application.

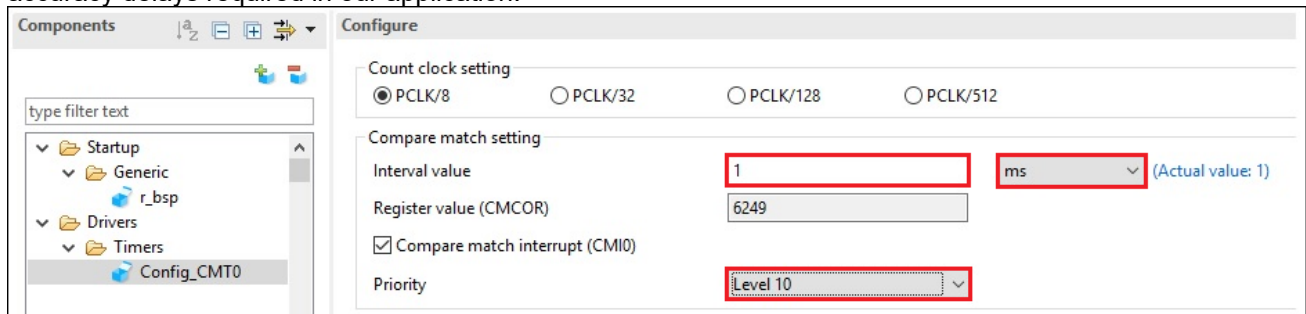


Figure 4-11 Config_CMT0 setting

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Drivers'. Select 'Compare Match Timer' then click 'Next'. In 'Add new configuration for selected component' dialog -> Resource, select 'CMT1' as shown in **Figure 4-12** below then click 'Finish'.

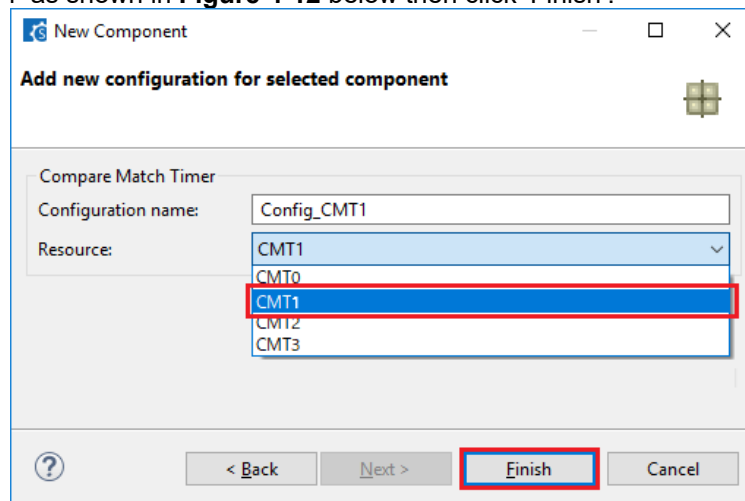


Figure 4-12 Select Resource – CMT1

Navigate to the 'Config_CMT1' and configure CMT1 as shown in **Figure 4-13**. This timer is configured to generate a high priority interrupt after 20ms. This timer is used as our short switch de-bounce timer later in this tutorial.

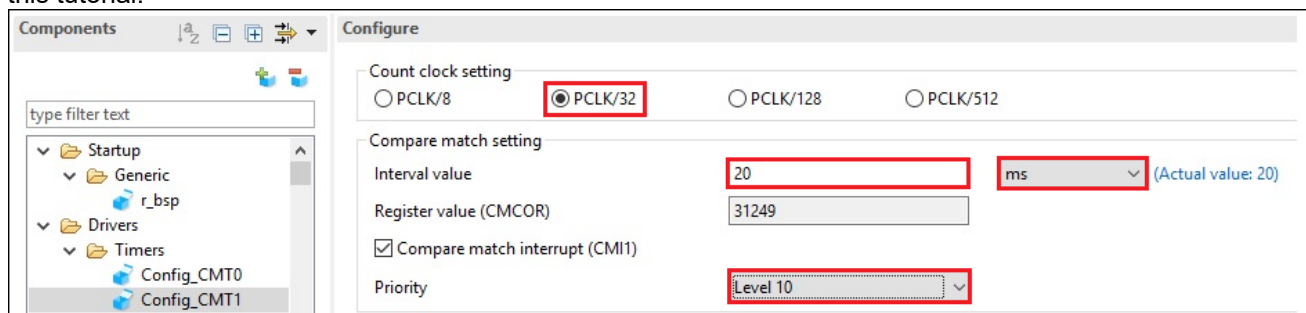


Figure 4-13 Config_CMT1 setting

Click 'Add component'  icon. In 'Software Component Selection' dialog -> Type, select 'Drivers'. Select 'Compare Match Timer' then click 'Next'. In 'Add new configuration for selected component' dialog -> Resource, select 'CMT2' as shown in **Figure 4-14** below then click 'Finish'.

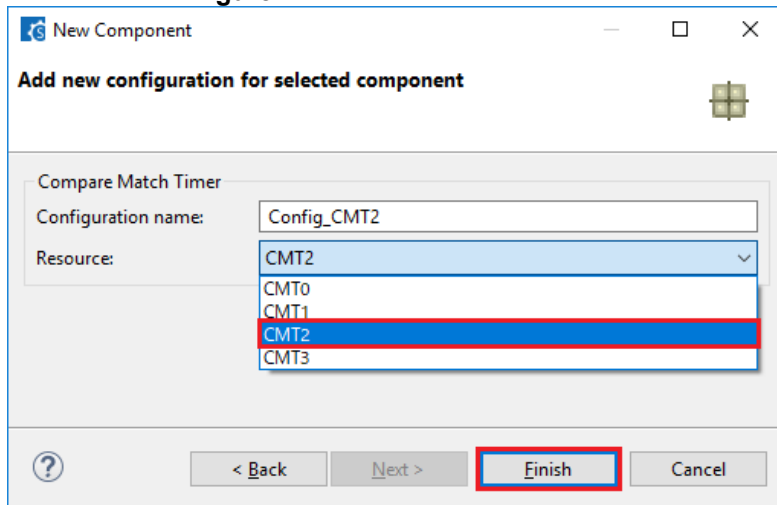


Figure 4-14 Select Resource – CMT2

Navigate to the 'Config_CMT2' and configure CMT2 as shown in **Figure 4-15**. This timer is configured to generate a high priority interrupt after 200ms. This timer is used as our long switch de-bounce timer later in this tutorial.

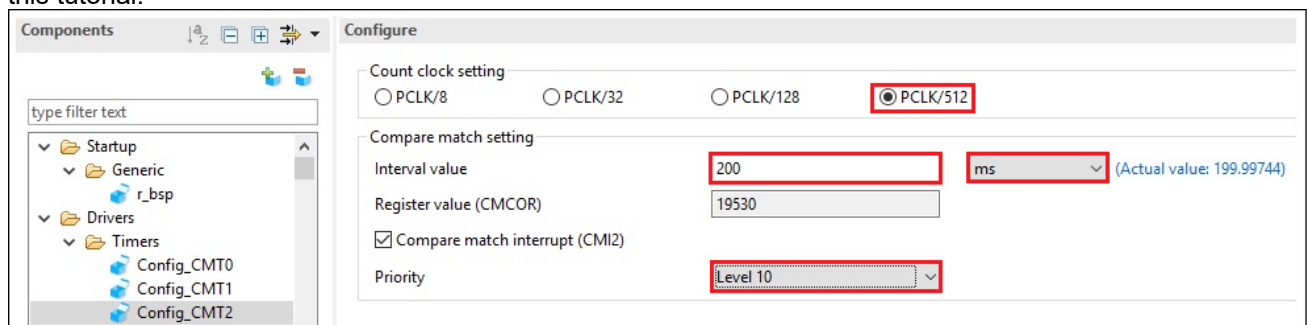


Figure 4-15 Config_CMT2 setting

4.5.3 Interrupt Controller

Referring to the RSK schematic, SW1 is connected to IRQ0(P10) and SW2 is connected to IRQ9 (PB3). SW3 is connected to IRQ7(P20) and the ADTRG0n. Tutorial used ADTRG0n and will be configured later in §4.5.7.

Click 'Add component'  icon.

In 'Software Component Selection' dialog -> Type, select 'Drivers'.

Select 'Interrupt Controller' as shown in **Figure 4-16** then click 'Next'.

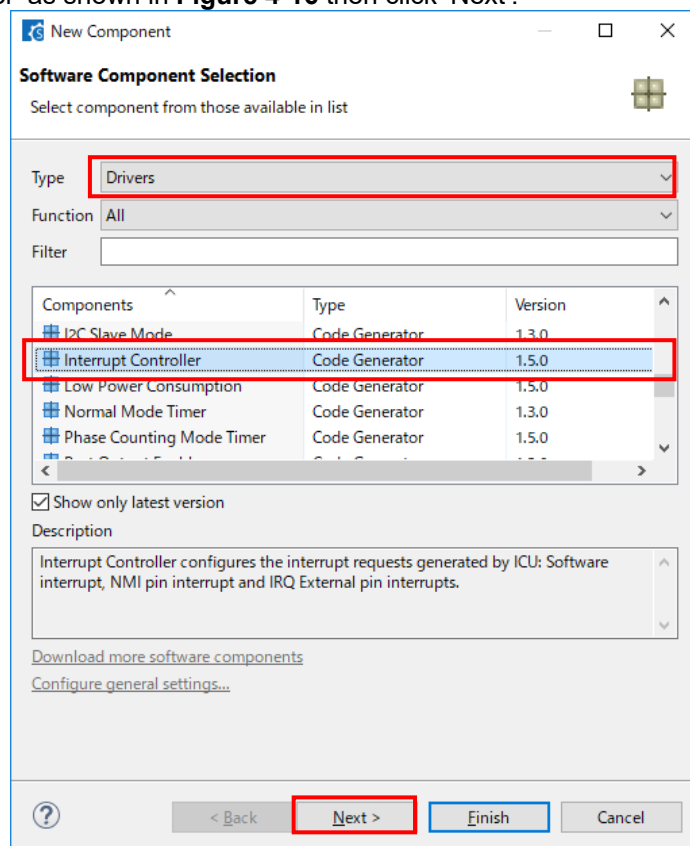


Figure 4-16 Select Interrupt Controller

In 'Add new configuration for selected component' dialog -> Resource, select 'ICU' as shown in **Figure 4-17** below then click 'Finish'.

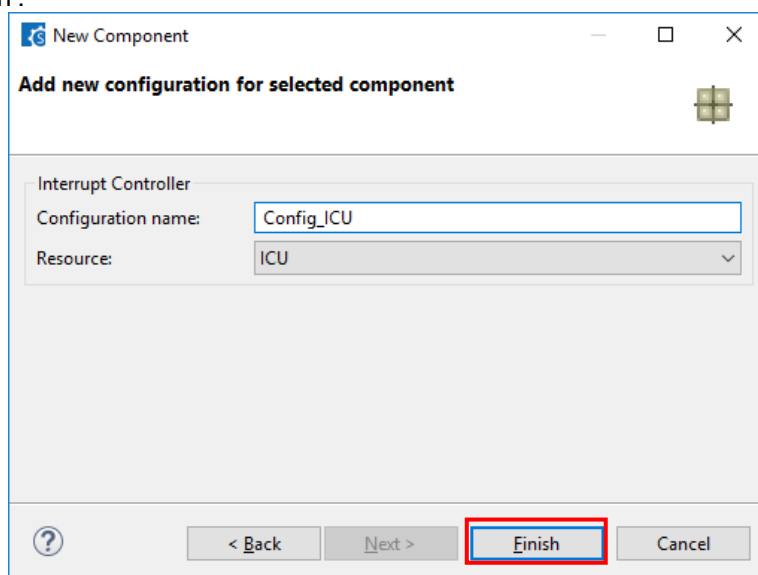


Figure 4-17 Select resource – ICU

Navigate to the 'Config_ICU', configure these two interrupts as falling edge triggered as shown in **Figure 4-18** below.

The screenshot displays the Smart Configurator interface for the RX72T. On the left, the 'Components' pane shows a tree structure with 'Config_ICU' selected under the 'Interrupt' category. The main 'Configure' pane on the right shows settings for various interrupts. The 'Software interrupt setting' section is at the top, followed by 'NMI pin interrupt setting'. Below these are individual settings for IRQ0 through IRQ15. IRQ0 and IRQ9 are checked, and their 'Detection type' is set to 'Falling edge'. All other interrupts are unchecked and their 'Detection type' is set to 'Low level'. The 'Priority' for all interrupts is set to 'Level 15 (highest)'. The 'Digital filter' is set to 'No filter' and the frequency is set to '0 (MHz)' for all interrupts.

Interrupt	Enabled	Detection type	Digital filter	Frequency (MHz)	Priority
Software interrupt	☐				
Software interrupt 2	☐				
NMI pin interrupt	☐	Falling edge	No filter	0	Level 15 (highest)
IRQ0	☒	Falling edge	No filter	0	Level 15 (highest)
IRQ1	☐	Low level	No filter	0	Level 15 (highest)
IRQ2	☐	Low level	No filter	0	Level 15 (highest)
IRQ3	☐	Low level	No filter	0	Level 15 (highest)
IRQ4	☐	Low level	No filter	0	Level 15 (highest)
IRQ5	☐	Low level	No filter	0	Level 15 (highest)
IRQ6	☐	Low level	No filter	0	Level 15 (highest)
IRQ7	☐	Low level	No filter	0	Level 15 (highest)
IRQ8	☐	Low level	No filter	0	Level 15 (highest)
IRQ9	☒	Falling edge	No filter	0	Level 15 (highest)
IRQ10	☐	Low level	No filter	0	Level 15 (highest)
IRQ11	☐	Low level	No filter	0	Level 15 (highest)
IRQ12	☐	Low level	No filter	0	Level 15 (highest)
IRQ13	☐	Low level	No filter	0	Level 15 (highest)
IRQ14	☐	Low level	No filter	0	Level 15 (highest)
IRQ15	☐	Low level	No filter	0	Level 15 (highest)

Figure 4-18 Config_ICU setting

4.5.4 Ports

Referring to the RSK schematic, LED0 is connected to P54, LED1 is connected to P55, LED2 is connected to P60 and LED3 is connected to P61. PA2 is used as one of the LCD control lines, together with PC5, P25 and P26.

Click 'Add component'  icon.

In 'Software Component Selection' dialog -> Type, select 'Drivers'.

Select 'Ports' as shown in **Figure 4-19** then click 'Next'.

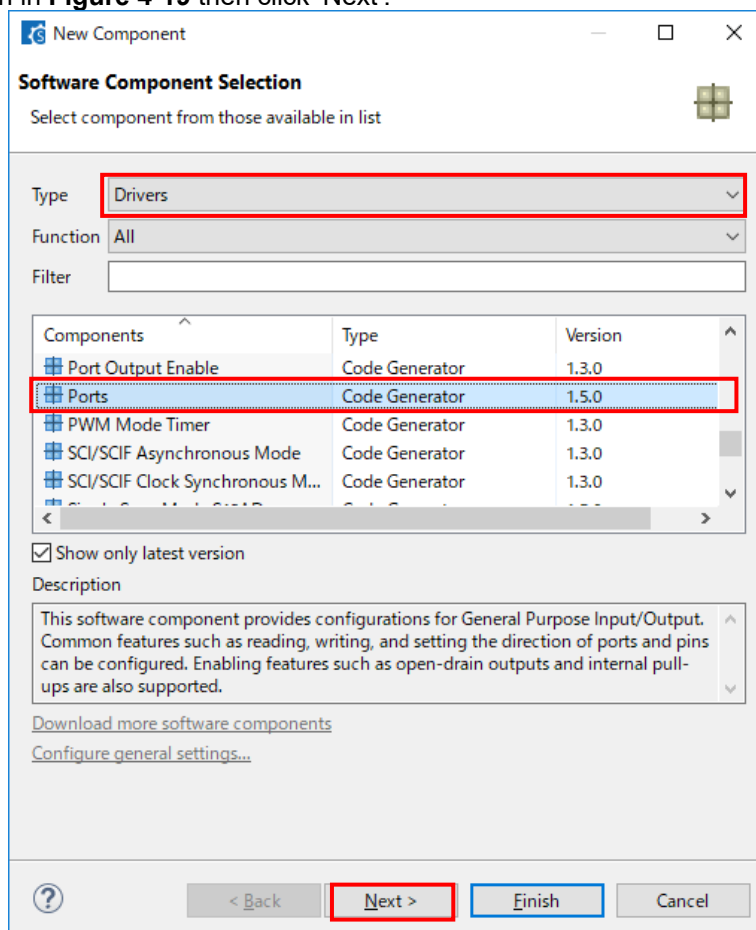


Figure 4-19 Select Ports

In 'Add new configuration for selected component' dialog -> Resource, select 'PORT' as shown in **Figure 4-20** below then click 'Finish'.

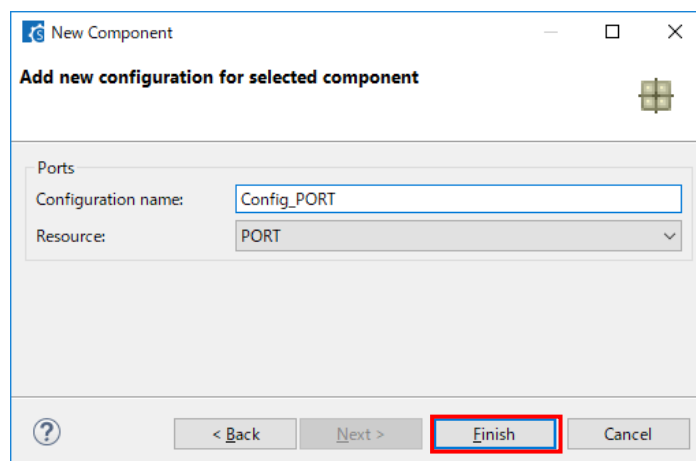


Figure 4-20 Select resource – PORT

'PORT2', 'PORT5', 'PORT6', 'PORTA', 'PORTC' tick box is checked as shown in **Figure 4-21** below.

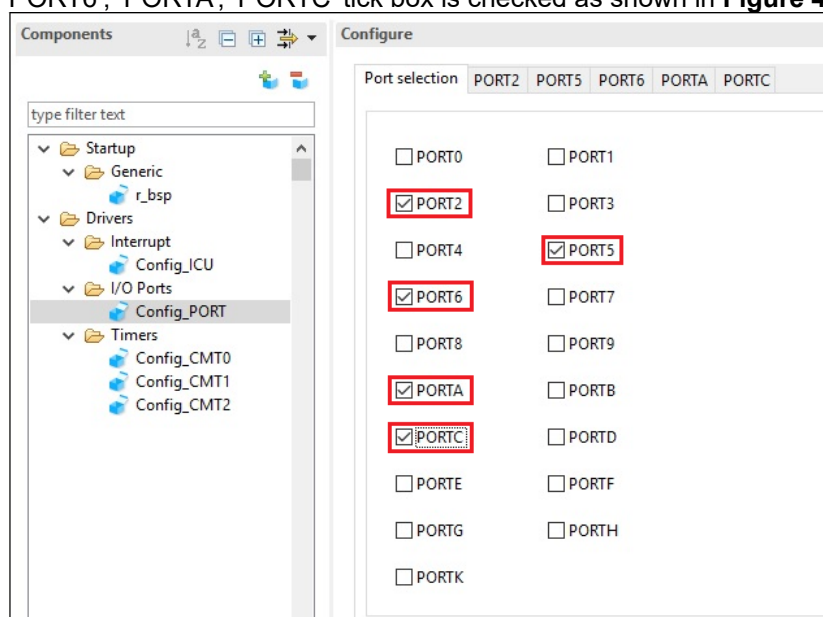


Figure 4-21 Select Port selection

Navigate to the 'Ports' configure these four I/O lines and LCD control lines as shown in **Figure 4-22**, **Figure 4-23**, **Figure 4-24**, **Figure 4-25** and **Figure 4-26** below. Ensure that the 'Output 1' tick box is checked, except P26. Select 'PORT2' tab.

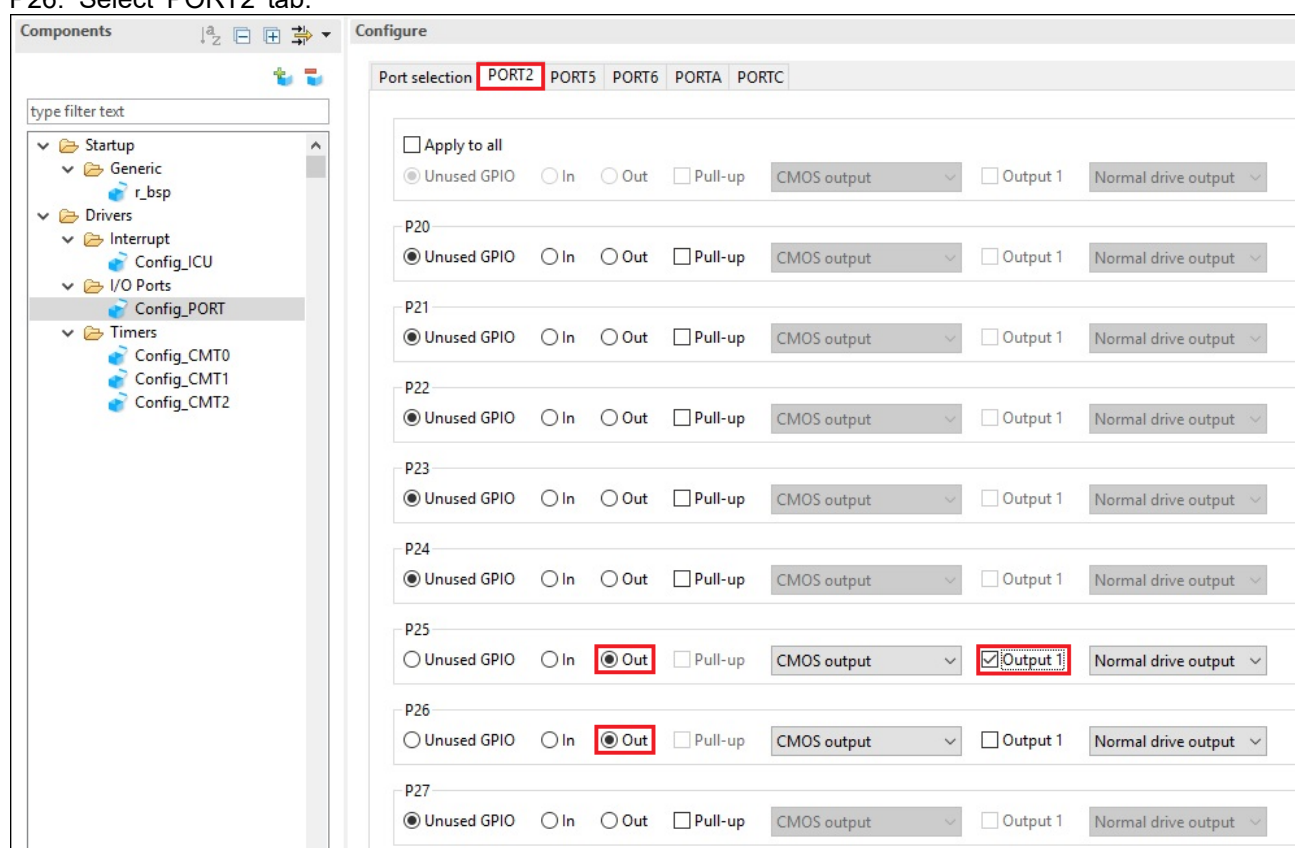


Figure 4-22 Select PORT2 tab

Select 'PORT5' tab.

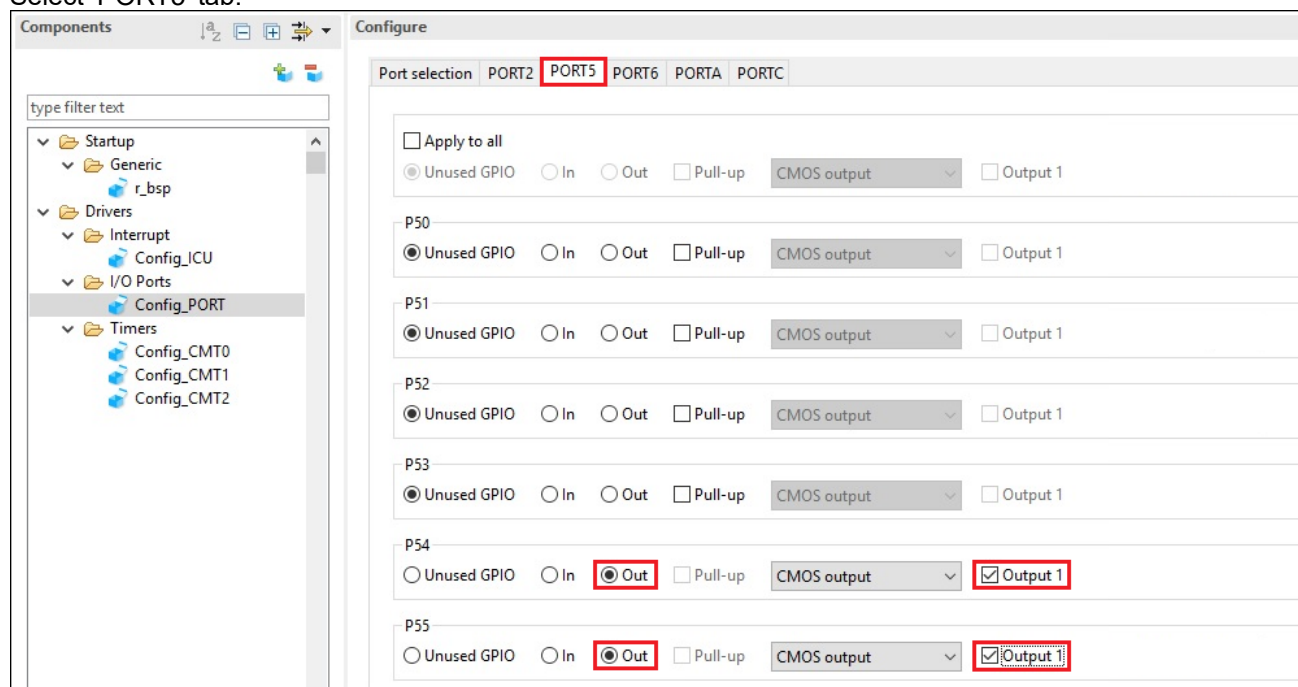


Figure 4-23 Select PORT5 tab

Select 'PORT6' tab.

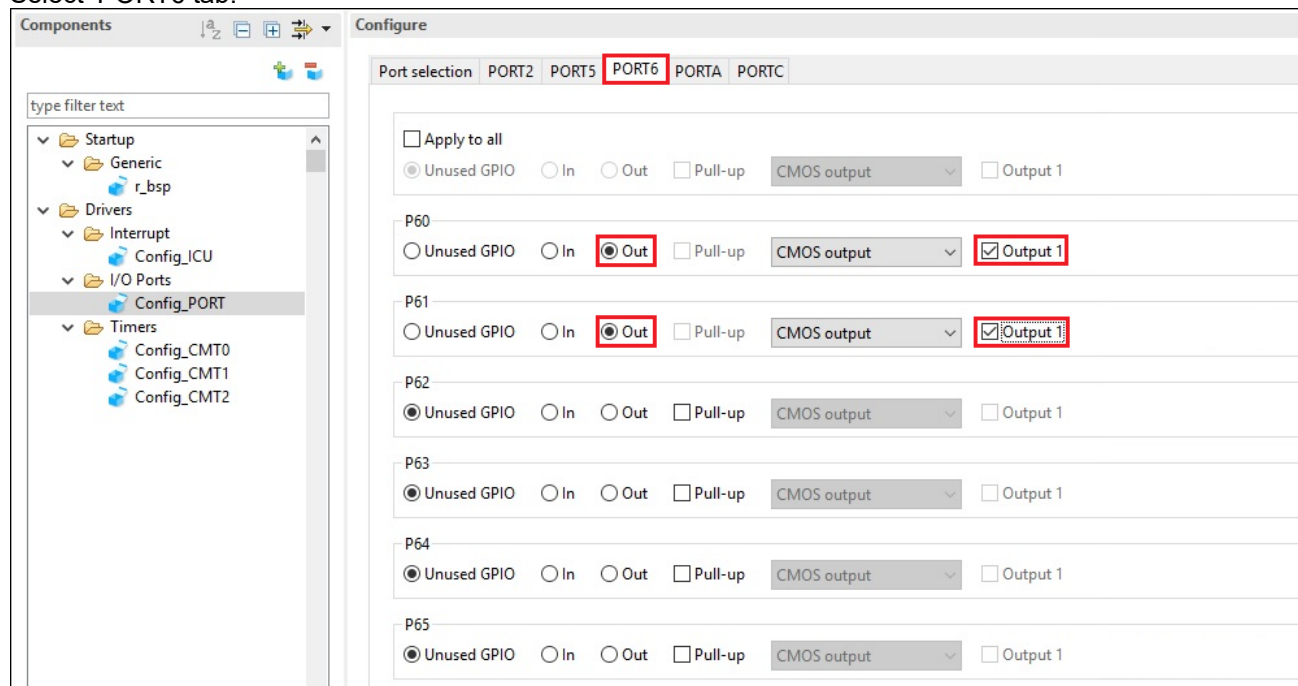


Figure 4-24 Select PORT6 tab

Select 'PORTA' tab.

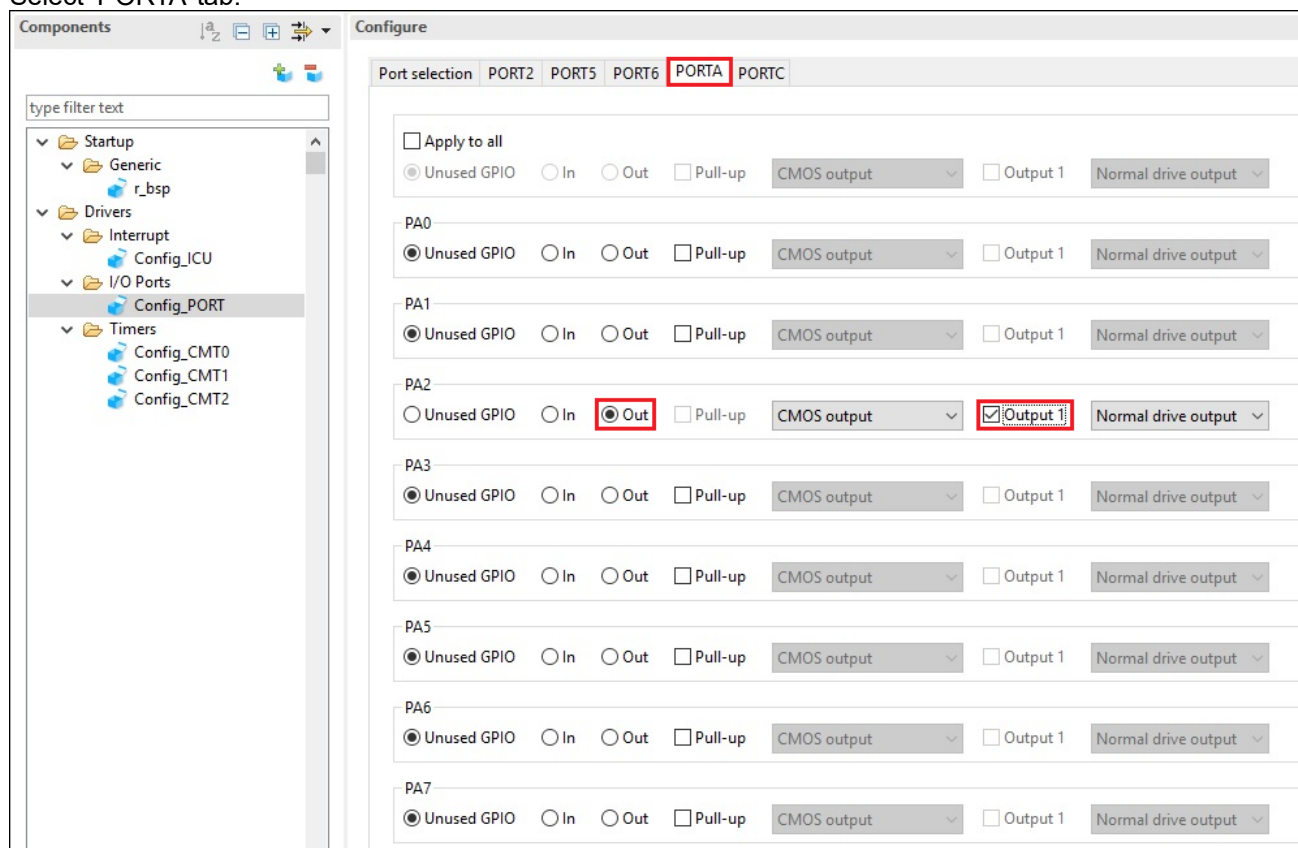


Figure 4-25 Select PORTA tab

Select 'PORTC' tab.

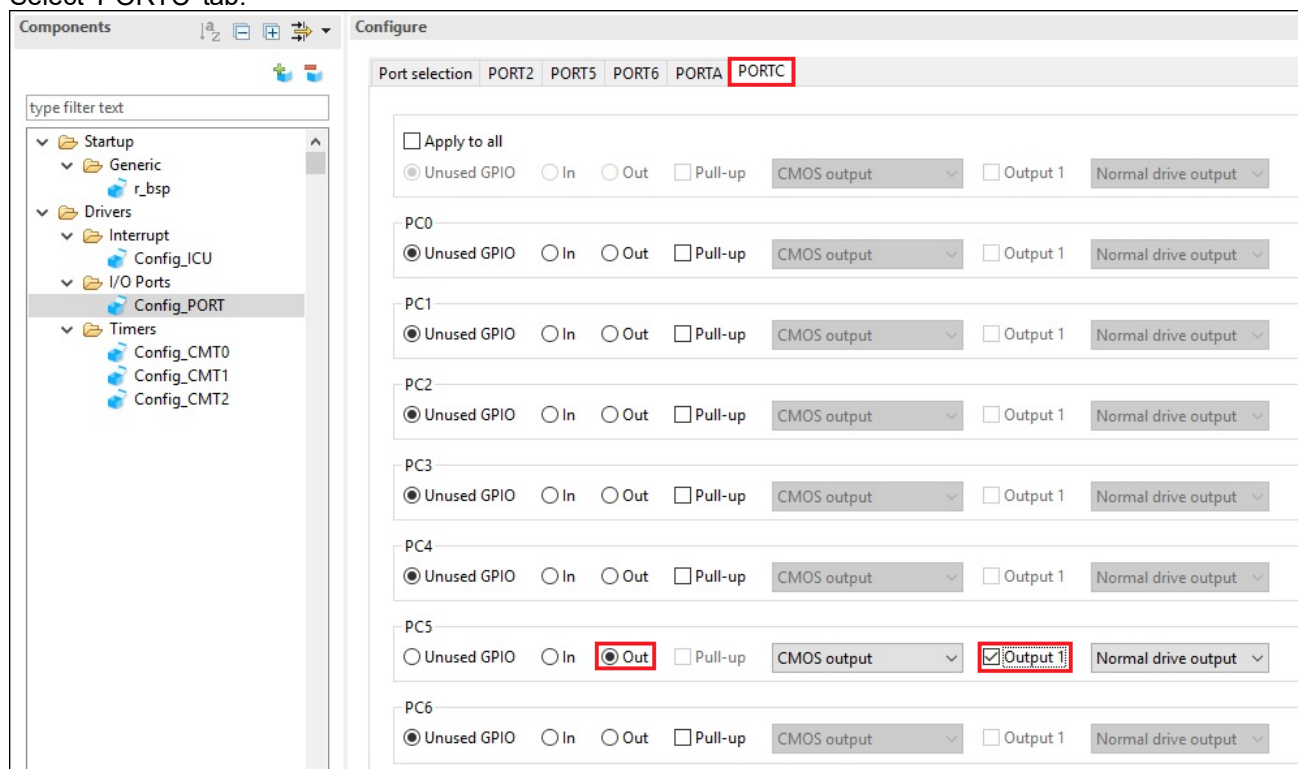


Figure 4-26 Select PORTC tab

4.5.5 SCI/SCIF Asynchronous Mode

In the RSKRX72T SCI11 is connected via a Renesas RL78/G1C to provide a USB virtual COM port as shown in the schematic.

Click 'Add component'  icon.

In 'Software Component Selection' dialog -> Type, select 'Drivers'. Select 'SCI/SCIF Asynchronous Mode' as shown in **Figure 4-27** then click 'Next'.

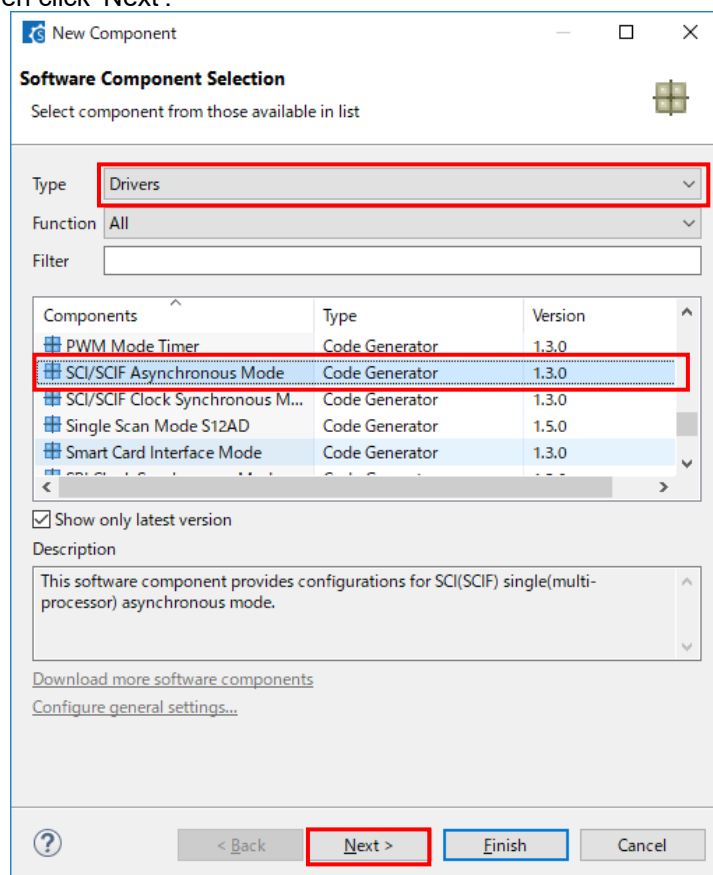


Figure 4-27 Select SCI/SCIF Asynchronous Mode

In 'Add new configuration for selected component' dialog -> Work mode, select 'Transmission/Reception' as shown in **Figure 4-28** below.

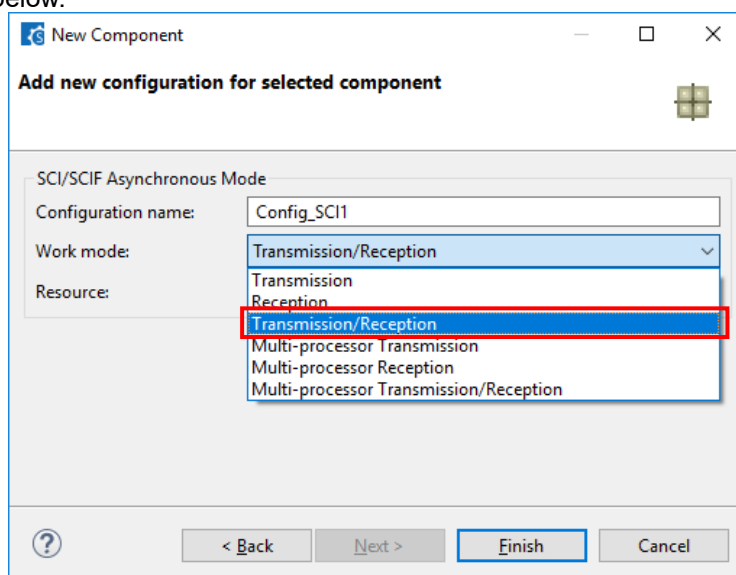


Figure 4-28 Select Work mode – Transmission/Reception

In 'Resource', select 'SCI11' as shown in **Figure 4-29** below.

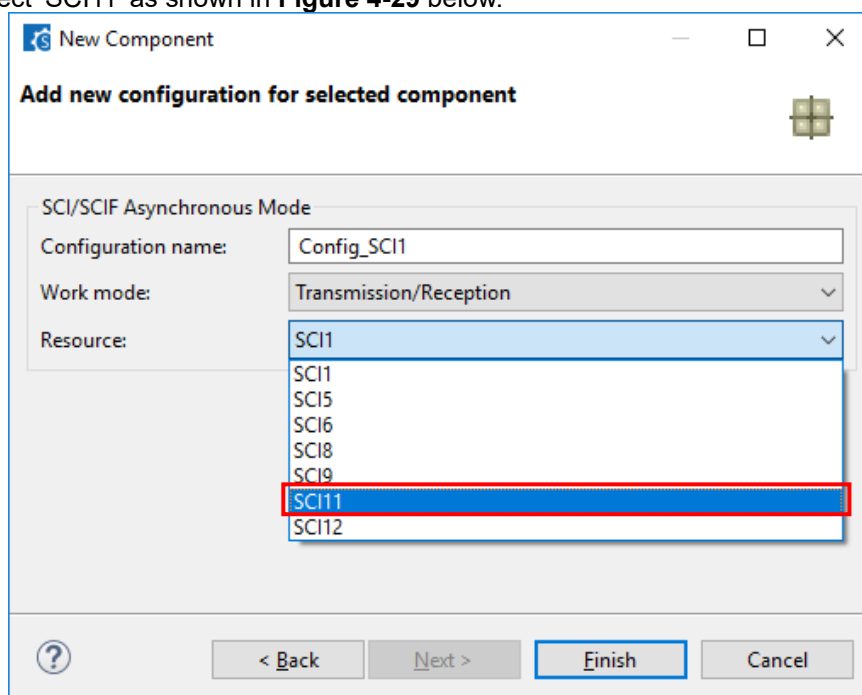


Figure 4-29 Select Resource – SCI11

Ensure that the 'Configuration name' is set to 'Config_SCI11' as shown in **Figure 4-30** below then click 'Finish'

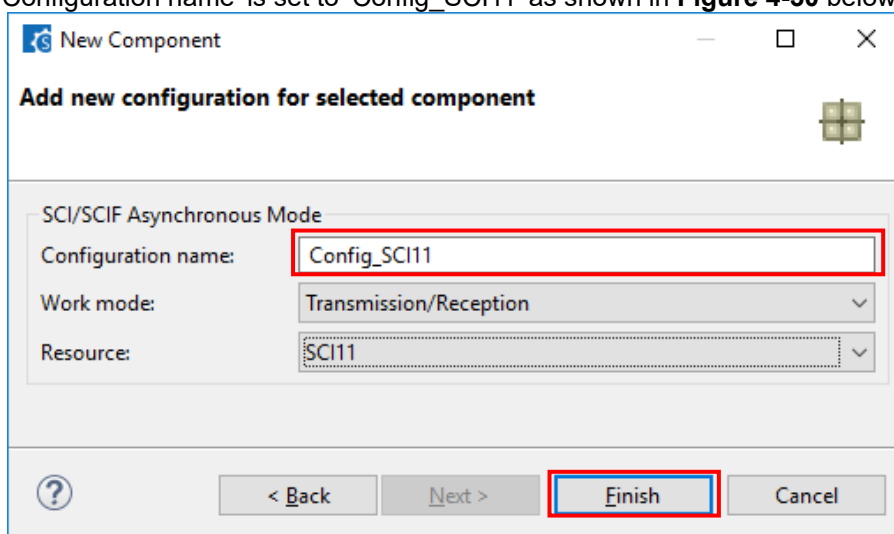


Figure 4-30 Ensure Configuration name - Config_SCI11

Configure SCI11 as shown in **Figure 4-31**. Ensure the 'Start bit edge detection' is set as 'Falling edge on RXD11 pin' and the 'Bit rate' is set to 19200 bps. All other settings remain at their defaults.

The screenshot displays the Smart Configurator interface for Config_SCI11. The left pane shows the component tree with Config_SCI11 selected. The right pane shows the configuration settings for SCI11. Key settings highlighted with red boxes include 'Falling edge on RXD11 pin' for start bit edge detection and '19200' for the bit rate. Other settings are at their defaults.

Setting	Value
FIFO mode setting	Non-FIFO mode
Start bit edge detection setting	Falling edge on RXD11 pin
Data length setting	8 bits
Parity setting	None
Stop bit length setting	1 bit
Transfer direction setting	LSB-first
Transfer rate setting	Internal clock
Base clock	16 cycles for 1-bit period
Bit rate	19200 (bps) (Actual value: 19201.229, Error: 0.006%)
Enable modulation duty correction	Disabled
SCK11 pin function	SCK11 is not used
Noise filter setting	Enable noise filter
Noise filter clock	Clock signal divided by 1
Hardware flow control setting	None
RTS11 output active trigger number	15
FIFO data setting	Transmit FIFO data trigger number: 0, Receive FIFO data trigger number: 8
Data match detection setting	Enable data match detection
Comparison data	0x00
Data handling setting	Transmit data handling: Data handled in interrupt service routine, Receive data handling: Data handled in interrupt service routine
Interrupt setting	TXI11 priority: Level 15 (highest), RXI11 priority: Level 15 (highest), Enable reception error interrupt (ERI11): Checked, TEI11, ERI11 priority (Group AL0): Level 15 (highest), Receive data ready interrupt: Receive data full interrupt (RXI)
Callback function setting	Transmission end: Checked, Reception end: Checked, Reception error: Checked

Figure 4-31 Config_SCI11 setting

4.5.6 SPI Clock Synchronous Mode

In the RSKRX72T SCI6 is used as an SPI master for the Pmod LCD on the PMOD1 connector as shown in the schematic. Click 'Add component'  icon.

In 'Software Component Selection' dialog -> Type, select 'Drivers'. Select 'SPI Clock Synchronous Mode' as shown in **Figure 4-32** then click 'Next'.

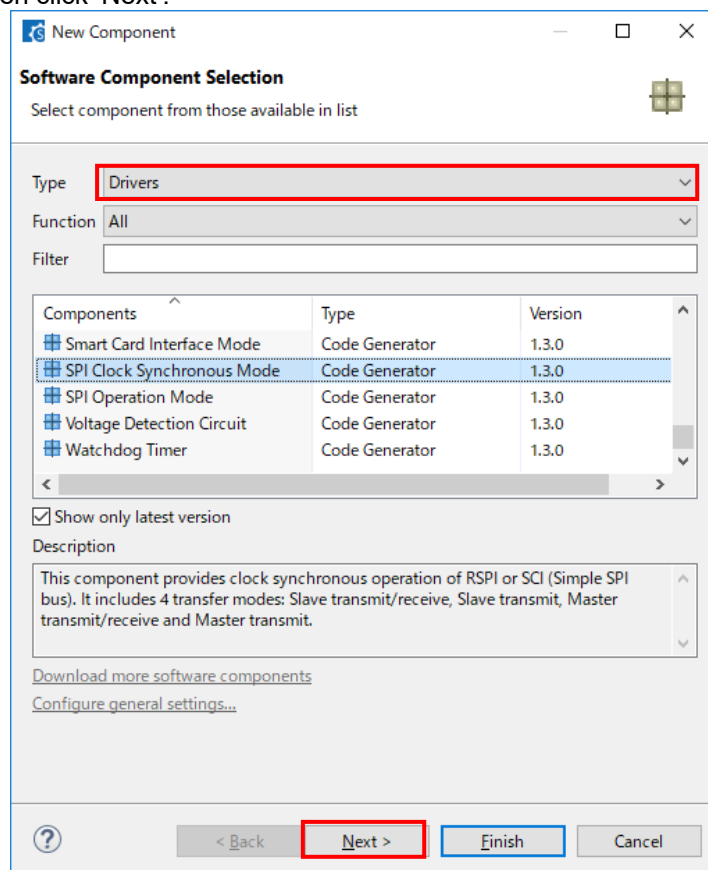


Figure 4-32 Select SPI Clock Synchronous Mode

Ensure Operation, is set to 'Master transmit only' as shown in **Figure 4-33** below.

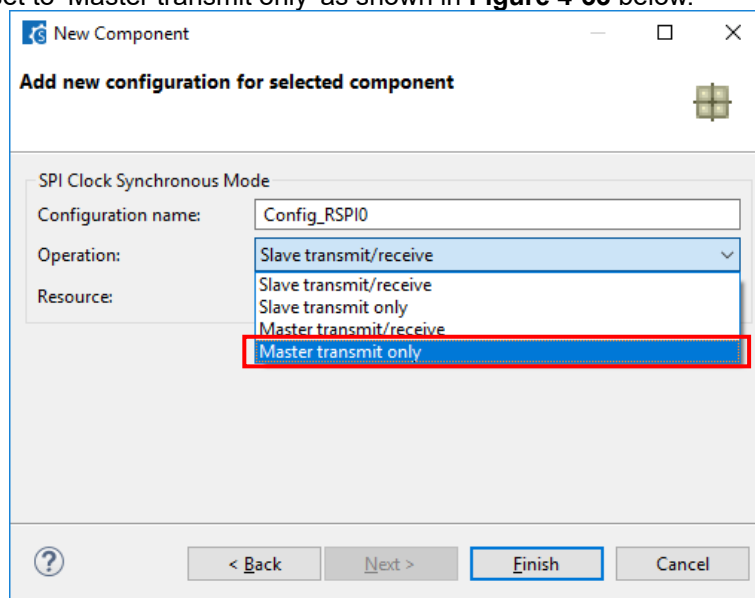


Figure 4-33 Select Operation – Master Transmit

In 'Resource', select 'SCI6' as shown in **Figure 4-34** below.

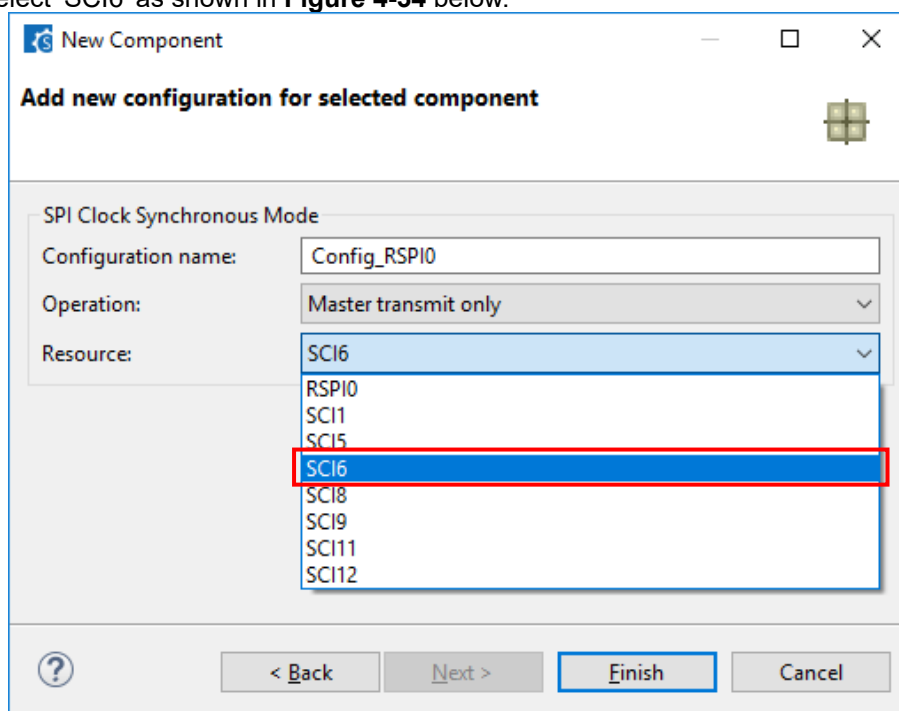


Figure 4-34 Select Resource – SCI6

Ensure that the 'Configuration name' is set to 'Config_SCI6' as shown in **Figure 4-35** below then click 'Finish'

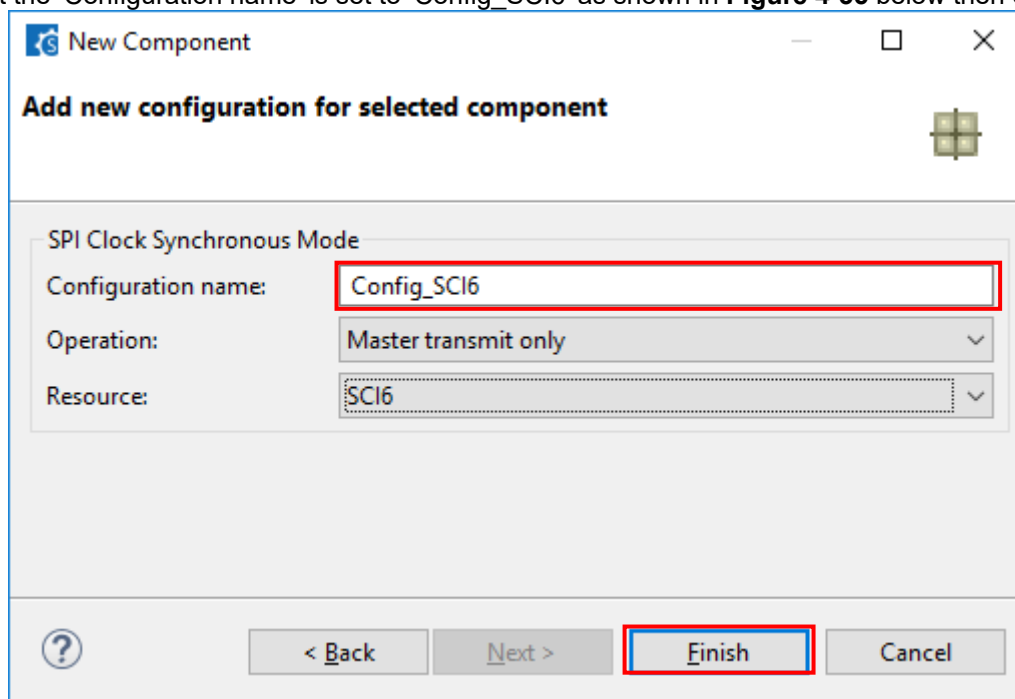


Figure 4-35 Ensure Configuration name - Config_SCI6

Configure SCI6 as shown in **Figure 4-36**. Ensure the 'Transfer direction' is set as 'MSB-first' and the 'Bit rate' is set to 8000 kbps. All other settings remain at their defaults.

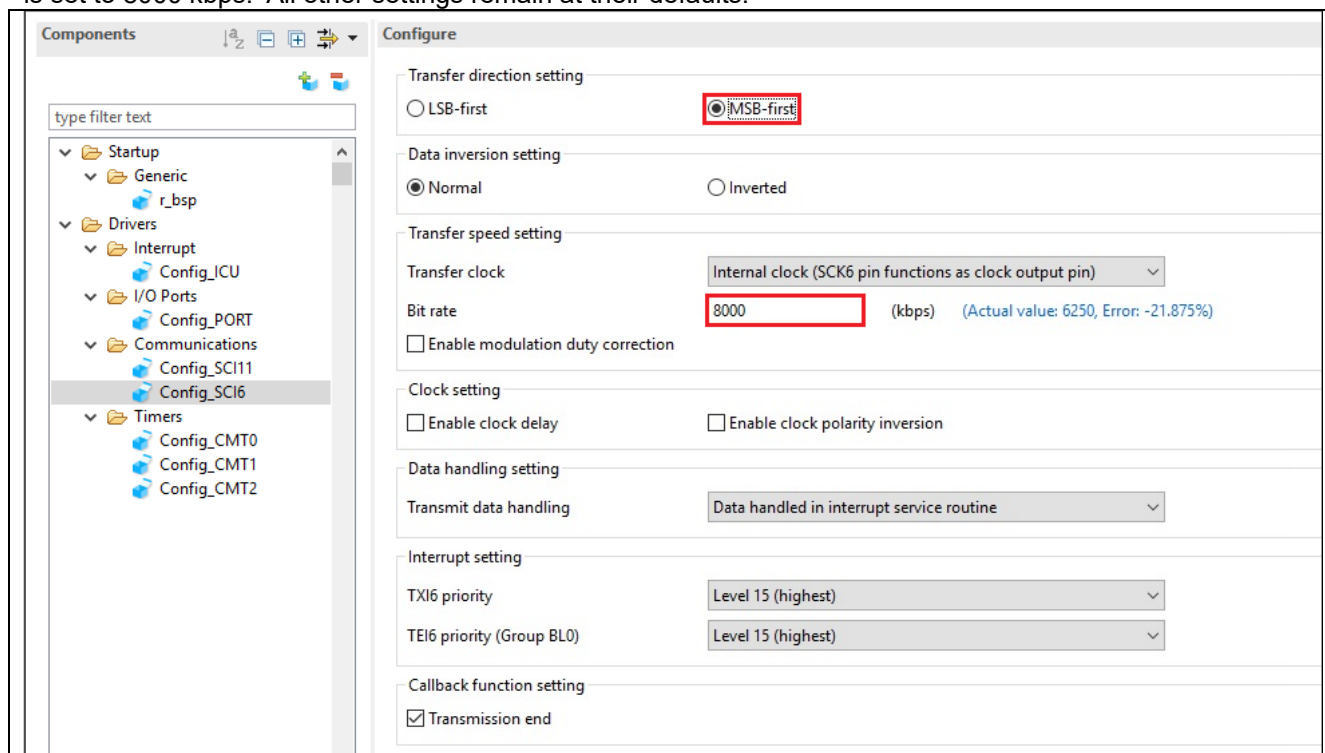


Figure 4-36 Config_SCI6 setting

4.5.7 Single Scan Mode S12AD

We will be using the S12AD on Single Scan Mode on the AN000 input, which is connected to the RV1 potentiometer output on the RSK. The conversion start trigger will be via the pin connected to SW3. Click 'Add component'  icon. In 'Software Component Selection' dialog -> Type, select 'Drivers'. Select 'Single Scan Mode S12AD' as shown in **Figure 4-37** then click 'Next'.

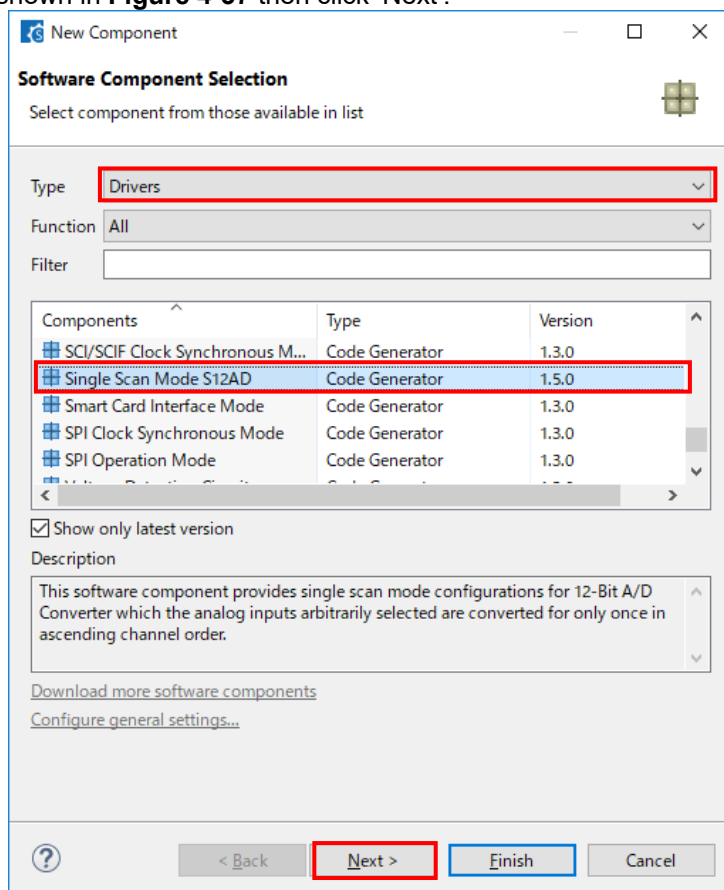


Figure 4-37 Select Single Scan Mode S12AD

In 'Add new configuration for selected component' dialog -> Resource, select 'S12AD0' as shown in **Figure 4-38** below then click 'Finish'.

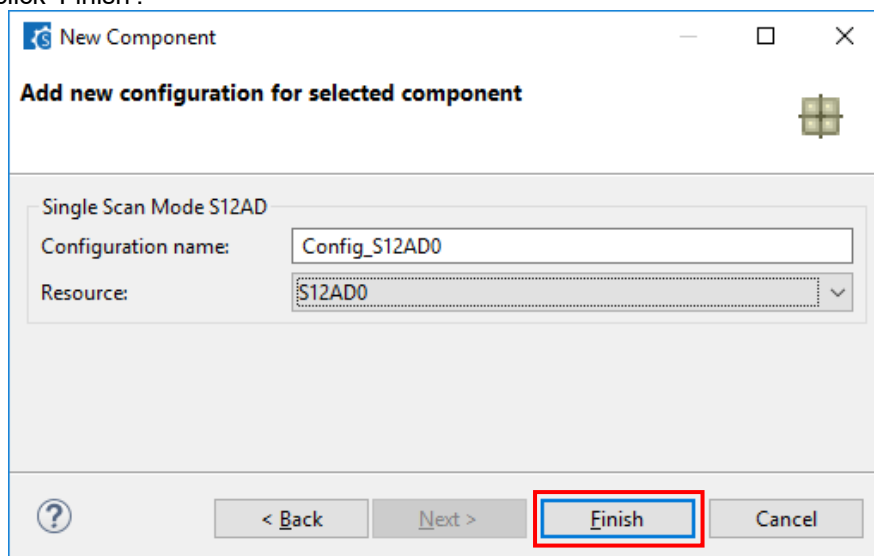


Figure 4-38 Select resource – S12AD0

Configure S12AD0 as shown in **Figure 4-39** and **Figure 4-40**. Ensure the 'Analog input channel' tick box for AN000 is checked and the 'Start trigger source' is set to 'A/D conversion start trigger pin'. All other settings remain at their defaults.

The screenshot displays the Smart Configurator interface for configuring the S12AD0 component. The left pane shows the component tree with 'Config_S12AD0' selected. The right pane shows the configuration settings for S12AD0, organized into sections:

- Basic setting**
 - Analog input mode setting
 - ☐ Double trigger mode
 - Analog input channel setting
 - ☒ AN000 ☐ AN001 ☐ AN002 ☐ AN003 ☐ AN004
 - ☐ AN005 ☐ AN006 ☐ AN007
 - Conversion start trigger setting
 - Start trigger source: **A/D conversion start trigger pin**
 - Interrupt setting
 - ☒ Enable AD conversion end interrupt (S12ADI) Priority: **Level 15 (highest)**
- Advance setting**
 - Add/Average AD value setting
 - ☐ AN000 ☐ AN001 ☐ AN002 ☐ AN003 ☐ AN004
 - ☐ AN005 ☐ AN006 ☐ AN007
 - Self diagnosis setting
 - Mode: **Unused**
 - Voltage used: **0V**
 - Disconnection detection assist setting
 - Charge setting: **Unused**
 - Period: **15 ADCLK**
 - Dedicated sample hold circuit channel setting
 - ☐ AN000 ☐ AN001 ☐ AN002
 - Programmable gain amplifier setting
 - Amplifier input setting (AN000~AN002): **Single-ended**
 - AN000 analog path selection: **A/D_AN000 | CMPC00_None | CMPC01_None**
 - Gain selection: **x 4.000**
 - AN001 analog path selection: **A/D_AN001 | CMPC10_None | CMPC11_None**
 - Gain selection: **x 4.000**
 - AN002 analog path selection: **A/D_AN002 | CMPC20_None | CMPC21_None**
 - Gain selection: **x 4.000**

Figure 4-39 Config_S12AD0 setting (1)

Data registers setting			
Data placement	Right-alignment		
Automatic clearing	Disable automatic clearing		
Addition/Average mode select	Addition mode		
Addition count	1-time		
Window function setting			
☒ Disable		☐ Enable	
Window A/B operation setting			
☐ Enable comparison window A		☐ Enable comparison window B	
Window A/B complex condition		Window A comparison condition matched OR window B comparison condition matched	
A/D comparison A setting			
Reference data 0 for comparison	0		
Reference data 1 for comparison	0		
☐ Use comparator for AN000	Reference data 0 > A/D-converted value		
☐ Use comparator for AN001	Reference data 0 > A/D-converted value		
☐ Use comparator for AN002	Reference data 0 > A/D-converted value		
☐ Use comparator for AN003	Reference data 0 > A/D-converted value		
☐ Use comparator for AN004	Reference data 0 > A/D-converted value		
☐ Use comparator for AN005	Reference data 0 > A/D-converted value		
☐ Use comparator for AN006	Reference data 0 > A/D-converted value		
☐ Use comparator for AN007	Reference data 0 > A/D-converted value		
A/D comparison B setting			
Reference data 0 for comparison	0		
Reference data 1 for comparison	0		
Comparison B channel	Unused		
	Reference data 0 > A/D-converted value		
Input sampling time setting			
Dedicated sample and hold circuit	0.54	(μs)	(Actual value: 0.540)
AN000/Self-diagnosis	0.54	(μs)	(Actual value: 0.540)
AN001	0.54	(μs)	(Actual value: 0.540)
AN002	0.54	(μs)	(Actual value: 0.540)
AN003	0.54	(μs)	(Actual value: 0.540)
AN004	0.54	(μs)	(Actual value: 0.540)
AN005	0.54	(μs)	(Actual value: 0.540)
AN006	0.54	(μs)	(Actual value: 0.540)
AN007	0.54	(μs)	(Actual value: 0.540)
(Total conversion time: 1.26μs)			
ADST0 output setting			
☐ Enable ADST0 pin output			

Figure 4-40 Config_S12AD0 setting (2)

4.6 Pins configuration page

Smart Configurator assigns pins to the software components that are added to the project. Assignment of the pins can be changed at Pins page.

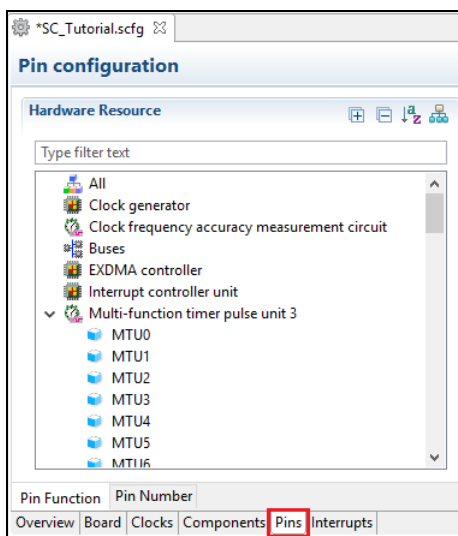


Figure 4-41 Pin configuration page

4.6.1 Change pin assignment of a software component

To change the pin assignment of a software component in Pin Function list. Click  to change view to show by Software Components.

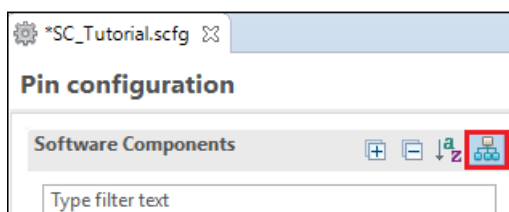


Figure 4-42 Change view to show by Software Components

Select the Config_ICU of software component. In the Pin Function list -> Assignment column, change the pin assignment IRQ0 to P10, IRQ9 to PB3. Ensure the 'Enable' tick box of IRQ0 and IRQ9 are checked, as shown in **Figure 4-43**.

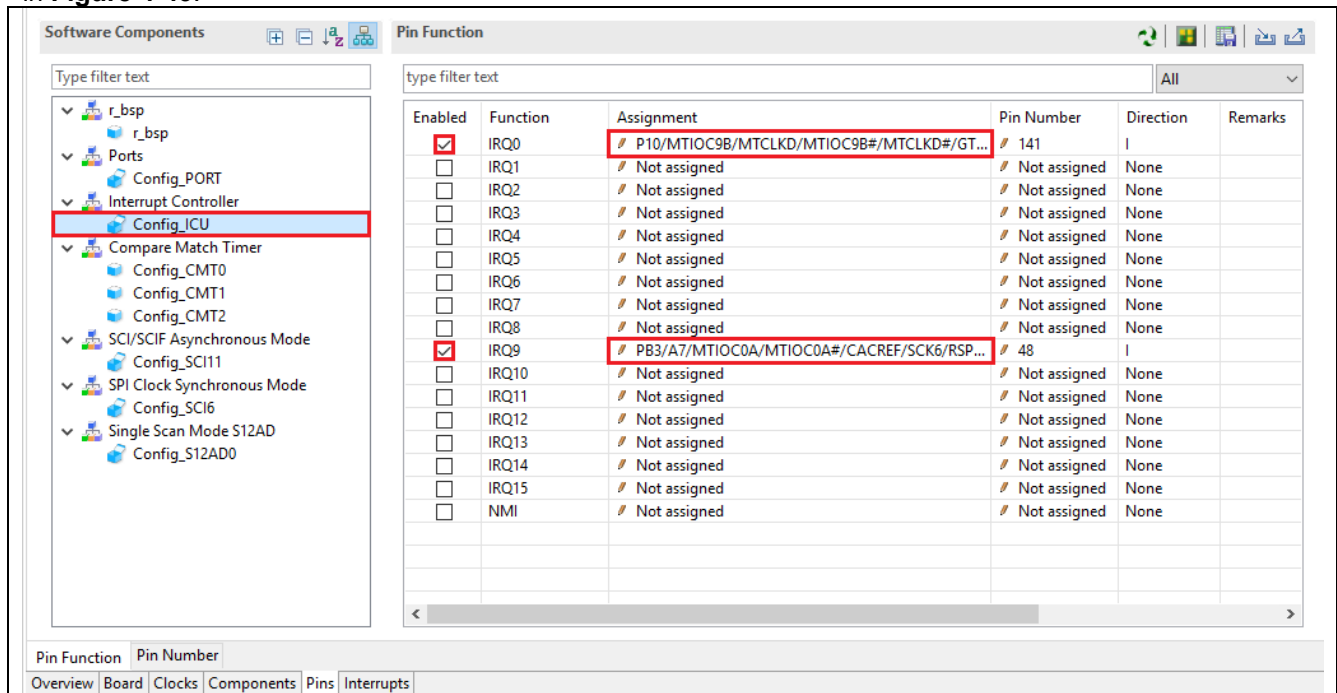


Figure 4-43 Configure pin assignment - Config_ICU

Select the Config_SCI11 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of RXD11 and TXD11 are checked and Assignment column of RXD11 is PB6 and TXD11 is PB5 as shown in **Figure 4-44**.

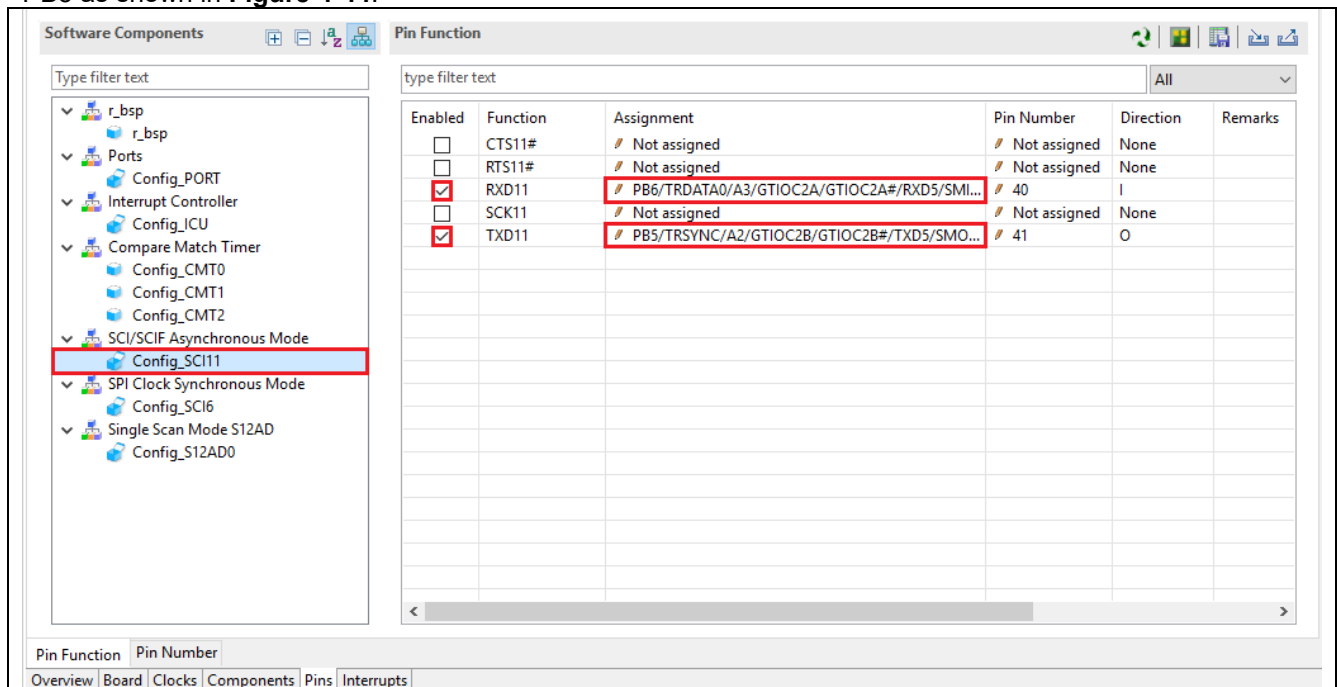


Figure 4-44 Configure pin assignment - Config_SCI11

Select the Config_SCI6 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of SCK6 and SMOSI6 are checked and Assignment column of SCK6 is PA4, SMOSI6 is PB0 as shown in **Figure 4-45**.

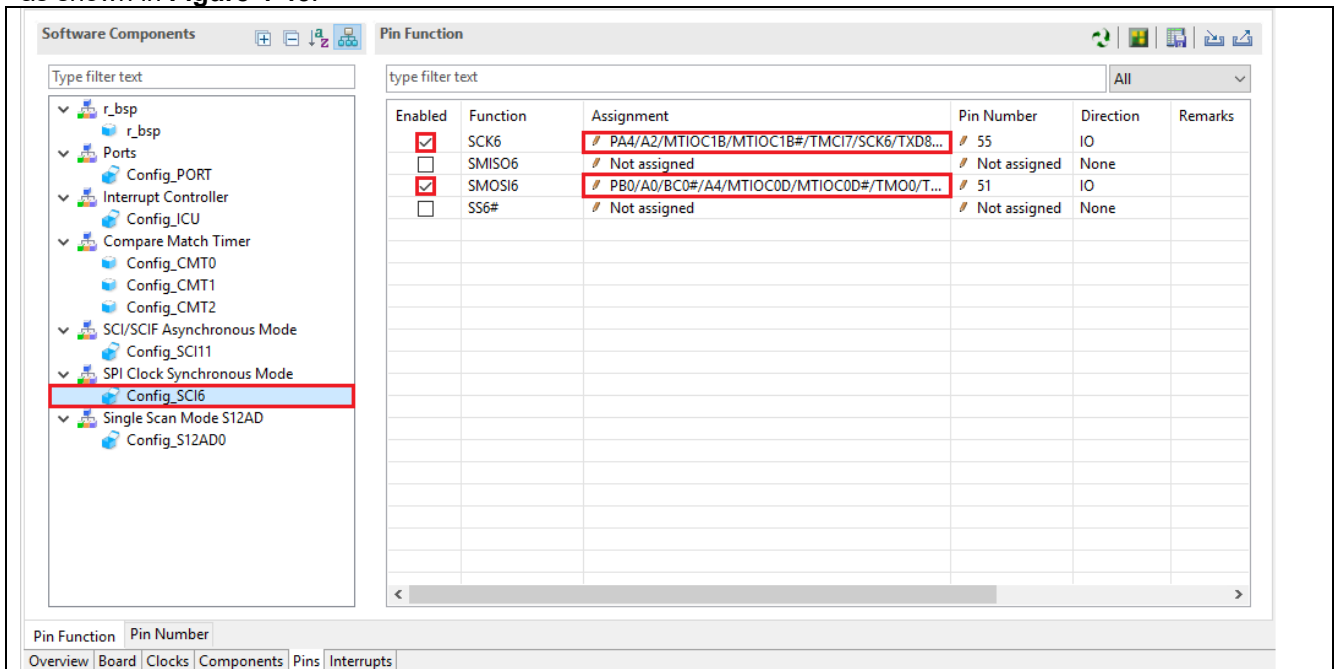


Figure 4-45 Configure pin assignment - Config_SCI6

Select the Config_S12AD0 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of AN000, AVCC0, AVSS0 and ADTRG0# are checked and Assignment column of AN000 is P40, ADTRG0# is P20 as shown in **Figure 4-46**.

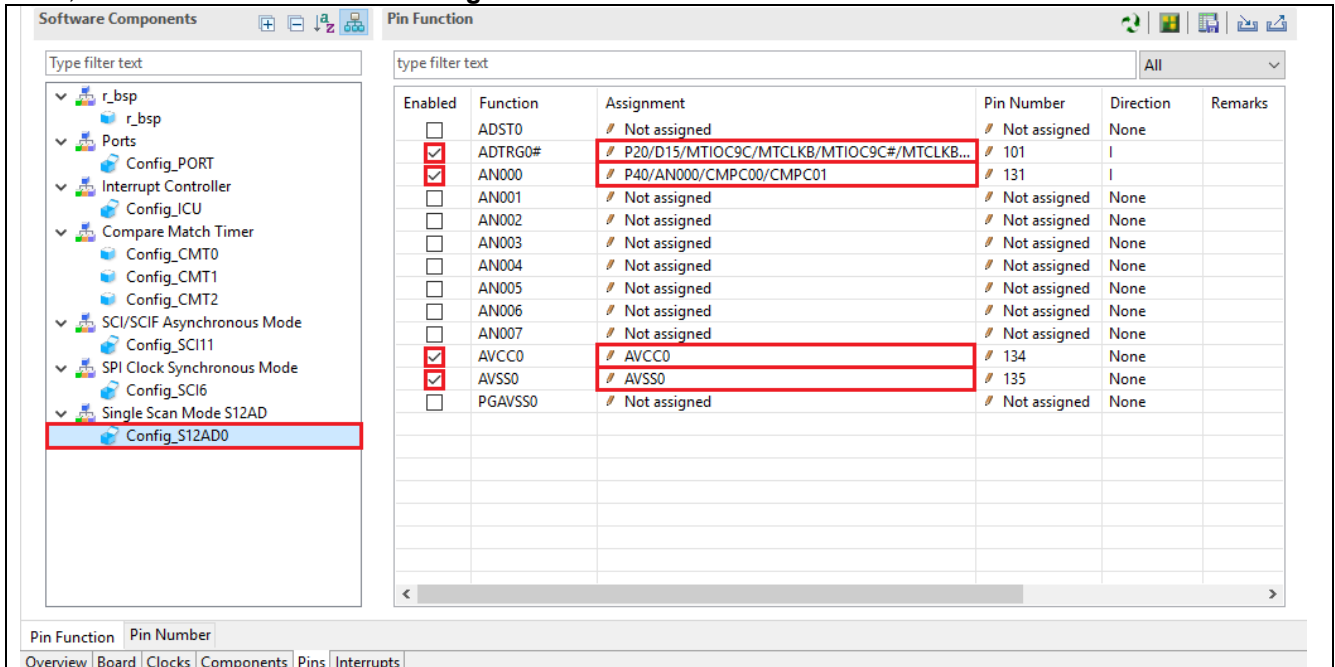


Figure 4-46 Configure pin assignment - Config_S12AD0

Peripheral function configuration is now complete. Save the project using the File -> Save, then click 'Generate Code' at location of **Figure 4-47**.

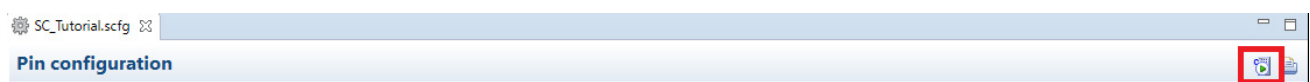


Figure 4-47 Generate Code Button

If the Section Setting Dialog is displayed as shown in the **Figure 4-48**, Please check the box and click “Yes”.

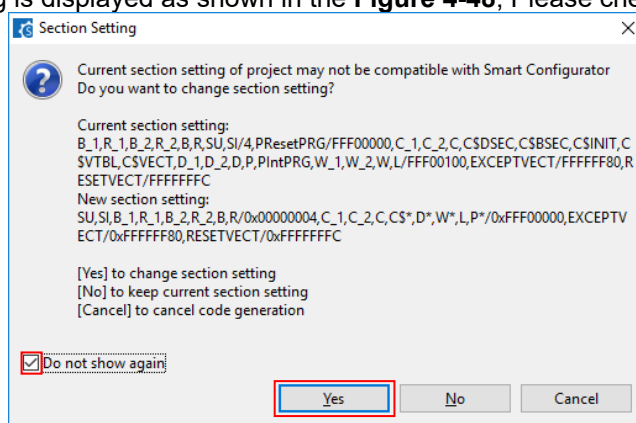


Figure 4-48 Section Setting Dialog

The Console pane should report ‘Code generation is successful’, as shown **Figure 4-49** below.

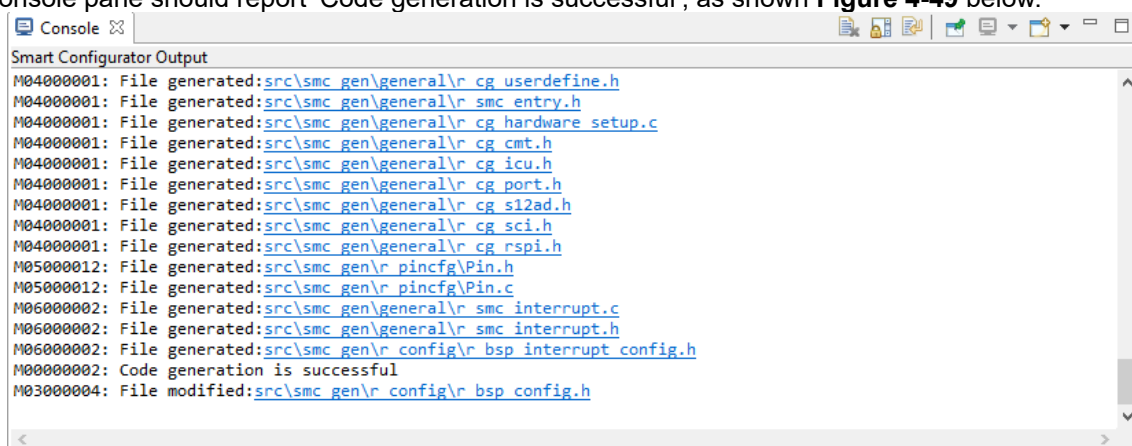


Figure 4-49 Smart Configurator console

When code generation is executed, the startup files generated at the time of CS+ project creation are replaced with those generated by Smart Configurator. **Figure 4-50** the project tree after code generation. In the next chapter, user code is added to these files, and SC_Tutorial is completed by adding a new source file to the project.

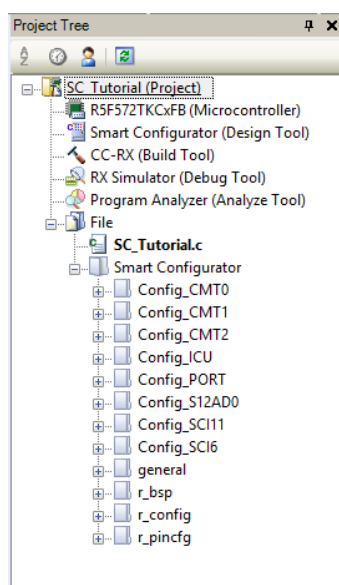
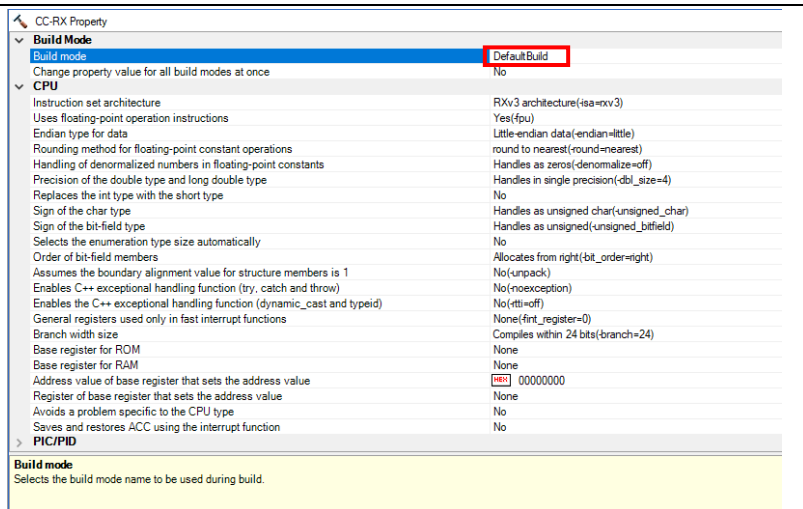


Figure 4-50 Smart Configurator folder structure

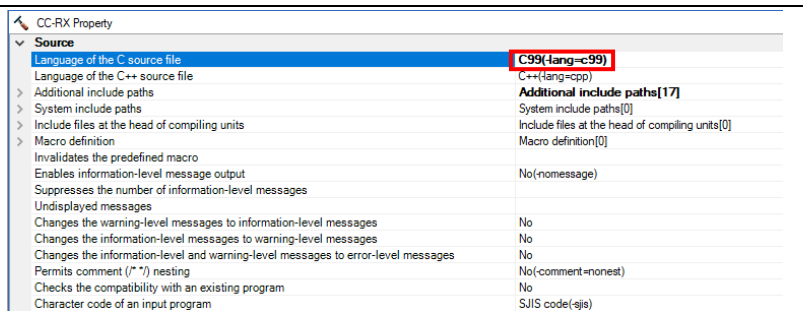
5. Completing the Tutorial Project

5.1 Project Settings

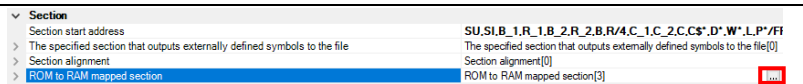
- In the 'Project Tree' pane, select 'CC-RX (Build Tool)'. The build properties will appear in the main window.
- CS+ creates a single build configuration called 'Default Build' for the project. This has standard code optimisation turned on by default.



- Select the 'Compile Options' tab at the bottom of the properties window pane. Under 'Language of the C source file' select 'C99(-lang=c99)' as shown opposite.



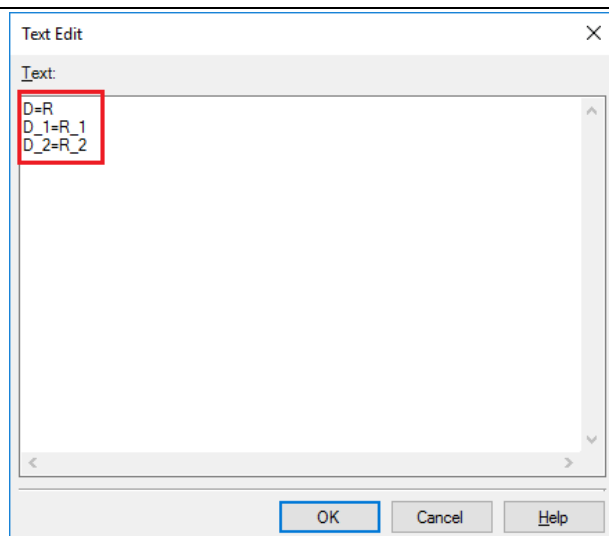
- Select the 'Link Options' tab at the bottom of the properties window pane. Under 'Section -> ROM to RAM mapped section', add the three mappings as shown opposite.



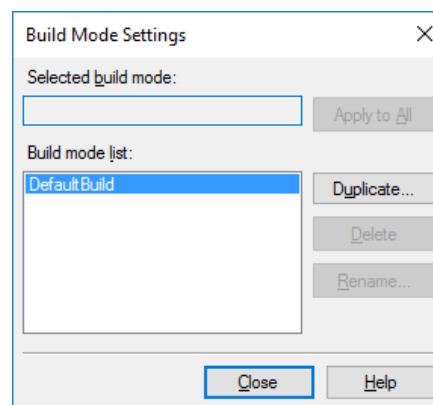
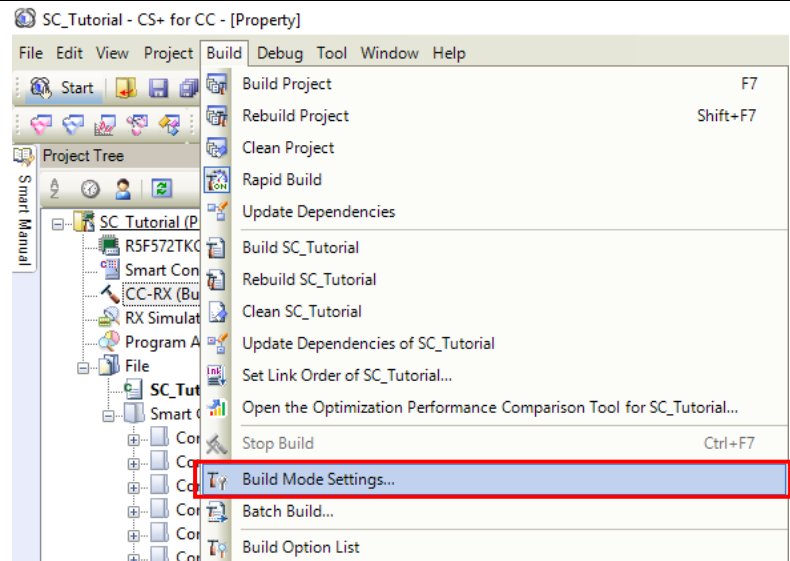
- These settings are easily added by clicking the button '...' and pasting the following text into the dialog:

D=R
D_1=R_1
D_2=R_2

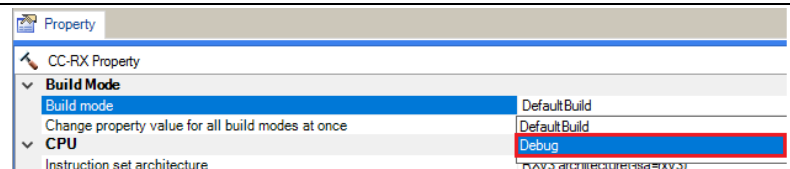
- This ensures that the linker assigns RAM rather than ROM addresses to C variables. Click 'OK'



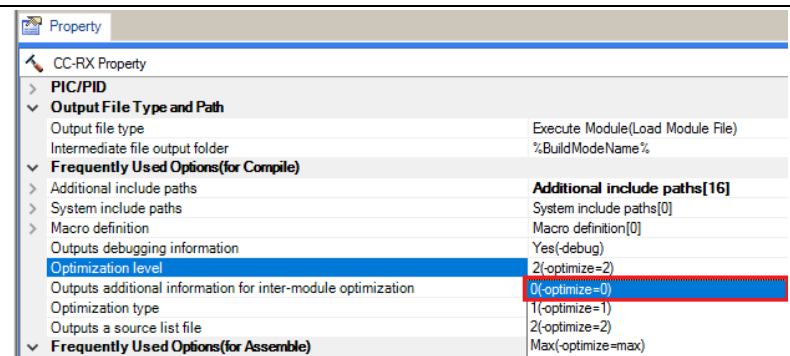
- From the 'Build' menu, select 'Build Mode Settings...'. Click 'Duplicate' and in the resulting 'Character String Input' dialog, enter 'Debug' for the name of the duplicate Build Mode.

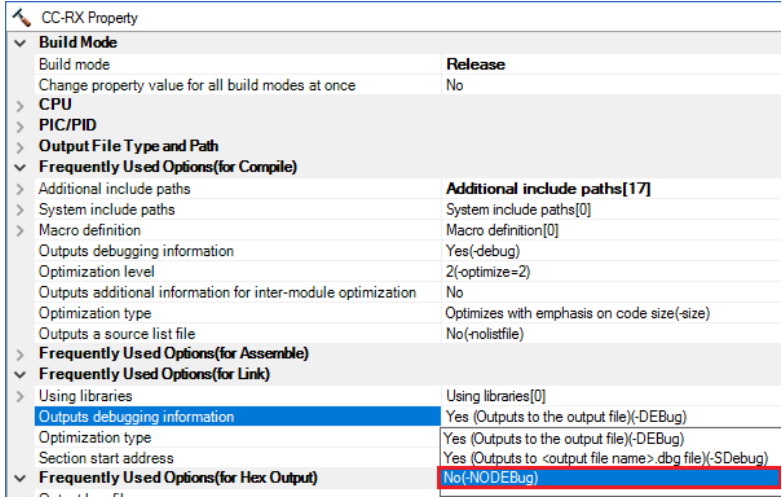


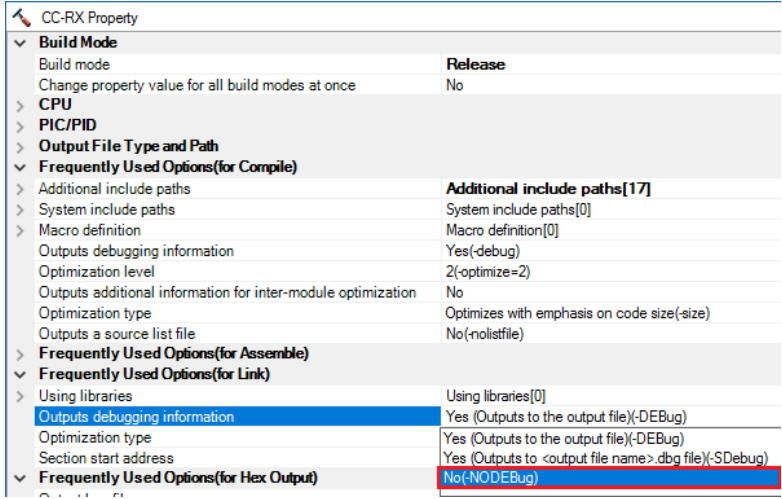
- The new 'Debug' Build Mode will be added to the Build Mode list. Click 'Close'. Now, in the main CC-RX Property window, under the 'Common Options' tab, click on the line containing 'Build Mode', click the pull-down arrow and select 'Debug' from the pull-down.



- In the 'Frequently Used Options (for Compile)' group, select the 'Optimization Level' option and select '0' from the pull-down. We have now created a 'Debug' Build Mode with no code optimisation and will be using the Build Mode to create and debug the project.

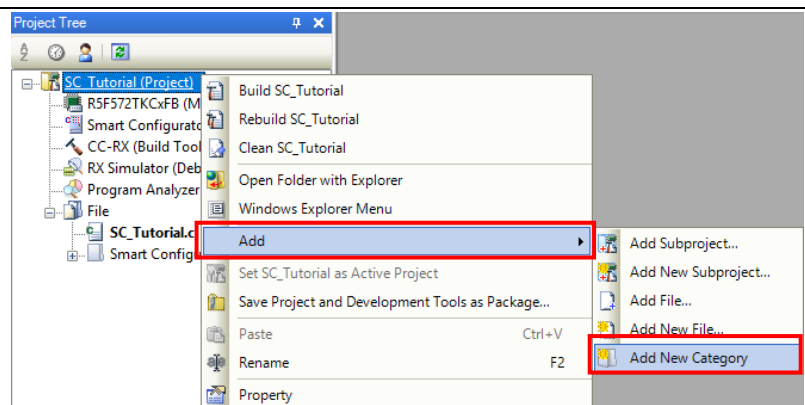


- All of the sample code projects contained in this RSK are configured with three Build Modes; 'DefaultBuild', 'Debug' and 'Release'. 'Release' is created in the same way as above; by duplicating 'DefaultBuild'. 'Release' Build Mode leaves code optimisation turned on and removes debug information from the output file.
- To remove debug information from the 'Release' Build Mode, in the 'CC-RX Property' window, select the 'Common Options' tab at the bottom of the window pane. For the 'Outputs debugging information' option, select 'No(-nodebug)'.

- Reset the Build Mode back to 'Debug' using the 'Build Mode' pull-down control.
- From the menus, select 'File -> Save All' to save all project settings.

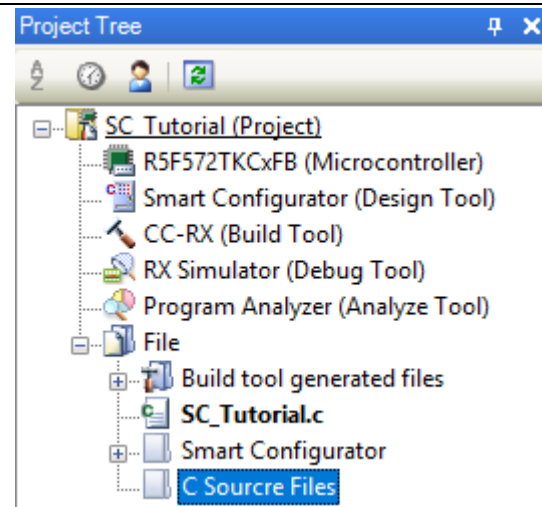


5.2 Additional Folders

- Before new source files are added to the project, we will create two additional folders in the CS+ Project Tree.
- In the Project Tree pane, right-click the SC_Tutorial project and select 'Add -> Add New Category'.

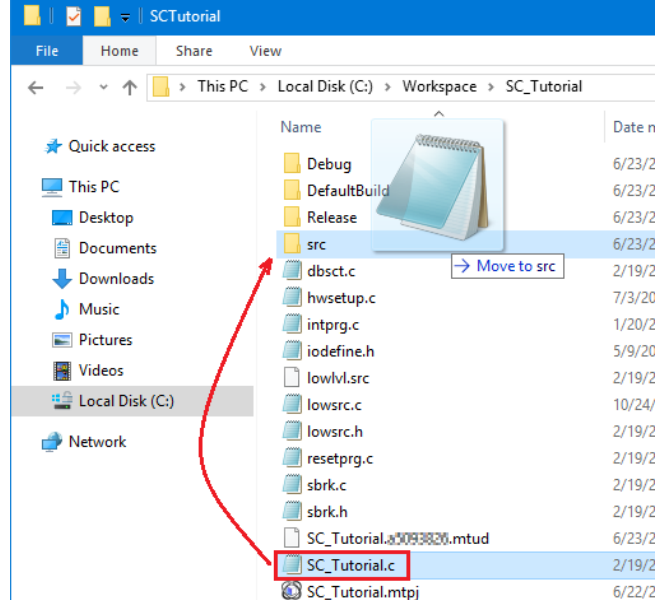
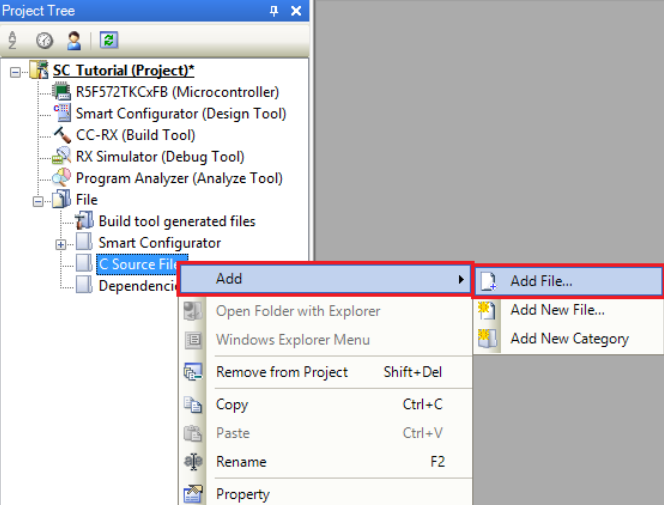
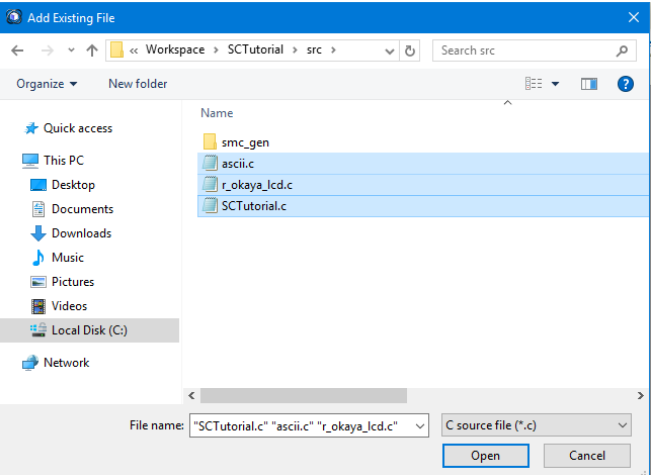


- Rename the newly-created 'New Category' folder to 'C Source Files'. Repeat these steps to create a new category folder for 'Dependencies'.



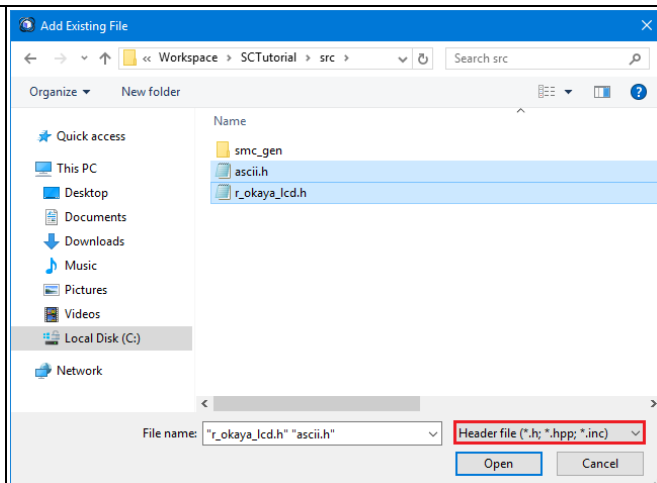
5.3 LCD Code Integration

API functions for the Okaya LCD display are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files `ascii.h`, `r_okaya_lcd.h`, `ascii.c`, and `r_okaya_lcd.c` in this folder. Copy these files into the `C:\Workspace\SC_Tutorial\src` folder.

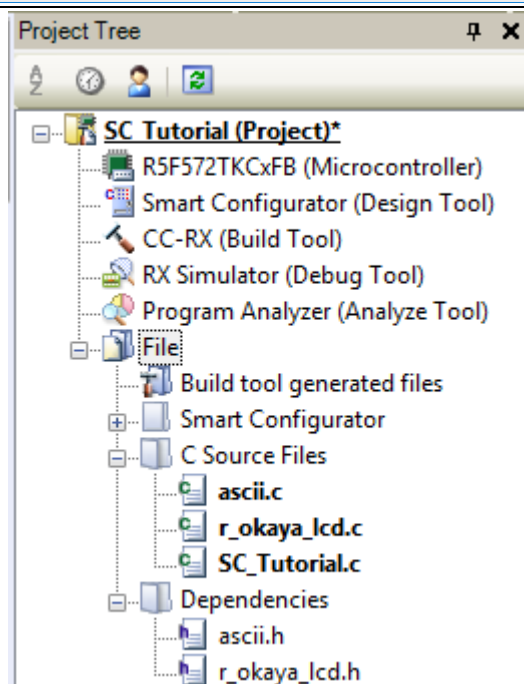
Move the 'SC_Tutorial.c' file from 'C:\Workspace\SC_Tutorial' to 'C:\Workspace\SC_Tutorial\src'.	
Right-click on the 'C Source Files' folder and select 'Add' -> 'Add File...'.	
Select the files to be added (<code>ascii.c</code>, <code>r_okaya_lcd.c</code>, <code>SC_Tutorial</code>) from <code>C:\Workspace\SC_Tutorial\src</code>.	

- Similarly, add 'ascii.h' and 'r_okaya_lcd.h' to the 'Dependencies' folder.

Note: Select the Header file (* .h; * .hpp; * .inc).



- Make sure the project tree is the same as the screen shot.



Code must be inserted in to the user code area in many files in this project, in the areas delimited by comments as follows:

```
/* Start user code for _xxxx_. Do not edit comment generated here */
/* End user code. Do not edit comment generated here */
```

Where `_xxxx_` depends on the particular area of code, i.e. 'function' for insertion of user functions and prototypes, 'global' for insertion of user global variable declarations, or 'include' for insertion of pre-processor include directives. User code inserted inside these comment delimiters is protected from being overwritten by Smart Configurator, if the user needs to subsequently change any of the Smart Configurator-generated code.

In the CS+ Project Tree, expand the 'src/smc_gen/general' folder and open the file 'r_cg_userdefine.h' by double-clicking on it. Insert the following #defines in between the user code delimiter comments as shown below.

```
/* Start user code for function. Do not edit comment generated here */
#define TRUE          (1)
#define FALSE         (0)

/* End user code. Do not edit comment generated here */
```

In the CS+ Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c' by double-clicking on it. Add header files above the 'main' function as shown below.

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
```

Scroll down to the 'main' function and insert the highlighted code as shown below into the beginning of the 'main' function:

```
void main(void)
{
    /* Initialize the debug LCD */
    R_LCD_Init();

    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *) "RSKR72T ");
    R_LCD_Display(1, (uint8_t *) "Tutorial ");
    R_LCD_Display(2, (uint8_t *) "Press Any Switch");
    while (1U)
    {
        ;
    }
}
```

5.3.1 SPI Code

The Okaya LCD display is driven by the SPI Master that was configured using Smart Configurator in §4.5.6. In the CS+ Project Tree, expand the 'Smart Configurator/Config_SCI6' and open the file 'Config_SCI6.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
/* Exported functions used to transmit a number of bytes and wait for completion */
MD_STATUS R_SCI6_SPIMasterTransmit(uint8_t * const tx_buf, const uint16_t tx_num);
/* End user code. Do not edit comment generated here */
```

Now, open the Config_SCI6_user.c file and insert the following code in the user area for global:

```
/* Start user code for global. Do not edit comment generated here */
/* Flag used locally to detect transmission complete */
static volatile uint8_t gs_sci6_txdone;
/* End user code. Do not edit comment generated here */
```

Insert the following code in the transmit end call-back function for SCI6:

```
static void r_Config_SCI6_callback_transmitend(void)
{
    /* Start user code for r_Config_SCI6_callback_transmitend. Do not edit comment generated here */
    gs_sci6_txdone = TRUE;
    /* End user code. Do not edit comment generated here */
}
```

Now insert the following function in the user code area at the end of the file:

```
/* Start user code for adding. Do not edit comment generated here */

/*****
* Function Name: R_SCI6_SPIMasterTransmit
* Description : This function sends SPI6 data to slave device.
* Arguments : tx_buf -
*             transfer buffer pointer
*             tx_num -
*             buffer size
* Return Value : status -
*               MD_OK or MD_ARGERROR
*****/
MD_STATUS R_SCI6_SPIMasterTransmit (uint8_t * const tx_buf,
                                     const uint16_t tx_num)
{
    MD_STATUS status = MD_OK;

    /* Clear the flag before initiating a new transmission */
    gs_sci6_txdone = FALSE;

    /* Send the data using the API */
    status = R_Config_SCI6_SPI_Master_Send(tx_buf, tx_num);

    /* Wait for the transmit end flag */
    while (FALSE == gs_sci6_txdone)
    {
        /* Wait */
    }

    return (status);
}

/*****
* End of function R_SCI6_SPIMasterTransmit
*****/
```

This function uses the transmit end callback function to perform flow control on the SPI transmission to the LCD, and is used as the main API call in the LCD code module.

5.3.2 CMT Code

The LCD code needs to insert delays to meet the timing requirements of the display module. This is achieved using the dedicated timer which was configured using Smart Configurator in §4.5.2. In the CS+ Project Tree, expand the 'Smart Configurator\Config_CMT0\Config_CMT0.h' and insert the following code in the user area for function at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */

void R_CMT_MsDelay(const uint16_t millisec);

/* End user code. Do not edit comment generated here */
```

Open the file 'Config_CMT0_user.c' and insert the following code in the user area for global at the beginning of the file:

```
/* Start user code for global. Do not edit comment generated here */

static volatile uint8_t gs_one_ms_delay_complete = FALSE;

/* End user code. Do not edit comment generated here */
```

Scroll down to the r_Config_CMT0_cmi0_interrupt function and insert the following line in the user code area:

```
static void r_Config_CMT0_cmi0_interrupt(void)
{
    /* Start user code for r_Config_CMT0_cmi0_interrupt. Do not edit comment generated here */

    gs_one_ms_delay_complete = TRUE;

    /* End user code. Do not edit comment generated here */
}
```

Then insert the following function in the user code area at the end of the file:

```
/* Start user code for adding. Do not edit comment generated here */

/*****
 * Function Name: R_CMT_MsDelay
 * Description   : Uses CMT0 to wait for a specified number of milliseconds
 * Arguments     : uint16_t millisecs, number of milliseconds to wait
 * Return Value  : None
 *****/
void R_CMT_MsDelay (const uint16_t millisec)
{
    uint16_t ms_count = 0;

    do
    {
        R_Config_CMT0_Start();
        while (FALSE == gs_one_ms_delay_complete)
        {
            /* Wait */
        }
        R_Config_CMT0_Stop();
        gs_one_ms_delay_complete = FALSE;
        ms_count++;
    } while (ms_count < millisec);
}
/*****
End of function R_CMT_MsDelay
*****/
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The program will display 'RSKR72T Tutorial Press Any Switch' on 3 lines in the LCD display.

5.4 Switch Code Integration

API functions for user switch control are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files `rskrx72tdef.h`, `r_rsk_switch.h` and `r_rsk_switch.c` in this folder. Copy these files into the `C:\Workspace\SC_Tutorial\src` folder. Add these three files into the project in the same way as the LCD files.

The switch code uses interrupt code in the files `Config_ICU.c`, `Config_ICU_user.c` and `Config_ICU.h` and timer code in the files `Config_ICU.c`, `Config_ICU_user.c`, `Config_CMT1.h`, `Config_CMT1.c`, `Config_CMT1_user.c`, `Config_CMT2.h`, `Config_CMT2.c`, and `Config_CMT2_user.c`, as described in §4.5.2 and §4.5.3. It is necessary to provide additional user code in these files to implement the switch press/release detection and de-bouncing required by the API functions in `r_rsk_switch.c`.

5.4.1 Interrupt Code

In the CS+ Project Tree, expand the 'Smart Configurator/Config_ICU' folder and open the file 'Config_ICU.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */

/* Function prototypes for detecting and setting the edge trigger of ICU_IRQ */
uint8_t R_ICU_IRQIsFallingEdge(const uint8_t irq_no);
void R_ICU_IRQSetFallingEdge(const uint8_t irq_no, const uint8_t set_f_edge);
void R_ICU_IRQSetRisingEdge(const uint8_t irq_no, const uint8_t set_r_edge);

/* End user code. Do not edit comment generated here */
```

Now, open the `Config_ICU.c` file and insert the following code in the user code area at the end of the file:

```
/* Start user code for adding. Do not edit comment generated here */

/*****
 * Function Name: R_ICU_IRQIsFallingEdge
 * Description  : This function returns 1 if the specified ICU_IRQ is set to
 *               falling edge triggered, otherwise 0.
 * Arguments    : uint8_t irq_no
 * Return Value : 1 if falling edge triggered, 0 if not
 *****/
uint8_t R_ICU_IRQIsFallingEdge (const uint8_t irq_no)
{
    uint8_t falling_edge_trig = 0x0;

    if (ICU.IRQCR[irq_no].BYTE & _04_ICU_IRQ_EDGE_FALLING)
    {
        falling_edge_trig = 1;
    }

    return (falling_edge_trig);
}

/*****
 * End of function R_ICU_IRQIsFallingEdge
 *****/
```

```

/*****
* Function Name: R_ICU_IRQSetFallingEdge
* Description  : This function sets/clears the falling edge trigger for the
*                specified ICU_IRQ.
* Arguments    : uint8_t irq_no
*                uint8_t set_f_edge, 1 if setting falling edge triggered, 0 if
*                clearing
* Return Value : None
*****/
void R_ICU_IRQSetFallingEdge (const uint8_t irq_no, const uint8_t set_f_edge)
{
    if (1 == set_f_edge)
    {
        ICU.IRQCR[irq_no].BYTE |= _04_ICU_IRQ_EDGE_FALLING;
    }
    else
    {
        ICU.IRQCR[irq_no].BYTE &= (uint8_t) ~_04_ICU_IRQ_EDGE_FALLING;
    }
}

/*****
* End of function R_ICU_IRQSetFallingEdge
*****/

/*****
* Function Name: R_ICU_IRQSetRisingEdge
* Description  : This function sets/clear the rising edge trigger for the
*                specified ICU_IRQ.
* Arguments    : uint8_t irq_no
*                uint8_t set_r_edge, 1 if setting rising edge triggered, 0 if
*                clearing
* Return Value : None
*****/
void R_ICU_IRQSetRisingEdge (const uint8_t irq_no, const uint8_t set_r_edge)
{
    if (1 == set_r_edge)
    {
        ICU.IRQCR[irq_no].BYTE |= _08_ICU_IRQ_EDGE_RISING;
    }
    else
    {
        ICU.IRQCR[irq_no].BYTE &= (uint8_t) ~_08_ICU_IRQ_EDGE_RISING;
    }
}

/*****
* End of function R_ICU_IRQSetRisingEdge
*****/

/* End user code. Do not edit comment generated here */

```


Open the Config_ICU_user.c file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */  
/* Defines switch callback functions required by interrupt handlers */  
#include "r_rsk_switch.h"  
/* End user code. Do not edit comment generated here */
```

In the same file insert the following code in the user code area inside the function r_Config_ICU_irq0_interrupt:

```
/* Start user code for r_Config_ICU_irq0_interrupt. Do not edit comment generated here */  
/* Switch 1 callback handler */  
R_SWITCH_IsrCallback1();  
/* End user code. Do not edit comment generated here */
```

In the same file insert the following code in the user code area inside the function r_Config_ICU_irq9_interrupt:

```
/* Start user code for r_Config_ICU_irq9_interrupt. Do not edit comment generated here */  
/* Switch 2 callback handler */  
R_SWITCH_IsrCallback2();  
/* End user code. Do not edit comment generated here */
```

5.4.2 De-bounce Timer Code

In the Project Tree, expand the 'Smart Configurator\Config_CMT1' folder and open the 'Config_CMT1_user.c' file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */  
  
/* Defines switch callback functions required by interrupt handlers */  
#include "r_rsk_switch.h"  
  
/* End user code. Do not edit comment generated here */
```

In the 'Config_CMT1_user.c' file insert the following code in the user code area inside the function `r_Config_CMT1_cmi1_interrupt`:

```
/* Start user code for r_Config_CMT1_cmi1_interrupt. Do not edit comment generated here */  
  
/* Stop this timer - we start it again in the de-bounce routines */  
R_Config_CMT1_Stop();  
  
/* Call the de-bounce call back routine */  
R_SWITCH_DebounceIsrCallback();  
  
/* End user code. Do not edit comment generated here */
```

In the Project Tree, expand the 'Smart Configurator\Config_CMT2' folder and open the 'Config_CMT2_user.c' file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */  
  
/* Defines switch callback functions required by interrupt handlers */  
#include "r_rsk_switch.h"  
  
/* End user code. Do not edit comment generated here */
```

Open the 'Config_CMT2_user.c' file and insert the following code in the user code area inside the function `r_Config_CMT2_cmi2_interrupt`:

```
/* Start user code for r_Config_CMT2_cmi2_interrupt. Do not edit comment generated here */  
  
/* Stop this timer - we start it again in the de-bounce routines */  
R_Config_CMT2_Stop();  
  
/* Call the de-bounce call back routine */  
R_SWITCH_DebounceIsrCallback();  
  
/* End user code. Do not edit comment generated here */
```

5.4.3 Main Switch and ADC Code

In this part of the tutorial we add the code to act on the switch presses to activate A/D conversions and display the result on the LCD. In §4.5.7 we configured the ADC to be triggered from the ADTRG0# pin, SW3. In this code, we also perform software triggered A/D conversion from the user switches SW1 and SW2, by reconfiguring the ADC trigger source on-the-fly once an SW1 or SW2 press is detected.

In the CS+ Project Tree, expand the 'Smart Configurator\general' folder and open the file 'r_cg_userdefine.h' by double-clicking on it. Insert the following code the user code area, resulting in the code shown below

```
/* Start user code for function. Do not edit comment generated here */

#define TRUE          (1)
#define FALSE        (0)
extern volatile uint8_t g_adc_trigger;

/* End user code. Do not edit comment generated here */
```

In the Project Tree, expand the 'C Source Files' folder and Open the file 'SC_Tutorial.c' and add the highlighted code, resulting in the code shown below:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config S12AD0.h"
#include "r_rsk_switch.h"

/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;

/* Prototype declaration for cb switch_press */
static void cb_switch_press (void);

/* Prototype declaration for get_adc */
static uint16_t get_adc(void);

/* Prototype declaration for lcd display adc */
static void lcd_display_adc (const uint16_t adc_result);
```

Next add the highlighted code below in the main function and the code inside the while loop, resulting in the code shown below:

```
void main(void)
{
    /* Initialize the switch module */
    R_SWITCH_Init();

    /* Set the call back function when SW1 or SW2 is pressed */
    R_SWITCH_SetPressCallback(cb_switch_press);

    /* Initialize the debug LCD */
    R_LCD_Init();

    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *) "RSKR72T ");
    R_LCD_Display(1, (uint8_t *) "Tutorial ");
    R_LCD_Display(2, (uint8_t *) "Press Any Switch ");

    /* Start the A/D converter */
    R_Config_S12AD0_Start();

    while (1U)
    {
        uint16_t adc_result;

        /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
        if (TRUE == g_adc_trigger)
        {
            /* Call the function to perform an A/D conversion */
            adc_result = get_adc();

            /* Display the result on the LCD */
            lcd_display_adc(adc_result);

            /* Reset the flag */
            g_adc_trigger = FALSE;
        }
        /* SW3 is directly wired into the ADTRG0n pin so will
        cause the interrupt to fire */
        else if (TRUE == g_adc_complete)
        {
            /* Get the result of the A/D conversion */
            R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);

            /* Display the result on the LCD */
            lcd_display_adc(adc_result);

            /* Reset the flag */
            g_adc_complete = FALSE;
        }
        else
        {
            /* do nothing */
        }
    }
}
```

Then add the definition for the switch call-back, get_adc and lcd_display_adc functions adding at the below of the main function, as shown below:

```
/* *****
 * Function Name : cb_switch_press
 * Description   : Switch press callback function. Sets g_adc_trigger flag.
 * Argument      : none
 * Return value  : none
 * *****/
static void cb_switch_press (void)
{
    /* Check if switch 1 or 2 was pressed */
    if (g_switch_flag & (SWITCHPRESS_1 | SWITCHPRESS_2))
    {
```

```

        /* set the flag indicating a user requested A/D conversion is required */
        g_adc_trigger = TRUE;

        /* Clear flag */
        g_switch_flag = 0x0;
    }
}
/*****
* End of function cb_switch_press
*****/

/*****
* Function Name : get_adc
* Description    : Reads the ADC result, converts it to a string and displays
*                  it on the LCD panel.
* Argument       : none
* Return value   : uint16_t adc value
*****/
static uint16_t get_adc (void)
{
    /* A variable to retrieve the adc result */
    uint16_t adc_result;

    /* Stop the A/D converter being triggered from the pin ADTRG0n */
    R_Config_S12AD0_Stop();

    /* Start a conversion */
    R_S12AD0_SWTriggerStart();

    /* Wait for the A/D conversion to complete */
    while (FALSE == g_adc_complete)
    {
        /* Wait */
    }

    /* Stop conversion */
    R_S12AD0_SWTriggerStop();

    /* Clear ADC flag */
    g_adc_complete = FALSE;

    R_Config_S12AD0_Get_ValueResult (ADCHANNEL0, &adc_result);

    /* Set AD conversion start trigger source back to ADTRG0n pin */
    R_Config_S12AD0_Start();

    return (adc_result);
}
/*****
* End of function get_adc
*****/

/*****
* Function Name : lcd_display_adc
* Description    : Converts adc result to a string and displays
*                  it on the LCD panel.
* Argument       : uint16_t adc result
* Return value   : none
*****/
static void lcd_display_adc (const uint16_t adc_result)
{
    /* Declare a temporary variable */
    uint8_t a;

    /* Declare temporary character string */
    char    lcd_buffer[11] = " ADC: XXXH";

    /* Convert ADC result into a character string, and store in the local.
       Casting to ensure use of correct data type. */
    a = (uint8_t)((adc_result & 0x0F00) >> 8);
    lcd_buffer[6] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
    a = (uint8_t)((adc_result & 0x00F0) >> 4);

```

```

    lcd_buffer[7] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
    a = (uint8_t)(adc_result & 0x000F);
    lcd_buffer[8] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));

    /* Display the contents of the local string lcd_buffer */
    R_LCD_Display(3, (uint8_t *)lcd_buffer);
}
/*****
* End of function lcd_display_adc
*****/

```

In the Project Tree, expand the 'Smart Configurator\Config_S12AD0' folder and open the file 'Config_S12AD0.h' by double-clicking on it. Insert the following code in the user code area for function, resulting in the code shown below:

```

/* Start user code for function. Do not edit comment generated here */

/* Flag indicates when A/D conversion is complete */
extern volatile uint8_t g_adc_complete;

/* Functions for starting and stopping software triggered A/D conversion */
void R_S12AD0_SWTriggerStart(void);
void R_S12AD0_SWTriggerStop(void);

/* End user code. Do not edit comment generated here */

```

Open the file 'Config_S12AD0.c' by double-clicking on it. Insert the following code in the user code area for adding at the end of the file, resulting in the code shown below:

```

/* Start user code for adding. Do not edit comment generated here */

/*****
* Function Name: R_S12AD0_SWTriggerStart
* Description   : This function starts the AD0 converter.
* Arguments     : None
* Return Value  : None
*****/
void R_S12AD0_SWTriggerStart(void)
{
    IR(S12AD, S12ADI) = 0U;
    IEN(S12AD, S12ADI) = 1U;
    S12AD.ADCSR.BIT.ADST = 1U;
}

/*****
End of function R_S12AD0_SWTriggerStart
*****/

/*****
* Function Name: R_S12AD0_SWTriggerStop
* Description   : This function stops the AD0 converter.
* Arguments     : None
* Return Value  : None
*****/
void R_S12AD0_SWTriggerStop(void)
{
    S12AD.ADCSR.BIT.ADST = 0U;
    IEN(S12AD, S12ADI) = 0U;
    IR(S12AD, S12ADI) = 0U;
}

/*****
End of function R_S12AD0_SWTriggerStop
*****/

/* End user code. Do not edit comment generated here */

```

Open the file `Config_S12AD0_user.c` and insert the following code in the user code area for global, resulting in the code shown below:

```
/* Start user code for global. Do not edit comment generated here */  
  
/* Flag indicates when A/D conversion is complete */  
volatile uint8_t g_adc_complete;  
  
/* End user code. Do not edit comment generated here */
```

Insert the following code in the user code area of the `r_Config_S12AD0_interrupt` function, resulting in the code shown below:

```
static void r_Config_S12AD0_interrupt(void)  
{  
    /* Start user code for r_Config_S12AD0_interrupt. Do not edit comment generated here */  
  
    g_adc_complete = TRUE;  
  
    /* End user code. Do not edit comment generated here */  
}
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. When any switch is pressed, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel. Return to this point in the SC_Tutorial to add the UART user code.

5.5 Debug Code Integration

API functions for trace debugging via the RSK serial port are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files `r_rsk_debug.h` and `r_rsk_debug.c` in this folder. Copy these files into the `C:\Workspace\SC_Tutorial\src` folder. Add these two files into the project in the same way as the LCD files.

In the `r_rsk_debug.h` file, ensure the following macro definition is included:

```
/* Macro for definition of serial debug transmit function - user edits this */
#define SERIAL_DEBUG_WRITE (R_SCI11_AsyncTransmit)
```

This macro is referenced in the `r_rsk_debug.c` file and allows easy re-direction of debug output if a different debug interface is used.

5.6 UART Code Integration

5.6.1 SCI Code

In the CS+ Project Tree, expand the 'Smart Configurator\Config_SCI11' folder and open the file 'Config_SCI11.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */

/* Exported functions used to transmit a number of bytes and wait for completion */
MD_STATUS R_SCI11_AsyncTransmit(uint8_t * const tx_buf, const uint16_t tx_num);

/* Character is used to receive key presses from PC terminal */
extern uint8_t g_rx_char;

/* End user code. Do not edit comment generated here */
```

Open the file 'Config_SCI11_user.c'. Insert the following code in the user area for global near the beginning of the file:

```
/* Start user code for global. Do not edit comment generated here */

/* Global used to receive a character from the PC terminal */
uint8_t g_rx_char;

/* Flag used locally to detect transmission complete */
static volatile uint8_t gs_sci11_txdone;

/* End user code. Do not edit comment generated here */
```

In the same file, insert the following code in the user code area inside the `r_Config_SCI11_callback_transmitend` function:

```
static void r_Config_SCI11_callback_transmitend (void)
{
    /* Start user code for r_Config_SCI11_callback_transmitend. Do not edit comment generated here */

    gs_sci11_txdone = TRUE;

    /* End user code. Do not edit comment generated here */
}
```


In the same file, insert the following code in the user code area inside the `r_Config_SCI11_callback_receiveend` function:

```
static void r_Config_SCI11_callback_receiveend(void)
{
    /* Start user code for r_Config_SCI11_callback_receiveend. Do not edit comment generated here */

    /* Check the contents of g_rx_char */
    if (('c' == g_rx_char) || ('C' == g_rx_char))
    {
        g_adc_trigger = TRUE;
    }

    /* Set up SCI11 receive buffer and callback function again */
    R_Config_SCI11_Serial_Receive((uint8_t *)&g_rx_char, 1);

    /* End user code. Do not edit comment generated here */
}
```

At the end of the file, in the user code area for adding, add the following function definition:

```

/*****
* Function Name: R_SCI11_AsyncTransmit
* Description   : This function sends SCI11 data and waits for the transmit end flag.
* Arguments    : tx_buf -
*                transfer buffer pointer
*                tx_num -
*                buffer size
* Return Value : status -
*                MD_OK or MD_ARGERROR
*****/
MD_STATUS R_SCI11_AsyncTransmit(uint8_t * const tx_buf, const uint16_t tx_num)
{
    MD_STATUS status = MD_OK;

    /* Clear the flag before initiating a new transmission */
    gs_sci11_txdone = FALSE;

    /* Send the data using the API */
    status = R_Config_SCI11_Serial_Send(tx_buf, tx_num);

    /* Wait for the transmit end flag */
    while (FALSE == gs_sci11_txdone)
    {
        /* Wait */
    }
    return (status);
}

/*****
* End of function R_SCI11_AsyncTransmit
*****/

```

5.6.2 Main UART code

In the Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c'. Add the following declaration to above the 'main' function:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config_S12AD0.h"
#include "r_rsk_switch.h"
#include "r_rsk_debug.h"
#include "Config_SCI11.h"

/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;

/* Prototype declaration for cb_switch_press */
static void cb_switch_press (void);

/* Prototype declaration for get_adc */
static uint16_t get_adc(void);

/* Prototype declaration for lcd_display_adc */
static void lcd_display_adc (const uint16_t adc_result);

/* Prototype declaration for uart display adc */
static void uart_display_adc(const uint8_t gs_adc_count, const uint16_t adc_result);

/* Variable to store the A/D conversion count for user display */
static uint8_t gs_adc_count = 0;
```

Add the following highlighted code to the main function:

```
void main(void)
{
    /* Initialize the switch module */
    R_SWITCH_Init();

    /* Set the call back function when SW1 or SW2 is pressed */
    R_SWITCH_SetPressCallback(cb_switch_press);

    /* Initialize the debug LCD */
    R_LCD_Init();

    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *) "RSKR72T ");
    R_LCD_Display(1, (uint8_t *) "Tutorial ");
    R_LCD_Display(2, (uint8_t *) "Press Any Switch ");

    /* Start the A/D converter */
    R_Config_S12AD0_Start();

    /* Set up SCI11 receive buffer and callback function */
    R_Config_SCI11_Serial_Receive((uint8_t *)&g_rx_char, 1);

    /* Enable SCI11 operations */
    R_Config_SCI11_Start();

    while (1U)
    {
        uint16_t adc_result;

        /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
        if (TRUE == g_adc_trigger)
        {
            /* Call the function to perform an A/D conversion */
            adc_result = get_adc();

            /* Display the result on the LCD */
            lcd_display_adc(adc_result);

            /* Increment the gs_adc_count */
            if (16 == (++gs_adc_count))
            {
                gs_adc_count = 0;
            }

            /* Send the result to the UART */
        }
    }
}
```

```

    uart_display_adc(gs_adc_count, adc_result);

    /* Reset the flag */
    g_adc_trigger = FALSE;
}
/* SW3 is directly wired into the ADTRG0n pin so will
   cause the interrupt to fire */
else if (TRUE == g_adc_complete)
{
    /* Get the result of the A/D conversion */
    R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);

    /* Display the result on the LCD */
    lcd_display_adc(adc_result);

    /* Increment the gs adc count */
    if (16 == (++gs_adc_count))
    {
        gs_adc_count = 0;
    }

    /* Send the result to the UART */
    uart_display_adc(gs_adc_count, adc_result);

    /* Reset the flag */
    g_adc_complete = FALSE;
}
else
{
    /* do nothing */
}
}
}

```

Then, add the following function definition in the end of the file:

```

/*****
* Function Name : uart_display_adc
* Description   : Converts adc result to a string and sends it to the UART1.
* Argument      : uint8_t : gs_adc_count
*                uint16_t: adc result
* Return value  : none
*****/
static void uart_display_adc (const uint8_t gs_adc_count, const uint16_t adc_result)
{
    /* Declare a temporary variable */
    char a;

    /* Declare temporary character string */
    static char uart_buffer[] = "ADC xH Value: xxxH\r\n";

    /* Convert ADC result into a character string, and store in the local.
       Casting to ensure use of correct data type. */
    a = (char)(gs_adc_count & 0x000F);
    uart_buffer[4] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
    a = (char)((adc_result & 0x0F00) >> 8);
    uart_buffer[14] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
    a = (char)((adc_result & 0x00F0) >> 4);
    uart_buffer[15] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
    a = (char)(adc_result & 0x000F);
    uart_buffer[16] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));

    /* Send the string to the UART */
    R_DEBUG_Print(uart_buffer);
}

/*****
* End of function uart_display_adc
*****/

```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. Connect the RSK G1CUSB0 port to a USB port on a PC. If this is the first time the RSK has been connected to the PC then a device driver will be installed automatically. Open Device Manager, the virtual COM port will be appeared under 'Port (COM & LPT)' as 'RSK USB Serial Port (COMx)', where x is a number.

Open a terminal program, such as HyperTerminal, on the PC with the same settings as for SCI11 (see §4.5.5). When any switch is pressed, or when 'c' is sent via the COM port, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel and send the result to the PC terminal program via the SCI11. Return to this point in the SC_Tutorial to add the LED user code.

5.7 LED Code Integration

In the Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c'. Add the following declaration to the above the 'main' function:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config_S12AD0.h"
#include "r_rsk_switch.h"
#include "r_rsk_debug.h"
#include "Config_SCI11.h"
#include "rskrx72tdef.h"

/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;

/* Prototype declaration for cb_switch_press */
static void cb_switch_press (void);

/* Prototype declaration for get_adc */
static uint16_t get_adc(void);

/* Prototype declaration for lcd_display_adc */
static void lcd_display_adc (const uint16_t adc_result);

/* Prototype declaration for uart_display_adc */
static void uart_display_adc(const uint8_t gs_adc_count, const uint16_t adc_result);

/* Variable to store the A/D conversion count for user display */
static uint8_t gs_adc_count = 0;

/* Prototype declaration for led display count */
static void led_display_count(const uint8_t count);
```

Add the following highlighted code to the main function:

```
void main(void)
{
    /* Initialize the switch module */
    R_SWITCH_Init();

    /* Set the call back function when SW1 or SW2 is pressed */
    R_SWITCH_SetPressCallback(cb_switch_press);

    /* Initialize the debug LCD */
    R_LCD_Init();

    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *) " RSKRX72T ");
    R_LCD_Display(1, (uint8_t *) " Tutorial ");
    R_LCD_Display(2, (uint8_t *) " Press Any Switch ");

    /* Start the A/D converter */
    R_Config_S12AD0_Start();
```

```

/* Set up SCI11 receive buffer and callback function */
R_Config_SCI11_Serial_Receive((uint8_t *)&g_rx_char, 1);

/* Enable SCI11 operations */
R_Config_SCI11_Start();

while (1U)
{
    uint16_t adc_result;

    /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
    if (TRUE == g_adc_trigger)
    {
        /* Call the function to perform an A/D conversion */
        adc_result = get_adc();

        /* Display the result on the LCD */
        lcd_display_adc(adc_result);

        /* Increment the gs_adc_count and display using the LEDs */
        if (16 == (++gs_adc_count))
        {
            gs_adc_count = 0;
        }
        led_display_count(gs_adc_count);

        /* Send the result to the UART */
        uart_display_adc(gs_adc_count, adc_result);
        /* Reset the flag */
        g_adc_trigger = FALSE;
    }
    /* SW3 is directly wired into the ADTRG0n pin so will
       cause the interrupt to fire */
    else if (TRUE == g_adc_complete)
    {
        /* Get the result of the A/D conversion */
        R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);

        /* Display the result on the LCD */
        lcd_display_adc(adc_result);

        /* Increment the gs_adc_count and display using the LEDs */
        if (16 == (++gs_adc_count))
        {
            gs_adc_count = 0;
        }
        led_display_count(gs_adc_count);

        /* Send the result to the UART */
        uart_display_adc(gs_adc_count, adc_result);
        /* Reset the flag */
        g_adc_complete = FALSE;
    }
    else
    {
        /* do nothing */
    }
}
}

```

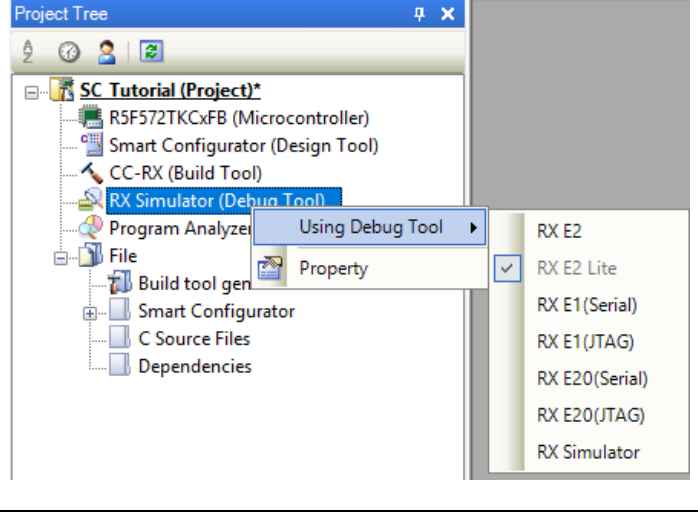
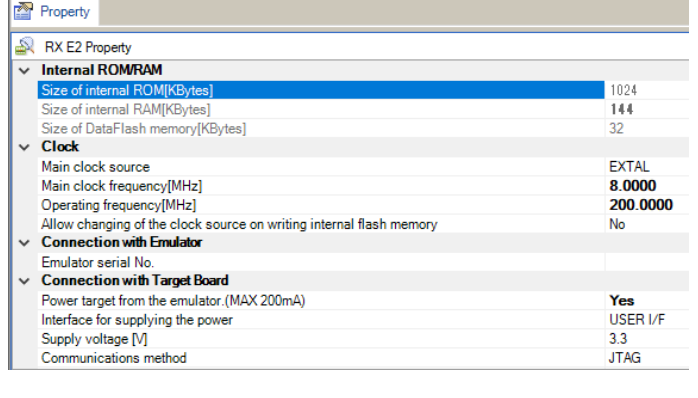
Then, add the following function definition in the user code area at the end of the file:

```
/* *****  
 * Function Name : led_display_count  
 * Description   : Converts count to binary and displays on 4 LEDs0-3  
 * Argument      : uint8_t count  
 * Return value  : none  
 * *****  
static void led_display_count (const uint8_t count)  
{  
    /* Set LEDs according to lower nibble of count parameter */  
    LED0 = (uint8_t)((count & 0x01) ? LED_ON : LED_OFF);  
    LED1 = (uint8_t)((count & 0x02) ? LED_ON : LED_OFF);  
    LED2 = (uint8_t)((count & 0x04) ? LED_ON : LED_OFF);  
    LED3 = (uint8_t)((count & 0x08) ? LED_ON : LED_OFF);  
}  
/* *****  
 * End of function led_display_count  
 * ***** */
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The code will perform the same but now the LEDs will display the `gs_adc_count` in binary form.

6. Debugging the Project

In the 'Project Tree' pane, right-click the 'RX Simulator (Debug Tool)'. Select 'Using Debug Tool -> RX E2 Lite'.	
- Double-click 'RX E2 Lite (Debug Tool)' to display the debugger tool properties. Under 'Clock', change the main clock frequency to 8 MHz, Communications method 'JTAG' and operating frequency to 200MHz.' - Under 'Connection with Target Board', change 'Power target from the emulator. (MAX 200mA)' to 'Yes'. - All other settings can remain at their defaults.	
- Connect the E2 Lite to the PC and the RSK E1/E2 Lite connector. Connect the Pmod LCD to the PMOD1 connector. - From the 'Debug' menu select 'Download' to start the debug session and download code to the target.	

7. Running the Smart Configurator Tutorial

7.1 Running the Tutorial

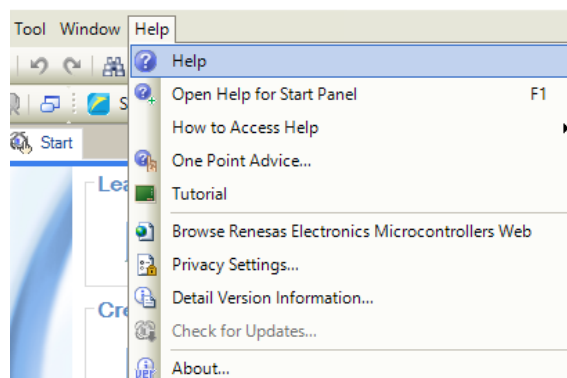
Once the program has been downloaded onto the RSK device, the program can be executed. Click the 'Go' button or press F5 to begin the program from the current program counter position. It is recommended that you run through the program once first, and then continue to the Tutorial manual to review the code.



8. Additional Information

Technical Support

For details on how to use CS+, refer to the help file by opening CS+, then selecting Help > Help Contents from the menu bar.



For information about the RX72T group microcontroller refer to the RX72T Group Hardware Manual.

For information about the RX assembly language, refer to the RX Family Software Manual.

Technical Contact Details

Please refer to the contact details listed in section 8 of the “Quick Start Guide”.

General information on Renesas microcontrollers can be found on the Renesas website at:
<https://www.renesas.com/>

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REVISION HISTORY	RX72T Group Renesas Starter Kit for RX72T Smart Configurator Tutorial Manual For CS+
------------------	--

Rev.	Date	Description	
		Page	Summary
1.00	Nov 30, 2018	—	First Edition issued

RX72T Group
Renesas Starter Kit for RX72T
Manual: Smart Configurator Tutorial Manual For CS+

Publication Date: Rev. 1.00 Nov 30, 2018

Published by: Renesas Electronics Corporation



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