

RX24U Group

Renesas Starter Kit Code Generator Tutorial Manual For CS+

RENESAS 32-Bit MCU RX Family / RX200 Series

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The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures;

- ensure attached cables do not lie across the equipment
- reorient the receiving antenna
- increase the distance between the equipment and the receiver
- connect the equipment into an outlet on a circuit different from that which the receiver is connected
- power down the equipment when not in use
- consult the dealer or an experienced radio/TV technician for help NOTE: It is recommended that wherever
 possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken;

- The user is advised that mobile phones should not be used within 10m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Renesas Starter Kit does not represent an ideal reference design for an end product and does not fulfil the regulatory standards for an end product.

How to Use This Manual

1. Purpose and Target Readers

This manual is designed to provide the user with an understanding of how to use Application Leading Tool (Code Generator) for RX together with the CS+ IDE to create a working project for the RSK platform. It is intended for users designing sample code on the RSK platform, using the many different incorporated peripheral devices.

The manual comprises of step-by-step instructions to generate code and import it into CS+, but does not intend to be a complete guide to software development on the RSK platform. Further details regarding operating the RX24U microcontroller may be found in the Hardware Manual and within the provided sample code.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the RX24U Group. Make sure to refer to the latest versions of these documents. The newest versions of the documents listed may be obtained from the Renesas Electronics Web site.

Document Type	Description	Document Title	Document No.
User's Manual	Describes the technical details of the RSK hardware.	RSKRX24U User's Manual	R20UT3758EG
Tutorial Manual	Provides a guide to setting up RSK environment, running sample code and debugging programs.	RSKRX24U Tutorial Manual	R20UT3759EG
Quick Start Guide	Provides simple instructions to setup the RSK and run the first sample.	RSKRX24U Quick Start Guide	R20UT3760EG
Code Generator Tutorial Manual	Provides a guide to code generation and importing into the CS+ IDE.	RSKRX24U Code Generator Tutorial Manual	R20UT3761EG
Schematics	Full detail circuit schematics of the RSK.	RSKRX24U Schematics	R20UT3757EG
Hardware Manual	Provides technical details of the RX24U microcontroller.	RX24U Group Hardware Manual	R01UH0658EJ

2. List of Abbreviations and Acronyms

Abbreviation	Full Form
ADC	Analog-to-Digital Converter
API	Application Programming Interface
bps	bits per second
CMT	Compare Match Timer
COM	COMmunications port referring to PC serial port
CPU	Central Processing Unit
DVD	Digital Versatile Disc
E1/E2 Lite	Renesas On-chip Debugging Emulator
GUI	Graphical User Interface
IDE	Integrated Development Environment
IRQ	Interrupt Request
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LSB	Least Significant Bit
LVD	Low Voltage Detect
MCU	Micro-controller Unit
MSB	Most Significant Bit
PC	Personal Computer
PLL	Phase-locked Loop
Pmod™	This is a Digilent Pmod [™] Compatible connector. Pmod [™] is registered to <u>Digilent Inc.</u> Digilent-Pmod_Interface_Specification
RAM	Random Access Memory
ROM	Read Only Memory
RSK	Renesas Starter Kit
RTC	Real Time Clock
SAU	Serial Array Unit
SCI	Serial Communications Interface
SPI	Serial Peripheral Interface
TAU	Timer Array Unit
TFT	Thin Film Transistor
TPU	Timer Pulse Unit
UART	Universal Asynchronous Receiver/Transmitter
USB	Universal Serial Bus
WDT	Watchdog Timer

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RENESAS

RSKRX24U

RENESAS STARTER KIT

1.1 Purpose

This RSK is an evaluation tool for Renesas microcontrollers. This manual describes how to use the CS+ IDE code generator plug in to create a working project for the RSK platform.

1.2 Features

This RSK provides an evaluation of the following features:

- Project Creation with CS+
- Code Generation using the code generator plug in.
- User circuitry such as switches, LEDs and a potentiometer

The RSK board contains all the circuitry required for microcontroller operation.



2. Introduction

This manual is designed to answer, in tutorial form, how to use the code generator plug in for the RX family together with the CS+ IDE to create a working project for the RSK platform. The tutorials help explain the following:

- Project generation using the CS+
- Detailed use of the code generator plug in for CS+
- Integration with custom code
- Building the project CS+

The project generator will create a tutorial project with three selectable build configurations:

- 'DefaultBuild' is a project with debug support and optimisation level set to two.
- 'Debug' is a project built with the debugger support included. Optimisation is set to zero.
- 'Release' is a project with optimised compile options (level two) and no outputs debugging information options selected, producing code suitable for release in a product.

The tutorial examples in this manual assume that installation procedures described in the RSK Quick Start Guide have been completed. Please refer to the Quick Start Guide for details of preparing the configuration.

These tutorials are designed to show you how to use the RSK and are not intended as a comprehensive introduction to the CS+ debugger, compiler toolchains or the E2 emulator Lite. Please refer to the relevant user manuals for more indepth information.



3. Project Creation with CS+

3.1 Introduction

In this section the user will be guided through the steps required to create a new C project for the RX24U MCU, ready to generate peripheral driver code using Code Generator. This project generation step is necessary to create the MCU-specific project and debug files.

3.2 Creating the Project

To use the program, start CS+: Windows[™] 7 & Vista: Start Menu > All Programs > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)

Windows™ 8.1 & 8: From Apps View , click 'CS+ for CC (RL78,RX,RH850)' icon

Windows[™] 10: Start Menu > All Apps > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)

•	CS+ will show the Start Page. Use the 'GO' button to Create a New Project.	Create New Project A new project can be created. A new project can also be created by reusing the file configuration registered to an existing project.	
•	In the 'Create Project' dialog, select 'RX' from the 'Microcontroller' pull- down.	Create Project Microcontroller: RX Using microcontroller:	
•	In the 'Using Microcontroller' list control, scroll down to 'RX24U' and expand the tree control by clicking '+'. Select 'R5F524UEAxFB(144pin)'.	Image: Search microcontroller) Update Image: SF524UBAxFB(144pin) Image: SF524UBAxFB(144pin) Image: SF524UBAxFB(144pin) Image: SF524UBAxFB(144pin) Image: SF524UCAxFB(144pin) Image: SF524UCAxFB(144pin)	
•	Ensure that in the 'Kind of project' pull- down, 'Empty Application(CC-RX)' is selected.	Kind of project:	
•	Choose an appropriate name and location for the project, then click 'Create'.	Project game: CG_Tutonal Place: C:\Workspace Bgowse Ø Make the project folder	
	Note: this tutorial assumes the project is named and located at the place shown opposite.	C:\Workspace\CG_Tutorial\CG_Tutorial.mtpj Pags the file composition of an existing project to the new project Project to be passed: (Input project file to be diverted.)	
•	If the folder entered cannot be found a 'Question' dialogue will be displayed; click 'Yes'.	Cgpy composition files in the diverted project folder to a new project folder.	
•	CS+ will create the blank project with the standard project tree. A 'Code Generator' node may also be shown, if previously enabled. Enable Code Generator can be seen in section 4.2.	CG_Tutorial - CS+ for CC - [Property] File Edit View Project Build Debug Tool Window Help Start Image: Start Solution List Image: Start Solution List Image: Start Project Tree Image: Start Solution List Image: Start<	



4. Code Generation Using the CS+ plug in

4.1 Introduction

Code Generator is a Windows[™] GUI tool for generating template 'C' source code and project settings for the RX24U. When using Code Generator, the user is able to configure various MCU features and operating parameters using intuitive GUI controls, thereby bypassing the need in most cases to refer to sections of the Hardware Manual.

By following the steps detailed in this tutorial, the user will generate a CS+ project called CG_Tutorial. A fully completed Tutorial project is contained on the DVD and may be imported into CS+ by following the steps in the Quick Start Guide. This tutorial is intended as a learning exercise for users who wish to use the Code Generator to generate their own custom projects for CS+.

Once the user has configured the project, the 'Generate Code' function is used to generate three code modules for each specific MCU feature selected. These code modules are name 'r_cg_xxx.h', 'r_cg_xxx.c', and 'r_cg_xxx_user.c', where 'xxx' is a three letter acronym for the relevant MCU feature, for example 'adc'. Within these code modules, the user is then free to add custom code to meet their specific requirement. Custom code should be added, whenever possible, in between the following comment delimiters:

```
/* Start user code for adding. Do not edit comment generated here */ /* End user code. Do not edit comment generated here */
```

Code Generator will locate these comment delimiters, and preserve any custom code inside the delimiters on subsequent code generation operations. This is useful if, after adding custom code, the user needs to re-visit Code Generator to change any MCU operating parameters.

The CG_Tutorial project uses interrupts for switch inputs, the ADC module, the Compare Match Timer (CMT), the Serial Communications Interface (SCI) and uses these modules to perform A/D conversion and display the results via the Virtual COM port to a terminal program and also on the LCD display on the RSK.

Following a tour of the key user interface features of Code Generator in §4.3, the reader is guided through each of the peripheral function configuration dialogs in §4.4. In §5, the reader is familiarised with the structure of the template code, as well as how to add their own code to the user code areas provided by the code generator.

4.2 Enabling Code Generator

After installation of CS+, Code Generator must be enabled. This step is only required once, CS+ will remember this setting on subsequent launches.

From the 'Tool' pull-down menu select 'Plug-in Setting...'. On the 'Additional Function' tab, click the box next to the 'Code Generator/Pin View Plug-in' option and ensure it is ticked:

1	Basic Function Publicitian Function				
	Module Name	Description			
	Code Generator/PinView Plug-in	Plug in to generate the device driver automatically and to view the device configuration. for RX,			
	🔲 辥 Debug Console Plug-in	DebugConsole plug-in to support using standard I/O.			

Click 'OK'. CS+ needs to restart to enable this selection, select 'Yes' from the Question dialogue box. After restarting, 'Code Generator (Design Tool)' node will now be shown in the left-hand 'Project Tree' window pane.





4.3 Code Generator Tour

This section presents a brief tour of Code Generator. For further details of the Code Generator paradigm and reference, refer to the Application Leading Tool Common Operations manual.

You can download the latest document from: https://www.renesas.com/applilet

Application Leading Tool is the stand-alone version of Code Generator and this manual is applicable to the Code Generator.

In the Project Tree pane, click on the 🗄 icon next to 'Code Generator' node to expand the list.

Expand the 'Peripheral Functions' node by clicking on the 🖽 next to it.

Open the 'Peripheral Function' tab by double clicking on the 'Peripheral Functions' name.

The CS+ main window will now contain a 'Peripheral Functions' tab with the Initial View as show in Figure 4-1.

CG_Tutorial - CS+ for CC - [Peripheral Function	ns]						
<u>File Edit View Project Build Debug Tool W</u>	<u>/i</u> ndow <u>H</u> elp						6
※ 19 18 19 19 18 単単	🍟 😽 🚮 DefaultBuild	• 🔬 🖓 🖏 🐂 🔘) (d) (d) (d) (d) (d) (d) (d) (d) (d) (d	a ça 🖕 👗 🥘 s	Start 🔒 🛃 🎒		Solution List
Project Tree # X	Property 💯 Peripheral Functions	.			· ·		
2 3 2 2		1000					
G Tutorial (Project)	🐻 Generate Code 🚣 📋 🚜 🖄 🗰	# 🍜 🛍 🛞 🐼 🤇	🕽 🖏 සි ඊ 📲 එ	7 🏭 🍇 🐟 🕖 😂			
R5F524UEAxFB (Microcontroller)	Clock setting Block diagram						*
Code Generator (Design Tool)	- Main clock oscillator setting						
🖶 🎤 Pin View	Operation						
Peripheral Functions Clock Generator	Main clock oscillation source	Resonator	•				
Original Clock Generatory Original Structure Generatory Original Structure Generatory	Frequency	8		(MHz)			E
Clock Frequency Accuracy Mea	Oscillator wait time	8192 cycles 👻	2048	(µs)			
	Oscillation stop detection function	Disabled	•				
📦 Buses	- High speed clock oscillator (HOCO) setting						
Data Transfer Controller	Operation						
ia ♥ I/O Ports	Frequency	32 MHz	×				
Port Output Enable 3	Oscillator wait time	142	(cycles)				
⊕ € General PWM Timer	Oscillator wait time	142	(cycles)				
🗓 🐨 8-Bit Timer	- PLL circuit setting						
😥 🐨 Compare Match Timer	Operation						
	PLL clock source	Main clock oscillator					
Serial Communications Interfac	Input frequency division ratio	x1 👻					
CRC Calculator	Frequency multiplication factor	x 8 👻					
12-Bit A/D Converter	Frequency	64	(MHz)				
D/A Converter	1						*
🖶 🐨 Comparator C	Output						д X
Data Operation Circuit	[EOF]						
🛓 📓 Code Preview							
File							
	All Messages						•
< III +	Output & Smart Browser 🔚 Error List						
F1 F2 F3		FB F7	FB	F9	FIII Full-screen	FN	FI2
			1	101	1	11	

Figure 4-1 Initial View

Code Generator provides GUI features for configuration of MCU subsystems and peripherals. Once the user has configured all required MCU subsystems and peripherals, the user can click the 'Generate Code' button, resulting in a fully configured CS+ project.

Navigation to the MCU peripheral configuration screens may be performed by double-clicking the required function in the Project Tree -> Project Name -> Peripheral Function on the left.

It is also possible to see a preview of the code that will be generated for the current peripheral function settings by double-clicking the required function in the Project Tree -> Project Name -> Code Preview on the left.



4.4 Code Generation

In the following sub-sections, the reader is guided through the steps to configure the MCU for a simple project containing interrupts for switch inputs, timers, ADC and a SCI.

4.4.1 Clock Generator

Figure 4-2 shows a screenshot of Code Generator with the Clock Generator function open. Click on the 'Clock setting' sub tab. Configure the system clocks as shown in the figure. In this tutorial we are using the on board 20 MHz crystal resonator for our main clock oscillation source and the PLL circuit is in operation. The PLL output is used as the main system clock and the divisors should be set as shown in **Figure 4-2**.

Property 💯 Peripheral Functions*		
🐻 Generate Code 🚣 📋 🚜 🛃 👹	占 💷 🙆 🖉 🧔 🖉 🖉 📲 ሪን 🛙	🖏 🔩 🧄 💱
Clock setting Block diagram		
-Main clock oscillator setting		
Operation		
Main clock oscillation source	Resonator -	
Frequency	20	(MHz)
Oscillator wait time	8192 cycles - 2048	<mark>(µs)</mark>
Oscillation stop detection function	Disabled 🗸]
- High speed clock oscillator (HOCO) setting		
Operation		
Frequency	32 MHz]
Oscillator wait time	142 (cycles)	
- PLL circuit setting		
Operation		
PLL clock source	Main clock oscillator -	
Input frequency division ratio	x 1/2 👻	
Frequency multiplication factor	x 8 🗸	
Frequency	80 (MHz)	
- Low speed clock oscillator (LOCO) setting		
Operation		
Frequency	4	(MHz)
-System clock setting		
Clock source	PLL circuit -]
System clock (ICLK)	x 1 🔹 80	(MHz)
Peripheral module clock (PCLKA)	x 1 🔹 80	(MHz)
Peripheral module clock (PCLKB)	x 1/2 🔻 40	(MHz)
Peripheral module clock (PCLKD)	x 1/2 🔻 40	(MHz)
Flash IF clock (FCLK)	x 1/4 👻 20	(MHz)
- IWDT-dedicated low-speed clock oscillator (IWDTLO	CO) setting	
Operation		
Frequency	15	(kHz)

Figure 4-2 Clock setting tab

Proceed to the next section on 'Interrupt Controller Unit'. Double clicking on the 'Interrupt Controller Unit' name in 'Peripheral functions' on the Project Tree.





4.4.2 Interrupt Controller Unit

Referring to the RSK schematic, SW1 is connected to IRQ0 (P10) and SW2 is connected to IRQ4 (P60). SW3 is connected directly to the ADTRG0n and will be configured later in §4.4.4. Navigate to the 'Interrupt Controller Unit' node in Code Generator and in the 'General' tab, configure these two interrupts as falling edge triggered as shown in **Figure 4-4** below.

Property 🧏 Peripheral F	unctions*					
🐻 Generate Code 🚣 🕻	3 🖉 ビ 🖉 🗿	; 💷 🙆 🖉 🖉 🍓	3 7 🖷 7	捻 👫 🧄 💈	3	
- Fast interrupt setting						
Fast interrupt	Interrupt source	BSC (BUSERR vect=16)		-		
- Software interrupt setting						
Software interrupt	Priority	Level 15 (highest)				
-NMI setting						
NMI pin interrupt	Valid edge	Falling -	Digital filter	No filter	▼ 0	(MHz)
- IRQ0 setting						
IRQ0	Pin	P10 -	Digital filter	No filter	▼ 0	(MHz)
	Valid edge	Falling 👻	Priority	Level 15 (highest)	•	
- IRQ1 setting						
IRQ1	Pin	P11 👻	Digital filter	No filter	- 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	•	
- IRQ2 setting						
IRQ2	Pin	P00 👻	Digital filter	No filter	- 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	T	
- IRQ3 setting						
IRQ3	Pin	PB4 👻	Digital filter	No filter	▼ 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	Y	
- IRQ4 setting						
✓ IRQ4	Pin	P60 👻	Digital filter	No filter	▼ 0	(MHz)
	Valid edge	Falling 👻	Priority	Level 15 (highest)	•	
- IRQ5 setting						
IRQ5	Pin	P02 -	Digital filter	No filter	- 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	-	
- IRQ6 setting						
IRQ6	Pin	P21 👻	Digital filter	No filter	- 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	T	
-IRQ7 setting						
IRQ7	Pin	P20 👻	Digital filter	No filter	- 0	(MHz)
	Valid edge	Low level 👻	Priority	Level 15 (highest)	-	
	F ¹		••••••••••••••••	4.4.1.		

Figure 4-4 Interrupt Functions tab



Proceed to the next section on 'Compare Match Timer'. Double clicking on the 'Compare Match Timer' name in 'Peripheral functions' on the Project Tree.



Figure 4-5 Select Compare Match Timer



4.4.3 **Compare Match Timer**

Navigate to the 'Compare Match Timer' node in Code Generator. CMT0 will be used as an interval timer for generation of accurate delays. CMT1 and CMT2 will be used as timers in de-bouncing of switch interrupts.

In the 'CMT0' sub-tab configure CMT0 as shown in Figure 4-6. This timer is configured to generate a high priority interrupt every 1ms. We will use this interrupt later in the tutorial to provide an API for generating high accuracy delays required in our application.

Property 🚆 Peripheral Functions*	
🔞 Generate Code 🚣 🔒 🚜 🛬 🖋 🗰 🍰 💷 🔞 🧔 🧔	? ም 🖷 ም 🛱 🏠 🔩 🐠 😕
CMT0 CMT1 CMT2 CMT3	
- Compare match timer operation setting	
Unused	
- Count clock setting	
● PCLK/8 ○ PCLK/32 ○ PCLK/128 ○	PCLK/512
- Interval value setting	
Interval value 1 m	s (Actual value: 1)
- Interrupt setting	
Enable compare match interrupt (CMI0)	
Priority Level 10	•

Figure 4-6 CMT0 tab

Navigate to the 'CMT1' sub-tab and configure CMT1 as shown in Figure 4-7. This timer is configured to generate a high priority interrupt after 20ms. This timer is used as our short switch de-bounce timer later in this tutorial.

Property 🕎 Peripheral Functions*
👸 Generate Code 🏂 🗋 🕉 🗠 📽 📽 🏭 🏭 🗐 🔅 🧔 🧐 🖉 🖉 📲 🎜 🕬 🍪 –
СМТО СМТ1 СМТ2 СМТ3
- Compare match timer operation setting
O Unused O Used
- Count clock setting
PCLK/8 PCLK/32 PCLK/128 PCLK/512
- Interval value setting
Interval value 20 ms - (Actual value: 20)
- Interrupt setting
Enable compare match interrupt (CMI1)
Priority Level 10
Figure 4-7 CMT1 tab

Figure 4-7 CM11 tab

Navigate to the 'CMT2' sub-tab and configure CMT2 as shown in Figure 4-8. This timer is configured to generate a high priority interrupt after 200ms. This timer is used as our long switch de-bounce timer later in this tutorial.

Property 💯 Peripheral Functions*	
🔞 Generate Code 🔬 🗋 🚜 🚩 💕 🗰 🍰 💷 🔞 🍭 🗳 🔗	" 📲 🍠 🏭 🕰 🔩 전
CMT0 CMT1 CMT2 CMT3	
- Compare match timer operation setting	
O Unused O Used	
- Count clock setting	
PCLK/8 PCLK/32 PCLK/128 PCLK/128	K/512
- Interval value setting	
Interval value 200 ms	✓ (Actual value: 200)
- Interrupt setting	
Enable compare match interrupt (CMI2)	
Priority Level 10	-
Figure 4-8 CMT2 tab	



Proceed to the next section on '12-Bit A/D Converter'. Double clicking on the '12-Bit A/D Converter' name in 'Peripheral functions' on the Project Tree.

Project Tree 📮 🗙
2 🕜 🤮 🗃
🚐 📲 Code Generator (Design Tool)
🗄 🖉 Pin View
🖨 💯 Peripheral Functions
🛓 🕤 Voltage Detection Circuit
Clock Frequency Accuracy Measurement Circuit
Low Power Consumption
Buses
Data Transfer Controller
i I/O Ports
Hulti-Function Timer Pulse Unit 3
🔒 💭 General PWM Timer
⊕
🗄 🥑 Compare Match Timer
🖶 💭 Serial Communications Interface
Serial Peripheral Interface
12-Bit A/D Converter
D/A Converter
🗄 🐨 Comparator C
Data Operation Circuit
🗈 🛒 Code Preview

Figure 4-9 Select 12-Bit A/D Converter



4.4.4 12-Bit A/D Converter

Navigate to the '12-Bit A/D Converter' tab in Code Generator. In the 'S12AD0' sub-tab configure S12AD0 as shown in **Figure 4-10**, **Figure 4-11** and configure the S12AD0 as shown. We will be using the S12AD0 in 12bit one shot mode on the AN000 input, which is connected to the RV1 potentiometer output on the RSK. The conversion start trigger will be via the pin connected to SW3.

Property 💯 Peripheral Fun	ctions*					
🐻 Generate Code 🔬 📋	a 🗠 💉 🕷	1 🏭 🎯 🤇	2008	ም 📲 ም 😂 🧌	i. 4k, √∭ 12≩	
<u>S12AD0</u> S12AD1 S12AD2						
- S12AD0 operation setting						
O Unused		Used				
- Operation mode setting			•			
Single scan mode		Group so	an mode		Continuous scan mode	
- Group scan select						
Two groups (A,B)		Three gr	oups (A,B,C)			
- Double trigger mode setting —						
Oisable		Enable				
- Self diagnosis setting						
Mode		Unused		-		
Voltage used		Use 0 V		T		
- Disconnection detection assist	setting					
Charge setting		Unused		•		
Period		2 ADCLK		v		
- Group scan priority setting						
Group priority		Group with	out priority		-	
Group action		Not restarte	ed or continued due	e to Group priority	*	
Restart channel selection		Restarted f	rom the first scan o	channel	v	
– A/D converted value count setti	ng					
Addition mode		Average	mode			
-Analog input channel setting —						
	Convert	Convert	Convert	Add/Average	Programmable gain	
	(Group A)	(Group B)	(Group C)	AD value	amplifier	
AN000 AN001						
AN001 AN002						
AN003						
AN016						
– Programmable gain amplifier se	etting					
Enable pass-through am	olifier AN000					
Amplifier gain selection		2.000		-		
		Figure 4-1	0 S12AD	0 tab (1)		



- Conversion start trigger setting				
Conversion start trigger (Group A)				
A/D conversion start trigger pin				-
Conversion start trigger (Group B)				
Compare match with or input capture to MTU0.TG	iRA			
Conversion start trigger (Group C)				
Compare match with or input capture to MTU1.TG	iRA			·
ADTRG0# pin selection	P20	•		
- Data registers setting				
AD converted value addition count	1-time conversion		*	
Data placement	Right-alignment	•		
Automatic clearing	Disable automatic clearing		-	
-AN000 / Self-diagnosis conversion time setting				
Input sampling time	3.667	(µs) (Actual value: 3.675)	
-AN001 conversion time setting				
Input sampling time	3.667	(µs) (Actual value: 3.675)	
- AN002 conversion time setting				
Input sampling time	3.667	(µs) (Actual value: 3.675)	
- AN003 conversion time setting				
Input sampling time	3.667	(µs) (Actual value: 3.675)	
- AN016 conversion time setting				
Input sampling time	3.667	(µs) (Actual value: 3.675)	
- Conversion time setting				
Total conversion time (Group A)	4.725	(µs)	
Total conversion time (Group B)		(µs		
Total conversion time (Group C)		(µs		
- Output setting				
ADST0 pin output enable	P02	Ŧ		
Interrupt setting Enable AD conversion end interrupt (S12ADI)				
Priority	Level 15 (highest)	•		
✓ Enable AD conversion end interrupt for group B (GBADI)			
Priority	Level 15 (highest)	-		
✓ Enable AD conversion end interrupt for group C (GCADI)			
Priority	Level 15 (highest)	-		

Figure 4-11 S12AD0 tab (2)



Proceed to the next section on 'Serial Communications Interface'. Double clicking on the 'Serial Communications Interface' name in 'Peripheral functions' on the Project Tree.



Figure 4-12 Select Serial Communications Interface



4.4.5 Serial Communications Interface

Navigate to the 'Serial Communications Interface' tab in Code Generator, select the SCI9 sub-tab and apply the settings shown in **Figure 4-13**. In the RSKRX24U SCI9 is used as an SPI master for the Pmod LCD on the PMOD1 connector as shown in the schematic.

Property 💯 Periphe	eral Functions*										
🐻 Generate Code 🛛	🛓 🗋 🚜 🖄	ov 🗰	dia 🕬	<u>ی</u> ک	00	8 7	* # 5	122 4	4 <u>1</u> 4	~D1	123 - V
SCI1 SCI5 SCI6	SCI8 <u>SCI9</u> S	CI11									
General setting Setting											
-Function setting											
O Unused											
Asynchronous m	iode		Trans	mission			-				
Asynchronous m	iode (Multi-proces	sor)	Trans	mission			-				
Clock synchrono	ous mode		Trans	mission			-				
Smart card interf	ace mode		Trans	mission			-				
Simple IIC bus	_										
Simple SPI bus			Maste	er transm	it only		•				
- Pin setting											
TXD9 PG1		-	RXD9	PO	i0		-				
SSDA9 PG1		-	SSCLS	PG	ìO		-				
SMOSI9 PG1		•	SMISC	9 PC	i0		-				

Figure 4-13 SCI9 General Setting tab

Select the SCI9 'Setting' sub-tab and configure the SPI Master as illustrated in **Figure 4-14**. Make sure the 'Transfer direction setting' is set to 'MSB-first' and the 'Bit rate' is set to 10000000. All other settings remain at their defaults.

Property 💯 Peripheral Functions*	
访 Generate Code 🏂 🗋 🚜 🚩 📽 🗱 轟 鄒 ⑫ ⑫ ⑫ ⑧ ℬ テ 嘴 テ 踸 💊 ♠ 例 注 SC11 SC15 SC16 SC18 SC19 SC111 General setting Setting	
- Transfer direction setting	
Data inversion setting Normal Normal	
- Transfer rate setting Transfer clock	
Bit rate to the second	r : 0%)
SCK9 pin function Clock output - -Clock setting	
Clock delay Clock is not delayed	
Enable clock polarity inversion Data handling setting	
Transmit data handling Data handled in interrupt service routine	
TXI9, TEI9 priority Level 15 (highest)	
-Callback function setting Image: Transmission end	

Figure 4-14 SCI9 Setting tab



Staying in the 'Serial Communications Interface' tab in Code Generator, select the SCI1 sub-tab and apply the settings shown in **Figure 4-15**. In the RSKRX24U SCI1 is connected via a Renesas RL78/G1C to provide a USB virtual COM port as shown in the schematic.

Property 💯 Peripheral Functions*	
🔞 Generate Code 🚣 🗋 🕷 🖄 🗰	s 📾 18 10, 19 19 18 7 🖷 7 18 💁 🔩 🕕 😣
SCI1 SCI5 SCI6 SCI8 SCI9 SCI11	
General setting Setting	
- Function setting	
Unused	
Asynchronous mode	Transmission/reception -
Asynchronous mode (Multi-processor)	Transmission
Clock synchronous mode	Transmission
Smart card interface mode	Transmission
Simple IIC bus	
Simple SPI bus	Slave transmit/receive 👻
- Pin setting	
TXD1 PD3 👻	RXD1 PD5 👻
SSDA1 PC4 👻	SSCL1 PC3 👻
SMOSI1 PC4 -	SMISO1 PC3 👻

Figure 4-15 SCI1 General Setting tab

Select the SCI1 'Setting' sub-tab and configure SCI1 as illustrated in **Figure 4-16**. Make sure the 'Start bit edge detection' is set as 'Falling edge on RXD1 pin' and the 'Bit rate' is set to 19200 bps. All other settings remain at their defaults.

Property 💯 Peripheral Functions*		
🐻 Generate Code 🛛 🚣 📋 🕷 🖄 🗰	1 🚠 💷 🙆 🖉 🖉 🖉 🥔	デ 幡 デ 環 痛 失 例 琴
SCI1 SCI5 SCI6 SCI8 SCI9 SCI11		
General setting Setting		
- Start bit edge detection setting		
Low level on RXD1 pin	Falling edge on RXD1 pin	
-Data length setting		
9 bits	8 bits	⊘ 7 bits
- Parity setting		
None	Even	Odd
- Stop bit length setting		
I bit	② 2 bits	
- Transfer direction setting		
ISB-first	MSB-first	
- Transfer rate setting		
Transfer clock	Internal clock	•
Base clock	16 cycles for 1-bit period	_
Bit rate	19200	 (bps) (Actual value: 19230.769, Error: 0.16%)
Enable modulation duty correction		
SCK1 pin function	SCK1 is not used	▼ P25 ▼
- Noise filter setting		
Enable noise filter		
Noise filter clock	Clock signal divided by 1	▼ 40000000 (Hz)
- Hardware flow control setting		
None	CTS	RTS
CTS1/RTS1 pin	P02	v
- Data handling setting		
Transmit data handling	Data handled in interrupt servi	ce routine 👻
Receive data handling	Data handled in interrupt servi	ce routine 👻
- Interrupt setting		
Enable error interrupt (ERI1)		
TXI1, RXI1, TEI1, ERI1 priority	Level 15 (highest)	•
- Callback function setting		
Transmission end	Reception end	Reception error

Figure 4-16 SCI1 Setting tab



Proceed to the next section on 'I/O Ports'. Double clicking on the 'I/O Ports' name in 'Peripheral functions' on the Project Tree.



Figure 4-17 Select I/O Ports



4.4.6 I/O Ports

Referring to the RSK schematic, LED0 is connected to P21, LED1 is connected to P22, LED2 is connected to PC3 and LED3 is connected to PC4. Navigate to the 'I/O Ports' tab in Code Generator and configure these four I/O lines as shown in **Figure 4-18** and **Figure 4-19** below. Ensure that the 'Output 1' tick box is checked. This ensures that the code is generated to set LEDs initially off.

Property 🧏 F	Peripheral Fu	unctions*					
🐻 Generate Cod	le 🚣 🕻) 🚜 🖄 💕	🗰 🍰 📬 (800.08	J 📲 J 🛱	👊 🦶 🕖 🤤	
Port0 Port1 Port1	ort2 Port3	Port4 Port5	Port6 Port7	Port8 Port9 PortA	PortB PortC	PortD PortE Po	rtF PortG
O Unused P21	🔘 In 🥊	🔘 Out 😲	Pull-up	CMOS output	-	Output 1	High-drive output
O Unused	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output
O Unused	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output
Unused P24	⊚ In	Out	Pull-up	CMOS output		Output 1	High-drive output
Unused P25	⊚ In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
Onused - P26	⊚ In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
Unused P27	⊚ In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
Unused	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output

Figure 4-18 I/O ports – Port2

Property 🙎	Peripheral Fu	unctions*					
🐻 Generate	Code 🚣 📋) 🚜 🖄 💕	🗰 🍰 🐲 (3 4 4 4 8	ፖ 📲 🍠 👪	🔬 🦶 🕖 💈	3
Port0 Port1	Port2 Port3	Port4 Port5	Port6 Port7	Port8 Port9 PortA	PortB PortC	PortD PortE P	ortF PortG
O Unused PC1	🔘 In	⊚ Out	Pull-up	CMOS output	•	Output 1	High-drive output
O Unused PC2	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output
O Unused PC3	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
O Unused	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output
O Unused	🔘 In	Out	Pull-up	CMOS output	•	✓ Output 1	High-drive output
O Unused PC6	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
 PC6 — Unused 	🔘 In	⊚ Out	Pull-up	CMOS output	•	Output 1	High-drive output

Figure 4-19 I/O ports – PortC

P27 is used as one of the LCD control lines, together with P34, P55 and P65. Configure these lines as shown in **Figure 4-20**, **Figure 4-21**, **Figure 4-22** and **Figure 4-23**.

Property 💯 F	Peripheral Fun	ctions*					
				ð 🥸 🔕 🕲 <i>B 7</i>	📲 🍠 🎎	👊 🦶 🕖 🤤	
Port0 Port1 Port1	ort2 Port3	Port4 Port5	Port6 Port7	Port8 Port9 PortA Po	ortB PortC F	PortD PortE Po	ortF PortG
 Unused 	🔘 In 😲	🔘 Out 😲	Pull-up	CMOS output	-	Output 1	High-drive output
- P21	⊚ In	Out	Pull-up	CMOS output	•	V Output 1	High-drive output
- P22	🔘 In	Out	Pull-up	CMOS output	•	Votput 1	High-drive output
Unused P24	🔘 In	Out	Pull-up	CMOS output	~	Output 1	High-drive output
O Unused - P25	🔘 In	Out	Pull-up	CMOS output	Ŧ	Output 1	High-drive output
Our Unused - P26	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
Unused P27	🔘 In	Out	Pull-up	CMOS output	~	Output 1	High-drive output
O Unused	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output

Figure 4-20 I/O ports – Port2

Property 🙎	Peripheral Fu	nctions*					
🐻 Generate	Code 🔬 🚊	a 🖄 🖉	🗰 🏭 🚛 (10 10 10 10 10 10 10 10 10 10 10 10 10 1	ቻ 📲 🎜 🖧	🕰 🧄 🕖 💱	ļ.
Port0 Port1	Port2 Port3	Port4 Port5	Port6 Port7	Port8 Port9 PortA	PortB PortC	PortD PortE P	ortF PortG
	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
 P31 Unused P32 	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
Unused - P33	🔘 In	Out	Pull-up	CMOS output	v	Output 1	High-drive output
 Unused P34 	⊚ In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
O Unused - P35	🔘 In	Out	Pull-up	CMOS output	•	Output 1	High-drive output
O Unused - P36	🔘 In	Out	Pull-up	CMOS output	-	Output 1	High-drive output
O Unused - P37	🔊 In 👎	🔘 Out 👎	Pull-up	CMOS output	-	Output 1	
 Unused 	🔘 ln 😲	🔘 Out !	Pull-up	CMOS output		Output 1	

Figure 4-21 I/O ports – Port3

4. Code Generation Using the CS+ plug in

RSKRX24U

Property 🧏 P	Peripheral Fu	inctions*		
🐻 Generate Cod	le 🚣 📋	i 🚜 🖄 💕	🛢 🏯 💷 🚳 🖗	. 🧐 🕲 🖉 🦏 🍠 🖧 🐔 🧄 🖓
Port0 Port1 Po - P50	rt2 Port3	Port4 Port5	Port6 Port7 Port8	Port9 PortA PortB PortC PortD PortE PortF PortG
 Unused P51 		Out	Pull-up	Output 1
 Unused P52 		Out	🗌 Pull-up	Output 1
 Unused P53 		Out	Pull-up	Output 1
 Unused P54 		Out	Pull-up	Output 1
 Unused P55	⊚ In	Out	Pull-up	Output 1
O Unused	🔘 In	Out	Pull-up	☑ Output 1
			Figure 4-22 I/O p	oorts – Port5
Property 💯 P	eripheral Fu	nctions*		
🐻 Generate Code	e 🚣 📋	a 🔛 🖉	📕 🚠 💷 🛞 🔇	😃 🕼 a? 📲 a? 🕮 🕰 🔩 🧄 💐
Port0 Port1 Por P60	rt2 Port3	Port4 Port5	Port6 Port7 Port8	Port9 PortA PortB PortC PortD PortE PortF PortG
 Unused P61 	🔊 In 🚺	🔘 Out 👎	Pull-up	Output 1
O Unused P62	🔘 In	⊚ Out	Pull-up	Output 1
 Unused P63 		⊚ Out	Pull-up	Output 1
 Unused P64 		Out	Pull-up	Output 1
 Our Output Output Output<th>⊚ In</th><th>Out</th><th>Pull-up</th><th>Output 1</th>	⊚ In	Out	Pull-up	Output 1
O Unused	🔘 In	Out	Pull-up	Output 1
			Figure 4-23 I/O p	oorts - Port6

Figure 4-23 I/O ports – Port6



Peripheral function configuration is now complete. Save the project using the File -> Save Project menu item, then click 'S Generate Code'. The Output pane should report 'The operation of generating file was successful', as shown **Figure 4-24** below.

Output	# ×
===== Start generate code (2016/12/21 11:57:28) ======	*
M0409002:The generating source folder is: C:\Workspace\CG_Tutorial\4	
M0409001:The following files were generated:	
M0409000:cg_src\r_cg_main.c was generated.d	
M0409000:cg_src\r_cg_dbsct.c was generated.	
M0409000:cg_src\r_cg_intprg.c was generated.d	
M0409000:cg_src\r_cg_resetprg.c was generated.	
M0409000:cg_src\r_cg_sbrk.c was generated.	
M0409000:cg_src\r_cg_vecttbl.c was generated.	
M0409000:cg_src\r_cg_sbrk.h was generated.	
M0409000:cg_src\r_cg_stacksct.h was generated.	
M0409000:cg_src\r_cg_vect.h was generated.	
M0409000:cg_src\r_cg_hardware_setup.c was generated.	
M0409000:cg_src\r_cg_macrodriver.h was generated.	-
M0409000:cg_src\r_cg_userdefine.h was generated.d	-
M0409000:cg_src\r_cg_cgc.c was generated.d	
M0409000:cg_src\r_cg_cgc_user.c was generated.	
M0409000:cg_src\r_cg_cgc.h was generated.d	
M0409000:cg_src\r_cg_icu.c was generated.	
M0409000:cg_src\r_cg_icu_user.c was generated.	
M0409000:cg_src\r_cg_icu.h was generated.	
M0409000:cg_src\r_cg_port.c was generated.	
M0409000:cg_src\r_cg_port_user.c was generated.d	
M0409000:cg_src\r_cg_port.h was generated.	
M0409000:cg_src\r_cg_cmt.c was generated.	
M0409000:cg_src\r_cg_cmt_user.c was generated.	
M0409000:cg_src\r_cg_cmt.h was generated.d	
M0409000:cg_src\r_cg_sci.c was generated.4	
M0409000:cg_src\r_cg_sci_user.c was generated.	
M0409000:cg_src\r_cg_sci.h was generated.d	
M0409000:cg_src\r_cg_s12ad.c was generated.u	
M0409000:cg_src\r_cg_s12ad_user.c was generated.d	
M0409000:cg_src\r_cg_s12ad.h was generated.d	
M0409003:The operation of generating file was successful.	
===== Generate code ended (2016/12/21 11:57:29) =====	
[EOF]	-
All Messages / Code Generator / "Rapid Build /	-

Figure 4-24 Code generator console

Figure 4-25 shows the Code Generator Files in the Project Tree pane. In the next section the CG_Tutorial project will be completed by adding user code into these files and adding new source files to the project.

Project Tree				
2 0 2 2				
□ <mark></mark>				
R5F524UEAxFB (Microcontroller)				
🖶 📲 Code Generator (Design Tool)				
RX Simulator (Debug Tool)				
- J File				
🖕 🛄 Code Generator				
r_cg_icu_user.c				
r_cg_port.c				
r_cg_port_user.c				
r_cg_cmt.c				
r_cg_sbrk.h				
r_cg_userdefine.h				
r_cg_pointh				
r_cq_sci.h				
r_cg_s12ad.h				

Figure 4-25 Code Generator Files in the Project Tree



5. Completing the Tutorial Project

5.1 Project Settings

•	In the 'Project Tree' pane, select	Property 🚰 Peripheral Functions*		
	'CC-RX (Build Tool)'. The build	CC-RX Property Build Mode		
	properties will appear in the main	Build mode	DefaultBuild	
		Change property value for all build modes at once CPU		
	window.	Instruction set architecture Uses floating-point operation instructions	RXv2 architecture(isa=rxv2) Yes(fpu)	
•	CS+ creates a single build	Endian type for data Rounding method for floating-point constant operations	Little-endian data(-endian=little) round to nearest(-round=nearest)	
	configuration called 'Default Build'	Handling of denormalized numbers in floating-point constants	Handles as zeros(-denomalize=off)	
		Precision of the double type and long double type Replaces the int type with the short type	Handles in single precision(-dbl_size=4) No	
	for the project. This has standard	Sign of the char type Sign of the bit-field type	Handles as unsigned char(-unsigned_char) Handles as unsigned(-unsigned_bitfield)	
	code optimisation turned on by	Selects the enumeration type size automatically Order of hit-field members	No Allocates from right(-bit_order=right)	
	default.	Assumes the boundary alignment value for structure members is 1	No(-unpack)	
		Enables C++ exceptional handling function (try, catch and throw) Enables the C++ exceptional handling function (dynamic_cast and typeid)	No(-noexception) No(-tti=off)	
		General registers used only in fast interrupt functions Branch width size	None(fint_register=0) Compiles within 24 bits(-branch=24)	
		Base register for ROM	None	
		Base register for RAM Address value of base register that sets the address value	None MEX 0000000	
		Register of base register that sets the address value Avoids a problem specific to the CPU type	None	
		Saves and restores ACC using the interrupt function	No	
		 PIC/PID Output File Type and Path 		
		Output file type Intermediate file output folder	Execute Module(Load Module File) %BuildModeName%	
		Frequently Used Options(for Compile) A Glitional include paths		
		System include paths	Additional include paths[1] System include paths[0]	
		 Macro definition Outputs debugging information 	Macro definition[0] Yes(-debug)	
		Optimization level	2(-optimize=2)	
•	Select the 'Compile Options' tab at	CC-RX Property		
	the bottom of the properties window	▲ Source	0/200//1	
	pane. Under 'Language of the C	Language of the C source file Language of the C++ source file	C(C89)(-lang=c) C(C89)(-lang=c)	
		 Additional include paths 	C99(lang=c99)	
	source file' select 'C99(-lang=c99)'	System include paths	System include p	
	as shown opposite.	Include files at the head of compiling units		ne head of compiling units[0]
	••	 Macro definition Invalidates the predefined macro 	Macro definition	[0]
		Enables information-level message output	No(-nomessage)	
		Suppresses the number of information-level messages		
		Changes the warning-level messages to information-level Changes the information-level messages to warning-level		
		Changes the information-level and warning-level messages		
		Permits comment (/* */) nesting	No(-comment=no	onest)
-	Soloot the 'Link Ontions' tab at the	Optimization		
•	Select the 'Link Options' tab at the	Optimization type Section	No optimize(-NOOPtimize)	
	bottom of the properties window	Section start address The specified section that outputs externally defined symbols to the file	B_1.R_1.B_2.R_2.B.R.SU,SI/04,PResetPRG/0FFF80	
	pane. Under 'Section -> ROM to	Section alignment	The specified section that outputs externally defined syn Section alignment[0]	
	RAM mapped section', add the three	ROM to RAM mapped section Verify	ROM to RAM mapped section[0]	
	••	b Others		
	mappings as shown opposite.			
	T he second H (1997)			ſ
•	These settings are easily added by	Text Edit	—	
	clicking the button '' and pasting	Text:		
	the following text into the dialog:			
	the following toxt into the dialog.		*	
		D 2=R 2		
D=	D			
D	1=R 1			
	—			
υ_	2=R_2			
•	This ensures that the linker assigns			
	RAM rather than ROM addresses to			
	C variables. Click 'OK'			
			*	
		•	F.	
		ОК	Cancel <u>H</u> elp	



RSKRX24U





RSKRX24U





5.3 LCD Code Integration

API functions for the Okaya LCD display are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files ascii.h, r_okaya_lcd.h, iodefine.h, ascii.c, and r_okaya_lcd.c in this folder. Copy these files into the C:\Workspace\CG_Tutorial folder.







Code must be inserted in to the user code area in many files in this project, in the areas delimited by comments as follows:

/* Start user code for _xxxxx_. Do not edit comment generated here */ /* End user code. Do not edit comment generated here */

Where _xxxx_ depends on the particular area of code, i.e. 'function' for insertion of user functions and prototypes, 'global' for insertion of user global variable declarations, or 'include' for insertion of pre-processor include directives. User code inserted inside these comment delimiters is protected from being overwritten by Code Generator, if the user needs to subsequently change any of the Code Generator-generated code.

In the CS+ Project Tree, expand the 'Code Generator' folder and open the file 'r_cg_userdefine.h' by doubleclicking on it. Insert the following #defines in between the user code delimiter comments as shown below. /* Start user code for function. Do not edit comment generated here */



 $/\star$ End user code. Do not edit comment generated here $\star/$

In the CS+ Project Tree, open the file 'r_cg_main.c' by double-clicking on it. Insert #include "r_okaya_lcd.h" in between the user code delimiter comments as shown below.

/* Start user code for include. Do not edit comment generated here */

#include "r okaya lcd.h"

/* End user code. Do not edit comment generated here */

Scroll down to the 'main' function and insert the highlighted code as shown below into the beginning of the user code area of the 'main' function:

```
void main(void)
{
    R_MAIN_UserInit();
    /* Start user code. Do not edit comment generated here */
    /* Initialize the debug LCD */
    R_LCD_Init();
    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *)" RSKRX24U ");
    R_LCD_Display(1, (uint8 t *)" Tutorial ");
    R_LCD_Display(2, (uint8_t *)" Press Any Switch ");
    while (1U)
    {
        ;
        }
        /* End user code. Do not edit comment generated here */
}
```



5.3.1 SPI Code

The Okaya LCD display is driven by the SPI Master that was configured using Code Generator in §4.4.5. In the CS+ Project Tree, open the file 'r_cg_sci.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

/* Start user code for function. Do not edit comment generated here */

```
MD STATUS R SCI9 SPIMasterTransmit(uint8 t * const tx buf, const uint16 t tx num);
```

```
/* End user code. Do not edit comment generated here */
```

Now, open the r_cg_sci_user.c file and insert the following code in the user area for global: /* Start user code for global. Do not edit comment generated here */

```
/* Flag used locally to detect transmission complete */
static volatile uint8_t sci9_txdone;
```

/* End user code. Do not edit comment generated here */

Insert the following code in the transmit end call-back function for SCI9:

```
void r_sci9_callback_transmitend(void)
{
    /* Start user code. Do not edit comment generated here */
    sci9_txdone = TRUE;
    /* End user code. Do not edit comment generated here */
}
```

Now insert the following function in the user code area at the end of the file:

/* Start user code for adding. Do not edit comment generated here */

```
Function Name: R SCI9 SPIMasterTransmit
 Description : This function sends SPI9 data to slave device.
* Arguments : tx_buf -
              transfer buffer pointer
          tx_num -
              buffer size
* Return Value : status -
MD STATUS R SCI9 SPIMasterTransmit (uint8 t * const tx buf, const uint16 t tx num)
{
  MD STATUS status = MD OK;
  /* Clear the flag before initiating a new transmission */
  sci9 txdone = FALSE;
  /* Send the data using the API */
  status = R SCI9 SPI Master Send(tx buf, tx num);
  /* Wait for the transmit end flag */
  while (FALSE == sci9 txdone)
  {
     /* Wait */
  }
  return (status);
}
                 *****
* End of function R SCI9 SPIMasterTransmit
```

This function uses the transmit end callback function to perform flow control on the SPI transmission to the LCD, and is used as the main API call in the LCD code module.

}

5.3.2 CMT Code

The LCD code needs to insert delays to meet the timing requirements of the display module. This is achieved using the dedicated timer which was configured using Code Generator in 4.4.3. Open the file r_cg_cmt.h and insert the following code in the user area for function at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
```

void R_CMT_MsDelay(const uint16_t millisec);

```
/* End user code. Do not edit comment generated here */
```

Open the file r_cg_cmt_user.c and insert the following code in the user area for global at the beginning of the file:

/* Start user code for global. Do not edit comment generated here */

static volatile uint8 t one ms delay complete = FALSE;

/* End user code. Do not edit comment generated here */

Scroll down to the <code>r_cmt_cmi0_interrupt</code> function and insert the following line in the user code area: <code>static void r_cmt_cmi0_interrupt(void)</code>

```
/* Start user code. Do not edit comment generated here */
one_ms_delay_complete = TRUE;
/* End user code. Do not edit comment generated here */
```

Then insert the following function in the user code area at the end of the file: /* Start user code for adding. Do not edit comment generated here */

```
* Function Name: R CMT MsDelay
* Description : Uses CMT0 to wait for a specified number of milliseconds
Arguments : uint16 t millisecs, number of milliseconds to wait
* Return Value : None
           void R CMT MsDelay (const uint16 t millisec)
{
  uint16 t ms count = 0;
  do
  {
    R CMTO Start();
     while (FALSE == one_ms_delay_complete)
     {
       /* Wait */
    R CMT0 Stop();
     one_ms_delay_complete = FALSE;
     ms count++;
  } while (ms count < millisec);</pre>
End of function R CMT MsDelay
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The program will display 'RSKRX24U Tutorial Press Any Switch' on 3 lines in the LCD display.

5.4 Switch Code Integration

API functions for user switch control are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files rskrx24udef.h, r_rsk_switch.h and r_rsk_switch.c in this folder. Copy these files into the C:\Workspace\CG_Tutorial folder. Import these three files into the project in the same way as the LCD files.

The switch code uses interrupt code in the files $r_cg_icu.h$, $r_cg_icu.c$ and $r_cg_icu_user.c$ and timer code in the files $r_cg_cmt.h$, $r_cg_cmt.c$ and $r_cg_cmt_user.c$, as described in §4.4.2 and §4.4.3. It is necessary to provide additional user code in these files to implement the switch press/release detection and de-bouncing required by the API functions in $r_rsk_switch.c$.

5.4.1 Interrupt Code

In the CS+ Project Tree, expand the 'Code Generator' folder and open the file 'r_cg_icu.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
/* Function prototypes for detecting and setting the edge trigger of ICU_IRQ */
uint8_t R_ICU_IRQIsFallingEdge(const uint8_t irq_no);
void R ICU IRQSetFallingEdge(const uint8 t irq_no, const uint8 t set f edge);
void R_ICU_IRQSetRisingEdge(const uint8_t irq_no, const uint8_t set_r_edge);
```

/* End user code. Do not edit comment generated here */

Now, open the r_cg_icu.c file and insert the following code in the user code area at the end of the file:



```
/* Start user code for adding. Do not edit comment generated here */
* Function Name: R ICU IRQIsFallingEdge
* Description : This function returns 1 if the specified ICU IRQ is set to
           falling edge triggered, otherwise 0.
* Arguments : uint8_t irq_no
* Return Value : 1 if falling edge triggered, 0 if not
                                       ******
uint8 t R ICU IRQIsFallingEdge (const uint8 t irq no)
  uint8 t falling edge trig = 0x0;
  if (ICU.IRQCR[irq no].BYTE & 04 ICU IRQ EDGE FALLING)
     falling edge trig = 1;
  }
  return (falling edge trig);
}
* End of function R ICU IRQIsFallingEdge
                           *****
* Function Name: R ICU IRQSetFallingEdge
^{\star} Description \, : This function sets/clears the falling edge trigger for the
           specified ICU IRQ.
         : uint8 t irq no
* Arguments
           uint8_t set_f_edge, 1 if setting falling edge triggered, 0 if
           clearing
* Return Value : None
                   void R_ICU_IRQSetFallingEdge (const uint8_t irq_no, const uint8_t set_f_edge)
  if (1 == set f edge)
  {
     ICU.IRQCR[irq_no].BYTE |= _04_ICU_IRQ_EDGE_FALLING;
  }
  else
  {
     ICU.IRQCR[irq no].BYTE &= (uint8 t) ~ 04 ICU IRQ EDGE FALLING;
  }
}
* End of function R ICU IRQSetFallingEdge
                              * * * * * * * * * * * * * * * * *
* Function Name: R ICU IRQSetRisingEdge
* Description : This function sets/clear the rising edge trigger for the
           specified ICU IRO.
* Arguments
          : uint8_t irq_no
           uint8_t set_r_edge, 1 if setting rising edge triggered, 0 if
           clearing
* Return Value : None
              void R_ICU_IRQSetRisingEdge (const uint8_t irq_no, const uint8_t set_r_edge)
  if (1 == set r edge)
  {
     ICU.IRQCR[irq_no].BYTE |= _08_ICU_IRQ_EDGE_RISING;
  }
  else
  {
     ICU.IRQCR[irq no].BYTE &= (uint8 t) ~ 08 ICU IRQ EDGE RISING;
  }
}
* End of function R ICU IRQSetRisingEdge
```

/* End user code. Do not edit comment generated here */
Open the r_cg_icu_user.c file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */
/* Defines switch callback functions required by interrupt handlers */
#include "r_rsk_switch.h"
```

/* End user code. Do not edit comment generated here */

In the same file insert the following code in the user code area inside the function r_icu_irq0_interrupt: /* Start user code. Do not edit comment generated here */

/* Switch 1 callback handler */ R_SWITCH_IsrCallback1();

 $/\,\star\,$ End user code. Do not edit comment generated here $\,\star/\,$

In the same file insert the following code in the user code area inside the function r_icu_irq4_interrupt:

```
/* Start user code. Do not edit comment generated here */
/* Switch 2 callback handler */
R_SWITCH_IsrCallback2();
/* End user code. Do not edit comment generated here */
```

5.4.2 De-bounce Timer Code

Open the r_cg_cmt_user.c file and insert the following code in the user code area for include near the top of the file:

/* Start user code for include. Do not edit comment generated here */
/* Defines switch callback functions required by interrupt handlers */
#include "r_rsk_switch.h"

/* End user code. Do not edit comment generated here */

In the same file insert the following code in the user code area inside the function r_cmt_cmi1_interrupt:

```
/* Start user code. Do not edit comment generated here */
/* Stop this timer - we start it again in the de-bounce routines */
R_CMT1_Stop();
/* Call the de-bounce call back routine */
R_SWITCH_DebounceIsrCallback();
/* End user code. Do not edit comment generated here */
```

In the same file insert the following code in the user code area inside the function r cmt cmi2 interrupt:

```
/* Start user code. Do not edit comment generated here */
/* Stop this timer - we start it again in the de-bounce routines */
R_CMT2_Stop();
/* Call the de-bounce call back routine */
R_SWITCH_DebounceIsrCallback();
/* End user code. Do not edit comment generated here */
```



5.4.3 Main Switch and ADC Code

In this part of the tutorial we add the code to act on the switch presses to activate A/D conversions and display the result on the LCD. In §4.4.4, we configured the ADC to be triggered from the ADTRG0# pin, SW3. In this code, we also perform software triggered A/D conversion from the user switches SW1 and SW2, by reconfiguring the ADC trigger source on-the-fly once an SW1 or SW2 press is detected.

In the CS+ Project Tree, expand the 'Code Generator' folder and open the file 'r_cg_userdefine.h' by doubleclicking on it. Insert the following code the user code area, resulting in the code shown below /* Start user code for function. Do not edit comment generated here */

#define	TRUE	(1)
#define	FALSE	(0)

extern volatile uint8_t g_adc_trigger;

/* End user code. Do not edit comment generated here */

Open the file 'r_cg_main.c' and insert #include "r_rsk_switch.h" in the user code area for include, resulting in the code shown below:

/* Start user code for include. Do not edit comment generated here */

#include "r_okaya_lcd.h"
#include "r_rsk_switch.h"

/* End user code. Do not edit comment generated here */

Next add the switch module initialization function call highlighted in the user code area inside the main function, resulting in the code shown below:

```
{
    R_MAIN_UserInit();
    /* Start user code. Do not edit comment generated here */
    /* Initialize the switch module */
    R_SWITCH_Init();
    /* Initialize the debug LCD */
    R_LCD_Init();
    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *)" RSKRX24U ");
    R_LCD_Display(1, (uint8_t *)" Tutorial ");
    R_LCD_Display(2, (uint8_t *)" Press Any Switch ");
    while (1U)
    {
        ;
        }
        /* End user code. Do not edit comment generated here */
}
```

In the same file, insert the declarations in the user code area for global, resulting in the code shown below: /* Start user code for global. Do not edit comment generated here */

```
/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;
/* Prototype declaration for cb_switch_press */
static void cb_switch_press (void);
/* Prototype declaration for get_adc */
static uint16_t get_adc(void);
/* Prototype declaration for lcd display adc */
static void lcd_display_adc (const uint16_t adc_result);
```

^{/*} End user code. Do not edit comment generated here */

Next add the highlighted code below in the user code area inside the main function and the code inside the while loop, resulting in the code shown below:

```
void main(void)
{
    R MAIN UserInit();
    /* Start user code. Do not edit comment generated here */
    /* Initialize the switch module */
    R SWITCH Init();
    /* Set the call back function when SW1 or SW2 is pressed */
    R SWITCH SetPressCallback(cb_switch_press);
    /* Initialize the debug LCD */
    R LCD Init ();
    /* Displays the application name on the debug LCD */
   R_LCD_Display(0, (uint8_t *)" RSKRX24U ");
R_LCD_Display(1, (uint8_t *)" Tutorial ");
R_LCD_Display(2, (uint8_t *)" Press Any Switch ");
    /* Start the A/D converter */
    R_S12AD0_Start();
    while (1U)
    {
        uint16 t adc result;
        /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
        if (TRUE == g_adc_trigger)
        {
             /* Call the function to perform an A/D conversion */
             adc result = get adc();
             /* Display the result on the LCD */
             lcd_display_adc(adc_result);
             /* Reset the flag */
             g_adc_trigger = FALSE;
        }
        /* SW3 is directly wired into the ADTRGOn pin so will
           cause the interrupt to fire */
        else if (TRUE == g_adc_complete)
             /* Get the result of the A/D conversion */
             R S12AD0 Get ValueResult(ADCHANNEL0, &adc result);
             /* Display the result on the LCD */
             lcd display adc(adc result);
             /* Reset the flag */
             g_adc_complete = FALSE;
        }
        <mark>else</mark>
        {
              * do nothing */
        }
    }
    /* End user code. Do not edit comment generated here */
```

Then add the definition for the switch call-back, get adc and lcd display adc functions in the user code area for adding at the end of the file, as shown below:

}

```
/* Start user code for adding. Do not edit comment generated here */
* Function Name : cb switch press
* Description : Switch press callback function. Sets g_adc_trigger flag.
* Argument
          : none
* Return value : none
       static void cb_switch_press (void)
  /* Check if switch 1 or 2 was pressed */
  if (g switch flag & (SWITCHPRESS 1 | SWITCHPRESS 2))
  {
     /* Set the flag indicating a user requested A/D conversion is required */
     g adc trigger = TRUE;
     /* Clear flag */
     g switch flag = 0x0;
  }
}
* End of function cb_switch_press
                   * Function Name : get_adc
* Description : Reads the ADC result, converts it to a string and displays
            it on the LCD panel.
* it or
* Argument : none
* Return value : uint16_t adc value
                           static uint16 t get adc (void)
{
  /* A variable to retrieve the adc result */
  uint16_t adc_result;
  /* Stop the A/D converter being triggered from the pin ADTRGOn */
  R S12AD0 Stop();
  /* Start a conversion */
  R_S12AD0_SWTriggerStart();
  /* Wait for the A/D conversion to complete */
  while (FALSE == g_adc_complete)
  {
     /* Wait */
  }
  /* Stop conversion */
  R S12AD0 SWTriggerStop();
  /* Clear ADC flag */
  g_adc_complete = FALSE;
  R S12AD0 Get ValueResult(ADCHANNEL0, &adc result);
  /* Set AD conversion start trigger source back to ADTRGOn pin */
  R S12AD0 Start();
  return (adc_result);
}
* End of function get adc
```



```
RSKRX24U
```

```
* Function Name : lcd display adc
 Description : Converts add result to a string and displays
             it on the LCD panel.
* It on the LCD paner
* Argument : uint16_t adc result
 Return value : none
              static void lcd_display_adc (const uint16_t adc_result)
   /* Declare a temporary variable */
  uint8 t a;
   /\,\star\, Declare temporary character string \,\star/\,
  char lcd_buffer[11] = " ADC: XXXH";
  /\star Convert ADC result into a character string, and store in the local.
    Casting to ensure use of correct data type. */
   a = (uint8 t)((adc result & 0x0F00) >> 8);
  lcd buffer[6] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (uint8 t) ((adc result & 0x00F0) >> 4);
  lcd_buffer[7] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (uint8 t)(adc result & 0x000F);
   lcd buffer[8] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   /* Display the contents of the local string lcd buffer */
  R_LCD_Display(3, (uint8_t *)lcd_buffer);
 * End of function lcd_display_adc
```

Open the file 'r_cg_s12ad.h' by double-clicking on it. Insert the following code in the user code area for function, resulting in the code shown below:

/* Start user code for function. Do not edit comment generated here */

```
/* Flag indicates when A/D conversion is complete */
extern volatile uint8_t g_adc_complete;
/* Functions for starting and stopping software triggered A/D conversion */
void R_S12AD0_SWTriggerStart(void);
void R_S12AD0_SWTriggerStop(void);
```

/* End user code. Do not edit comment generated here */

Open the file 'r_cg_s12ad.c' by double-clicking on it. Insert the following code in the user code area for adding at the end of the file, resulting in the code shown below: /* Start user code for adding. Do not edit comment generated here */

/**************************************
* Function Name: R S12AD0 SWTriggerStart
* Description : This function starts the AD converter.
* Arguments : None
* Return Value : None

void R_S12AD0_SWTriggerStart(void)
IR(S12AD, S12ADI) = 0U; IEN(S12AD, S12ADI) = 1U;
S12AD, S12AD) = 10; S12AD.ADCSR.BIT.ADST = 10;
J
, /************************************
End of function R S12AD0 SWTriggerStart



Function Name	**************************************
Description	This function stops the AD converter.
Arguments	None
Return Value	None
*****	* * * * * * * * * * * * * * * * * * * *
oid R S12AD0 S	TriggerStop(void)
S12AD.ADCSF	BIT.ADST = 0U;
IEN(S12AD,	12ADI) = 0U;
IR(S12AD, S	2ADI) = 0U;
******	***************************************
Ind of function	R S12AD0 SWTriggerStop

 $/\,{}^{\star}$ End user code. Do not edit comment generated here ${}^{\star}/$

Open the file r_cg_s12ad_user.c and insert the following code in the user code area for global, resulting in the code shown below:

/* Start user code for global. Do not edit comment generated here */

/* Flag indicates when A/D conversion is complete */
volatile uint8_t g_adc_complete;

/* End user code. Do not edit comment generated here */

Insert the following code in the user code area of the r_s12ad0_interrupt function, resulting in the code shown below:

static void r_s12ad0_interrupt(void)
{
 /* Start user code. Do not edit comment generated here */
 g_adc_complete = TRUE;
 /* End user code. Do not edit comment generated here */
}

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. When any switch is pressed, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel. Return to this point in the CG_Tutorial to add the UART user code.



5.5 Debug Code Integration

API functions for trace debugging via the RSK serial port are provided with the RSK. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files r_rsk_debug.h and r_rsk_debug.c in this folder. Copy these files into the C:\Workspace\CG_Tutorial folder. Import these two files into the project in the same way as the LCD files.

In the r_rsk_debug.h file, ensure the following macro definition is included:

```
/* Macro for definition of serial debug transmit function - user edits this */
#define SERIAL_DEBUG_WRITE (R_SCI1_AsyncTransmit)
```

This macro is referenced in the r_rsk_debug.c file and allows easy re-direction of debug output if a different debug interface is used.

5.6 UART Code Integration

5.6.1 SCI Code

In the CS+ Project Tree, expand the 'Code Generator' folder and open the file 'r_cg_sci.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

/* Start user code for function. Do not edit comment generated here */

```
/* Exported functions used to transmit a number of bytes and wait for completion */
MD_STATUS R_SCI9_SPIMasterTransmit(uint8_t * const tx_buf, const uint16_t tx_num);
MD_STATUS R_SCI1_AsyncTransmit(uint8_t * const tx_buf, const uint16_t tx_num);
/* Character is used to receive key presses from PC terminal */
extern uint8_t g_rx_char;
```

/* Flag used to control transmission to PC terminal */
extern volatile uint8_t g_tx_flag;

/* End user code. Do not edit comment generated here */

Open the file 'r_cg_sci_user.c. Insert the following code in the user area for global near the beginning of the file:

/* Start user code for global. Do not edit comment generated here */ $\,$

/* Global used to receive a character from the PC terminal */
uint8_t g_rx_char;

/* Flag used to control transmission to PC terminal */
volatile uint8_t g_tx_flag = FALSE;

/* Flag used locally to detect transmission complete */
static volatile uint8 t sci9 txdone;
static volatile uint8_t sci1_txdone;

/* End user code. Do not edit comment generated here */

In the same file, insert the following code in the user code area inside the r_sci1_callback_transmitend function:

void r_sci1_callback_transmitend(void)
{
 /* Start user code. Do not edit comment generated here */
 sci1_txdone = TRUE;
 /* End user code. Do not edit comment generated here */
}



In the same file, insert the following code in the user code area inside the r_sci1_callback_receiveend function:

```
void r_sci1_callback_receiveend(void)
{
    /* Start user code. Do not edit comment generated here */
    /* Check the contents of g_rx_char */
    if (('c' == g_rx_char) || ('C' == g_rx_char))
    {
        g_adc_trigger = TRUE;
    }
    /* Set up SCI1 receive buffer and callback function again */
    R_SCI1_Serial_Receive((uint8_t *)&g_rx_char, 1);
    /* End user code. Do not edit comment generated here */
}
```

At the end of the file, in the user code area for adding, add the following function definition:

```
* Function Name: R SCI1 AsyncTransmit
* Description : This function sends SCI1 data and waits for the transmit end flag.
* Arguments : tx_buf -
               transfer buffer pointer
           tx_num -
              buffer size
* Return Value : status -
              MD OK or MD ARGERROR
_---
************************
                             MD_STATUS R_SCI1_AsyncTransmit (uint8 t * const tx buf, const uint16 t tx num)
{
  MD STATUS status = MD OK;
  /* clear the flag before initiating a new transmission */
  sci1 txdone = FALSE;
  /* Send the data using the API */
  status = R SCI1 Serial Send(tx buf, tx num);
  /* Wait for the transmit end flag */
  while (FALSE == sci1 txdone)
  {
     /* Wait */
  }
  return (status);
}
* End of function R_SCI1_AsyncTransmit
                            *****
    ****
****
```



5.6.2 Main UART code

Open the file 'r_cg_main.c'. Add the following declaration to the user code area for include near the top of the file:

/* Start user code for include. Do not edit comment generated here */

#include "r_okaya_lcd.h"
#include "r rsk switch.h"
#include "r_rsk_debug.h"

/* End user code. Do not edit comment generated here */

Add the following declaration to the user code area for global near the top of the file: /* Start user code for global. Do not edit comment generated here */ /* Variable for flagging user requested ADC conversion */ volatile uint8_t g_adc_trigger = FALSE; /* Prototype declaration for cb_switch_press */ static void cb_switch_press (void); /* Prototype declaration for get_adc */ static uint16_t get_adc(void); /* Prototype declaration for lcd_display_adc */ static void lcd_display_adc (const uint16_t adc_result); /* Prototype declaration for uart_display_adc */ static void uart_display_adc(const uint8_t adc_count, const uint16_t adc_result); /* Variable to store the A/D conversion count for user display */ static uint8_t adc_count = 0;

/* End user code. Do not edit comment generated here */

Add the following highlighted code to the user code area in the main function:



void main(void) {

```
R MAIN UserInit();
/* Start user code. Do not edit comment generated here */
/* Initialize the switch module */
R_SWITCH_Init();
/* Set the call back function when SW1 or SW2 is pressed */
R SWITCH SetPressCallback(cb_switch_press);
/* Initialize the debug LCD */
R LCD Init();
/* Displays the application name on the debug LCD */
R LCD Display(0, (uint8_t *)" RSKRX24U ");
R LCD Display(1, (uint8_t *)" Tutorial ");
R LCD Display(2, (uint8_t *)" Press Any Switch ");
/* Start the A/D converter */
R_S12AD0_Start();
 /* Set up SCI1 receive buffer and callback function */
R_SCI1_Serial_Receive((uint8_t *)&g_rx_char, 1);
/* Enable SCI1 operations */
R SCI1 Start();
while (1U)
{
    uint16 t adc result;
     /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
    if (TRUE == g_adc_trigger)
    {
         /* Call the function to perform an A/D conversion */
        adc result = get adc();
         /* Display the result on the LCD */
        lcd_display_adc(adc_result);
         /* Increment the adc count */
         if (16 == (++adc_count))
         {
             adc count = 0;
         }
        /* Send the result to the UART */
        uart_display_adc(adc_count, adc_result);
         /* Reset the flag */
        g adc trigger = FALSE;
    }
    /\star SW3 is directly wired into the ADTRGOn pin so will
       cause the interrupt to fire */
    else if (TRUE == g_adc_complete)
         /* Get the result of the A/D conversion */
        R S12AD0 Get ValueResult(ADCHANNEL0, &adc result);
         /* Display the result on the LCD */
        lcd_display_adc(adc_result);
         /* Increment the adc count */
         if (16 == (++adc count))
         {
             adc count = 0;
        }
        /* Send the result to the UART */
        uart_display_adc(adc_count, adc_result);
         /* Reset the flag *
        g_adc_complete = FALSE;
    }
    else
    {
         /* do nothing */
     }
}
/* End user code. Do not edit comment generated here */
```

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Then, add the following function definition in the user code area at the end of the file:

```
* Function Name : uart_display_adc
* Description : Converts adc result to a string and sends it to the UART1.
 Argument : uint8_t : adc_count
              uint16_t: adc result
* Return value : none
* * * * * * * * * * * * * * * *
               static void uart display adc (const uint8 t adc count, const uint16 t adc result)
{
   /* Declare a temporary variable */
   char a:
   /* Declare temporary character string */
   static char uart buffer[] = "ADC xH Value: xxxH\r\n";
   /* Convert ADC result into a character string, and store in the local.
     Casting to ensure use of correct data type. */
   a = (char) (adc_count & 0 \times 000F);
   uart buffer[4] = (char) ((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char)((adc result & 0x0F00) >> 8);
   uart_buffer[14] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char)((adc result & 0x00F0) >> 4);
   uart buffer[15] = (char) ((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char) (adc_result & 0 \times 000F);
   uart buffer[16] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   /* Send the string to the UART */
   R DEBUG Print(uart buffer);
}
* End of function uart display adc
                           *****
       * * * * * * * * * * * * *
                   * * * * * * *
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. Connect the RSK G1CUSB0 port to a USB port on a PC. If this is the first time the RSK has been connected to the PC then a device driver will be installed automatically. Open Device Manager, the virtual COM port will now appears under 'Port (COM & LPT)' as 'RSK USB Serial Port (COMx)', where x is a number.

Open a terminal program, such as HyperTerminal, on the PC with the same settings as for SCI1 (see §4.4.5). When any switch is pressed, or when 'c' is sent via the COM port, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel and send the result to the PC terminal program via the SCI1. Return to this point in the CG_Tutorial to add the LED user code.



5.7 LED Code Integration

Open the file 'r_cg_main.c'. Add the following declaration to the user code area for include near the top of the file:

/* Start user code for include. Do not edit comment generated here */ #include "r_okaya_lcd.h"
#include "r_rsk_switch.h"
#include "r_rsk_debug.h" #include "rskrx24udef.h' /* End user code. Do not edit comment generated here */ Add the following declaration to the user code area for global near the top of the file: * Start user code for global. Do not edit comment generated here */ /* Variable for flagging user requested ADC conversion */ volatile uint8_t g_adc_trigger = FALSE; /* Prototype declaration for cb_switch_press */ static void cb switch press (void); /* Prototype declaration for get adc */ static uint16 t get adc(void); /* Prototype declaration for lcd display adc */ static void lcd_display_adc (const uint16_t adc_result); /* Prototype declaration for uart_display_adc */ static void uart display adc(const uint8 t adc count, const uint16 t adc result); /* Variable to store the A/D conversion count for user display */ static uint8 t adc count = 0; /* Prototype declaration for led display count */ static void led_display_count(const uint8_t count); /* End user code. Do not edit comment generated here */

Add the following highlighted code to the user code area in the main function:



void main(void)
{

```
R MAIN UserInit();
/* Start user code. Do not edit comment generated here */
/* Initialize the switch module */
R SWITCH Init();
/* Set the call back function when SW1 or SW2 is pressed */
R_SWITCH_SetPressCallback(cb_switch_press);
/* Initialize the debug LCD */
R LCD Init();
/* Displays the application name on the debug LCD */
R LCD_Display(0, (uint8_t *)" RSKRX24U ");
R_LCD_Display(1, (uint8_t *)" Tutorial ");
R_LCD_Display(2, (uint8_t *)" Press Any Switch ");
/* Start the A/D converter */
R_S12AD0_Start();
/* Set up SCI1 receive buffer and callback function */
R_SCI1_Serial_Receive((uint8_t *)&g_rx_char, 1);
/* Enable SCI1 operations */
R_SCI1_Start();
while (1U)
{
    uint16 t adc result;
    /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
    if (TRUE == g_adc_trigger)
    {
         /* Call the function to perform an A/D conversion */
         adc_result = get_adc();
         /* Display the result on the LCD */
         lcd_display_adc(adc_result);
         /* Increment the adc_count and display using the LEDs */
         if (16 == (++adc_count))
         {
             adc count = 0;
         led_display_count(adc_count);
         /* Send the result to the UART */
         uart_display_adc(adc_count, adc_result);
         /* Reset the flag */
         g adc trigger = FALSE;
    }
    /* SW3 is directly wired into the ADTRGOn pin so will
       cause the interrupt to fire */
    else if (TRUE == g_adc_complete)
         /* Get the result of the A/D conversion */
         R S12AD0 Get ValueResult(ADCHANNEL0, &adc result);
         /* Display the result on the LCD */
         lcd_display_adc(adc_result);
         /* Increment the adc_count and display using the LEDs */
         if (16 == (++adc_count))
         {
             adc count = 0;
         led_display_count(adc_count);
         /* Send the result to the UART */
         uart_display_adc(adc_count, adc_result);
/* Reset the flag */
         g adc complete = FALSE;
    }
    else
    {
         /* do nothing */
    }
}
/* End user code. Do not edit comment generated here */
```

}

Then, add the following function definition in the user code area at the end of the file:

/* End user code. Do not edit comment generated here */

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The code will perform the same but now the LEDs will display the adc_count in binary form.



6. Debugging the Project

•	In the 'Project Tree' pane, right-click the 'RX Simulator (Debug Tool)'. Select 'Using Debug Tool -> RX E2 Lite'.	Project Tree 4 × CG Tutorial (Project)* CG Tutorial (Project)* SF524UEAxFB (Microcontroller) Code Generator (Design Tool) Project Tree Code Generator (Design Tool) Code Preview CC-RX (Build Tool) RX Simulator (Debug Tool) File Vsing Debug Tool Property	RX E2 Lite RX E1(Serial) RX E20(Serial) RX Simulator
•	Double-click 'RX E2 Lite (Debug Tool)' to display the debugger tool properties. Under 'Clock', change the main clock frequency to 20 MHz and operating frequency to 80MHz.' Under 'Connection with TargetBoard', change 'Power targetfrom the emulator.(MAX 200mA)to 'Yes' All other settings can remain at their defaults.	Property RX E2 Lite Property Internal ROM/RAM Size of internal ROM/KBytes] Size of internal RAM[KBytes] Size of DataFlash memory[KBytes] Clock Main clock source Main clock frequency[MHz] Operating frequency[MHz] Allow changing of the clock source on writing internal flash memory Connection with Target Board	512 32 8 EXTAL 20.0000 80.0000 No
		Power target from the emulator.(MAX 200mA) Supply voltage Communications method FINE baud rate[bps]	Yes 3.3V FINE 1500000
•	Connect the E2 Lite to the PC and the RSK E1/E2 Lite connector. Connect the Pmod LCD to the PMOD1 connector. From the 'Debug' menu select 'Download' to start the debug session and download code to the target.		



7. Running the Code Generator Tutorial

7.1 Running the Tutorial

Once the program has been downloaded onto the RSK device, the program can be executed. Click the 'Go' button or press F5 to begin the program from the current program counter position. It is recommended that you run through the program once first, and then continue to the Tutorial manual to review the code.





8. Additional Information

Technical Support

For details on how to use CS+, refer to the help file by opening CS+, then selecting Help > Help Contents from the menu bar.



For information about the RX24U group microcontroller refer to the RX24U Group Hardware Manual.

For information about the RX assembly language, refer to the RX Family Software Manual.

Technical Contact Details

Please refer to the contact details listed in section 8 of the "Quick Start Guide".

General information on Renesas microcontrollers can be found on the Renesas website at: <u>https://www.renesas.com/</u>

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SALES OFFICES

Renesas Electronics America Inc. 2801 Scott Boulevard Santa Clara, CA 95050-2549, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130 Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004 Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-585-100, Fax: +44-1628-585-900 Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327 Renesas Electronics (China) Co., Ltd. Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679 Renesas Electronics (Shanghai) Co., Ltd. Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333 Tel: +86-21-2226-0888, Fax: +86-21-2226-0999 Renesas Electronics Hong Kong Limited Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022 Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670 Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300 Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HALII Stage, Indiranagar, Bangalore, India Tel: +91-80-67208700, Fax: +91-80-67208777 Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

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