

16

# e<sup>2</sup> studio Integrated Development Environment

# User's Manual: Getting Started Guide

RENESAS MCU RZ Family

All information contained in these materials, including products and product specifications, represents information on the product at the time of publication and is subject to change by Renesas Electronics Corp. without notice. Please review the latest information published by Renesas Electronics Corp. through various means, including the Renesas Electronics Corp. website (http://www.renesas.com).

Renesas Electronics

Rev.1.02 Sep.2020

### Notice

- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
- Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
  - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

- 6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
- This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
   Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas
- Please contact a Renesas Electronics sales onice if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
- (Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

#### **Corporate Headquarters**

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

www.renesas.com

#### Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

## Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit: <u>www.renesas.com/contact/</u>.

# General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

#### 1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

#### 2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power is supplied until the power reaches the level at which reseting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

#### 5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

## How to Use This Manual

This manual describes the role of the e<sup>2</sup> studio integrated development environment for developing applications and systems and provides an outline of its features .

e<sup>2</sup> studio is an Integrated Development Environment (IDE) for RX family, RL78 family and RZ family, integrating the necessary tools for the development phase of software (design, implementation, and debugging) into a single platform.

By providing an integrated environment, it is possible to perform all development using just this product, without the need to use many separate tools .

Readers		or users who wish to understand the functions of the software and hardware applications.
Purpose		de the user with the explanation of the features provided by nence the development of their hardware and software d devices.
Organization	This manual can be broad	lly divided into the following sections.
	CHAPTER 1 GENERA CHAPTER 2 INSTALL CHAPTER 3 IMPORT CHAPTER 4 BUILD CHAPTER 5 DEBUG CHAPTER 6 HELP	
How to Read This Manual	It is assumed that the read logic circuits, and micropre	ders of this manual have general knowledge of electronics, ocessors.
Conventions	Data significance:	Higher digits on the left and lower digits on the right
	Active low representation:	XXX (overscore over pin or signal name)
	Note:	Footnote for item marked with Note in the text
	Caution:	Information requiring particular attention
	Remark:	Supplementary information
	Numeric representation:	Decimal XXXX

Abbreviation	Full Form
API	Application Programming Interface
ARM	Advanced RISC Machine
CAN	Controller Area Network
CDT	C/C++ Development Tooling
CG	peripheral Code Generator
CPU	Central Processing Unit
ELF/DWARF	Executable and Linkable Format / Debugging With Attributed Record Formats
FIT	Firmware Integration Technology
GB	Gigabyte
GCC	GNU Compiler Collection
GDB	GNU project DeBugger
GHz	Giga Hertz
GUI	Graphical User Interface
IDE	Integrated Development Environment
I/O	Input / Output
JTAG	Joint Test Action Group
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LVDS	Low-Voltage Differential Signalling
MCU	Microcontroller Unit
OS	Operating System
PC	Personal Computer or Program Counter
PMOD	Peripheral MODule (an open standard defined by Digilent Inc <sup>™</sup> )
RTOS	Real Time Operating System
SC	Smart Configurator
SD	Secure Digital
SFR	Special Function Registers
SVN	SubVersioN
TFT	Thin Film Transistor
URL	Uniform Resource Locator
USB	Universal Serial Bus

## List of Abbreviations and Acronyms

## Table of Contents

1. G	eneral	1
1.1	System Configuration	1
1.2	System Requirements	1
1.3	Supported Compiler	2
1.4	Additional Tool for Compiler Update	
1.5	Supported Emulator	2
2. In	istallation	3
2.1	Installation of e <sup>2</sup> studio IDE (64-bit version)	3
2.2	Installation of e <sup>2</sup> studio IDE (32-bit version)	13
2.3	Installation of Additional Software	22
2.4	Uninstallation of the e <sup>2</sup> studio IDE	25
2.5	Major Version Upgrade for the e <sup>2</sup> studio IDE	25
2.6	Update e <sup>2</sup> studio plugins	25
2.7	Register Toolchain to e <sup>2</sup> studio	29
3. In	nport / Generate Project	
3.1	Import Existing Project	
3.2	New Project Generation	
4. B	uild	51
4.1	Build Option Settings	
4.2	Building the Sample Project	
4.3	Exporting Build Configuration Settings	
5. D	ebug	
5.1	Change Existing Debug Configurations	
5.2	Create New Debug Configurations	
5.3	Launch Bar	
5.4	Basic Debugging Features	
5.5	How to debug (RZ/A2M)	
6. H	elp	
	1	•••••••

## 1. General

Renesas  $e^2$  studio is the Integrated Development Environment for Renesas embedded microcontrollers.  $e^2$  studio is based on the industry-standard open-source Eclipse IDE framework and the C/C++ Development Tooling (CDT) project, covering build (editor, compiler, and linker control) and debug phases with an extended GNU Debug (GDB) interface support.

This chapter describes the system configuration and operating environment for the e<sup>2</sup> studio IDE for developing applications for the RZ family high-end ARM-based microprocessors.

## 1.1 System Configuration



Below is an example of a typical system configuration.

Figure 1-1. System Configuration Example - RZ/A1H Renesas Starter Kit

## 1.2 System Requirements

Below are the system requirements for this product.

Hardware Environment:

- Processor: At least 1GHz (hyper-threading/multi-core supporting CPU)
- Main Memory: At least 4GB of free space. 8GB is recommended.
- Hard disk Capacity: At least 2GB of free space
- Display: Resolution at least 1,024 x 768; at least 65,536 colors
- Interface: USB 2.0 (High-speed/Full-speed). High-speed is recommended.

Software Environment:

Windows® 8.1 (32/ 64-bit versions) and Windows® 10 (32/64-bit versions) 64-bit OS is required for  $e^2$  studio 2020-04 or later.



## 1.3 Supported Compiler

RZ family devices use GNU compilers for ARM architecture. Each device has a recommended version of the GNU compiler. Refer to <u>https://www.renesas.com/rz</u>. These compilers can be installed during the installation of e<sup>2</sup> studio by selecting them in the **Additional Software** pane (an Internet connection is required - see chapter 2.1) or can be downloaded from the web page below and installed separately (toolchain integration will be necessary).

- GNU ARM Embedded toolchain (GCC 6; 6-2016-q4-major or higher version): <u>https://developer.arm.com/open-source/gnu-toolchain/gnu-rm/downloads</u>
- GNU ARM Embedded toolchain (GCC<sup>note</sup> 5; 5-2016-q3-update or lower version): <u>https://launchpad.net/gcc-arm-embedded/+download</u>
- GNUARM-NONE toolchain: <u>https://gcc-renesas.com/rz/rz-download-toolchains/</u>

Refer to chapter 2.2 for compiler installation and chapter 2.6 for e<sup>2</sup> studio toolchain integration.

Note: 'GCC' stands for 'the GNU Compiler Collection' (Refer to https://gcc.gnu.org/).

## 1.4 Additional Tool for Compiler Update

**Libgen Update for GNU ARM Embedded Toolchains** is a tool used to build 'newlib' specific libraries which is a C standard library implementation intended for embedded systems. Instead of a 'prebuilt' version of a library (a library built with a set configuration when the toolchain is created), the Libgen Update allows users to have their own 'project-built' library build configuration, using a custom set of options.

**Libgen Update for GNU ARM Embedded Toolchains** can be installed during the installation of e<sup>2</sup> studio if selected (an Internet connection is required), or can be downloaded from the web page below:

Libgen for GNU ARM embedded toolchain (V1.2018.02 or higher version) <u>https://gcc-renesas.com/rz/rz-download-toolchains/</u>

Refer to chapter 2.2 for the installation of Libgen Update for GNU ARM Embedded Toolchains.

## 1.5 Supported Emulator

Segger J-Link Lite (RZ) and other emulators

For more detail, see RZ family product web page: <u>https://www.renesas.com/rz</u>



## 2. Installation

The latest  $e^2$  studio IDE installer package can be downloaded from Renesas website for free. Renesas now supports both 32-bit and 64-bit versions of  $e^2$  studio.

Please check detailed information from: <u>https://www.renesas.com/e2studio</u>. Note that user has to login to the Renesas account (in MyRenesas page) for the software download.

This chapter describes the installation and un-installation for the e<sup>2</sup> studio IDE.

e<sup>2</sup> studio installer can be used to upgrade e<sup>2</sup> studio as well as new installation. However, it does not support update between major versions such as from v5.x to v6.y, or from v6.m to v7.n.

Please uninstall the earlier versions before installation. Alternatively, install new e<sup>2</sup> studio into a new folder if you would like to keep earlier versions.

The detailed information is described below.

## 2.1 Installation of e<sup>2</sup> studio IDE (64-bit version)

1. Double-click on e<sup>2</sup> studio installer to invoke the e<sup>2</sup> studio installation wizard page. Click [Install].

**Note:** If e<sup>2</sup> studio was installed in your PC, the option to modify, remove the existing version or install e<sup>2</sup> studio to a different location will be displayed.

🛃 Renesas e² studio 2020-04 Setup				
Renesas e <sup>2</sup> studio 2020-04 Setup			REN	ΕSΛS
e2 studio version 20.4.0.R20200328-1512 is already installed.				
What do you want to do	?			
Modify Add or remove fea Location: C:\Renes Winistall the existi Location: C:\Renes Install Install to a different	as\e2_studio ng version. as\e2_studio	g installation.		
<u>v202003281637</u>	< Back	Next >	Install	Cancel

Figure 2-1. 64-bit e2 studio installation wizard



#### 2. Welcome page

User can change the install folder by clicking [Change...]. Click [Next] to continue.

**Note1:** If you would like to have multiple versions of  $e^2$  studio, please specify new folder here. **Note2:** Multi-byte characters cannot be used for  $e^2$  studio installation folder name.



Figure 2-2 Installation of 64-bit e<sup>2</sup> studio – Welcome page



#### 3. Device Families

Select Devices Families to install. Click the [Next] button to continue.

Select the device families you w	ish to install support for
Welcome	RENESAS RA
	Build, Debug & Code Generation support for Renesas RA devices
Extra Features	REVESAS RZ Build, Debug & Code Generation support for Renesas RZ devices
Customise Features	RL78 Build, Debug & Code Generation support for Renesas RL78 devices
Additional Software	RX Build, Debug & Code Generation support for Renesas RX devices
Licenses	RENESAS RH850
Shortcuts	RHB50 Debug support for Renesas RH850 devices
Summary	RE DILLO DI
Installing	Build & Debug support for Renesas RE devices
Results	ect All

Figure 2-3. Installation of 64-bit e2 studio – Device Families



#### 4. Extra Features

Select Extra Features (i.e. Language packs, SVN & Git support, RTOS support...) to install. For non-English language users, please select Language packs at this step.

Click the [Next] button to continue.

<b>Renesas e<sup>2</sup> studio 202</b> Select the extra features y	•	RENESAS
Welcome Device Families		Japanese Language Support Chinese (Simplified) Language Support
→ Extra Features Customise Features		Chinese (Traditional) Language Support
Additional Software		Git Integration Git SCM Support
Licenses Shortcuts		Terminals ANSI/vt102 compatible Terminal support for Serial, ssh and Telnet RTOS
Summary Installing		FreeRTOS & OpenRTOS Debug Support
Results	Select All	
<u>v202004121319</u>		< <u>B</u> ack <u>Next</u> > <u>Install</u> Cancel

Figure 2-4 Installation of 64-bit e2 studio – Extra Features



#### 5. Customise Features

Select the components to install and click the [Next] button to continue.

enesas e² studio 20	020-04 Setup	5
Welcome	Select the components you want to install.	
Device Families	✓ Renesas e2 studio (20.4.0.R20200412-1149)	1
Extra Features	OpenJDK & OpenJFX	
Customise		l
Features	⊕ 🖉 Renesas e2 studio Common Components for ARM Devices (20.4.0.R20200412-1149)	
Additional	Common components for Renesas e2 studio	
Software		
Licenses	Renesas e2 studio Tools	
Shortcuts	C Renesas RA Family Support (20.4.0.R20200412-1149)     Renesas RA family support to allow project generation, build & debug	
Summary	<ul> <li></li></ul>	
Installing	Select All Optional Deselect All Optional	
Results	Size of install: 711.4 MB	

Figure 2-5 Installation of 64-bit e2 studio – Features



#### 6. Additional Software

Select additional software (i.e. compilers, utilities, QE...) and click [Next] to continue.

**Note:** With no Internet access available, additional software installation can be skipped because software catalog cannot be downloaded. The additional software can be installed later.

	ftware you wish to install	ESA
Welcome	GCC Toolchains & Utilities (0)	
Device Families	GNU ARM Embedded	*
Customise Features	GNU ARM Embedded 6 2017q2 Download size: 82.6 MB <b>LibGen for GNU ARM Embedded</b> 1.1.0 LibGen for GNU ARM Embedded (requires GNU ARM Embedded too	olchain)
Software Licenses	Download size: 7.6 MB	orchainy
Licenses Shortcuts		

Figure 2-6 Installation of 64-bit e<sup>2</sup> studio – Additional Software



#### 7. License Agreement

Read and accept the software license agreement. Click the [Next] button.

Please note that user must accept the license agreement, otherwise installation cannot be continued.

Welcome	Please read and accept the follo	wing Software Agreements
Device Families Extra Features Customise Features Additional Software Licenses Shortcuts Summary Installing Results	Renesas e2 studio OpenJDK License Agreement ARM DS-5 Toolchain Integration	License Terms and Conditions for RENESAS e2 studio This Renesas e2 studio license agreement ("Agreement") is between the entity on whose behalf you are entering into this Agreement ("Client") and Renesas Electronics Corporation, a Japanese company with its registered office at 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan ("Renesas"). YOU SHOULD READ THIS AGREEMENT CAREFULLY, AS IT CONSTITUTES A BINDING CONTRACT BETWEEN CLIENT AND RENESAS. The Renesas IDE Software (defined below) is intended for commercial use by a company or corporation only and is not designed, developed or produced for any private use or purpose. If you are an individual, or you intend to install the Renesas IDE Software on behalf of an individual, or the Renesas IDE Software is expected to be used for a private purpose directly or indirectly, you should click "No" on the installer. Otherwise, by clicking the "I accept" button or other button or mechanism designed to acknowledge agreement to the terms of an electronic copy of this Agreement, or by installing, accessing, or otherwise copying or using all or any portion of the Renesas IDE Software, you accept this Agreement on behalf of the

Figure 2-7 Installation of 64-bit e<sup>2</sup> studio – Licenses



#### 8. Shortcuts

Select shortcut name for start menu and click [Next] button to continue.

**Note:** If  $e^2$  studio was installed in another location, it is recommended to rename to distinguish from the other  $e^2$  studio(s).

Renesas e² studio 20	020-04 Setup	R	ENESAS
Welcome Device Families Extra Features Customise Features Additional Software Licenses Shortcuts Summary Installing Results	Shortcuts to important pro locations:	ograms and files will be creat Renesas Electronics e2 stud	
v202004121319	< <u>B</u> ack	<u>N</u> ext > Install	Cancel

Figure 2-8 Installation of 64-bit e<sup>2</sup> studio – Shortcuts



#### 9. Summary

Components list to be installed is shown. Please confirm the contents and click the [Install] button to install the Renesas e<sup>2</sup> studio IDE.



Figure 2-9 Installation of 64-bit e<sup>2</sup> studio – Summary

10. Installing...

The installation is performed. Based on selected items of Addition Software, new dialogs are opened to proceed with installation for these softwares.



#### 11. Results

Installation results are listed here. Please note if any errors are shown.

Click [OK] button to complete the installation.

🔜 Renesas e² studio 202	20-04 Setup	— 🗆 X
Renesas e² studio 20	20-04 Setup	RENESAS
Welcome	Installation of <b>e2 studio</b> is complete.	
Device Families	Please click <b>OK</b> to close.	
Extra Features Customise Features	☐ Launch e2 studio? ✓ View Release Notes?	
Additional Software	✓ View What's New?	
Licenses		
Shortcuts		
Summary		
Installing		
→ Results		
v202004121319	< Back Next >	OK Cancel

Figure 2-10 Installation of 64-bit e<sup>2</sup> studio – Results



## 2.2 Installation of e<sup>2</sup> studio IDE (32-bit version)

1. Double-click on e<sup>2</sup> studio installer to invoke the e<sup>2</sup> studio installation wizard page. Click [Install].

**Note:** If e<sup>2</sup> studio was installed in your PC, the option to modify, remove the existing version or install e<sup>2</sup> studio to a different location will be displayed. If you would like to have multiple versions of e<sup>2</sup> studio, **please specify "Install to a different location" here.** Click the [Next] button to continue.

🛃 e² studio Setup			— 🗆 X
e <sup>2</sup> studio 7.8.0.R202	00321-2326 Setup	RE	NESAS
e2 studio is already insta			
	What do you want to do?		
	Upgrade Upgrade to version 7.8.0.R202 Location: C:\Renesas\e2_studio		
	Hodify Add or remove features in the Location: C:\Renesas\e2_studio		
	Remove Uninstall the existing version. Location: C:\Renesas\e2_studio	_7_8	
	Install Install to a different location.		
<u>v202003220126</u>	< <u>B</u> ack <u>N</u> ex	t > <u>I</u> nstall	Cancel

Figure 2-11. 32-bit e<sup>2</sup> studio installation wizard

2. Welcome page

Click the [Next] button to continue.

🛃 e² studio Setup		—	) X
e² studio 7.8.0.R2020	0321-2326 Setup	RENE	SVZ
⇔ <b>Welcome</b>	Welcome to the $e^2$ <b>studio</b> installation wizard. This winstallation process.	vizard will guide you thr	rough the
O Device Families	Click <b>Next</b> to proceed to the next page.		
<ul> <li>Extra Components</li> <li>Components</li> <li>Additional Software</li> </ul>	Click <b>Back</b> to change something on a previous page Click <b>Cancel</b> to cancel this installation at any time.	2.	
○ Licenses ○ Shortcuts			
<ul> <li>Summary</li> <li>Installing</li> <li>Results</li> </ul>			
		Configure Prox	ky Settings
<u>v202003220126</u>	< <u>B</u> ack <u>Next</u> >	Install Ca	ancel

Figure 2-12. Welcome page of 32-bit installer



#### 3. Install Folder

The default installation location is set to: C:\Renesas\e2\_studio. Enter the install folder directly into the textbox or click the **Browse...** button to modify it. Click the **Next** button to continue.

**Note1:** If you would like to have multiple versions of  $e^2$  studio, please specify new folder here. **Note2:** Multi-byte characters cannot be used for  $e^2$  studio installation folder name.

😼 e² studio Setup			_	
e² studio 7.8.0.R2020	00321-2326 Setup		RENE	SΛS
<ul> <li>Welcome</li> </ul>	Where would you like to i	nstall?		
Install Folder	C:\Renesas\e2_studio			
O Device Families			€ <u>R</u> estore Default	D Browse
O Extra Components				
○ Components				
O Additional Software				
○ Licenses				
○ Shortcuts				
O Summary				
O Installing				
○ Results				
<u>v202003220126</u>	< <u>B</u> ack <u>1</u>	Next >	Install	Cancel

Figure 2-13. Installation of 32-bit e<sup>2</sup> studio – Install Folder



#### 4. Device Families

Select Devices Families to install. Make sure you tick the checkbox for Support for RZ Devices.



Figure 2-14. Device Families Page (32-bit): Support for RZ Devices



**Note:** Support for RZ/G Devices is the option for RZ/G Linux application project generation and debugging. (Only 32-bit version of e<sup>2</sup> studio has this option.) If you need to develop applications for Linux on RZ/G device, please select this too.



Figure 2-15. Device Family Page (32-bit): Support for RZ/G Devices



#### 5. Extra Components

Select the required extra components (e.g. language packs, SVN & Git support, RTOS support...). For example, select **RTOS** if using a RTOS such as FreeRTOS or OpenRTOS. Click the **Next** button to continue.

e <sup>2</sup> studio 7.8.0.R2020	0321-2326 S	etup RENESAS
Welcome		Japanese Language Pack Provides Japanese language support
<ul> <li>Install Folder</li> <li>Device Families</li> </ul>		Chinese (Simplified) Language Pack Provides Simplified Chinese language support
Extra Components O Components		Chinese (Traditional) Language Pack Provides Traditional Chinese language support
• Additional Software		Collaboration Includes SVN & Git support
<ul> <li>Shortcuts</li> </ul>	☑ 🖵	RTOS FreeRTOS & OpenRTOS Debug Support
○ Summary ○ Installing		Terminals ANSI/vt102 compatible Terminal support for Serial, ssh and Telnet
○ Results		
	Select All D	eselect All

Figure 2-16. Extra Components Page (32-bit): RTOS is selected



#### 6. Components

The components needed by the selected **Device Families**, are automatically selected. Please check the selected components and then click the **Next** button to continue.

🛃 e² studio Setup		
e <sup>2</sup> studio 7.8.0.R2020	0321-2326 Setup	IESVZ
Welcome	Select the components you want to install and deselect the components you do no	t want to install.
Install Folder	Required Components	* ^
• Device Families	Renesas e2 studio 7.8.0.R20200321-2326	
Extra Components	Renesas e2 studio Common Core 6.12.0.v20200305-1438 Renesas e2 studio Common Core	
⇔ Components	IAR Plugin Manager 1.1.0.201906261345	
O Additional Software	IAR Plugin Manager simplifies installation of IAR compiler and debugger plugins.	
○ Licenses	Optional Components	*
○ Shortcuts	GCC for Renesas RZ Build Support 7.8.0.v20200313-1056	
O Summary	GCC for Renesas RZ Build Support	
O Installing	Renesas RZ Debug Support 7.8.0.v20191127-2112 Renesas RZ Debug Support	
○ Results	Renesas common Build 6.10.0.v20200313-1019 Renesas Common Build	
	Renesas e2 studio Common Build for GCC Toolchains 6.8.2.v20200312-124 Renesas e2 studio Common Build for GCC Toolchains	45
	Select All Optional Deselect All Optional	
	Around	774.5 MB required
v202003220126	< Back Next > Install	Cancel

Figure 2-17. Components Page (32-bit e<sup>2</sup> studio)



#### 7. Additional Software

Check that additional software (i.e. GCC ARM Embedded (it is same with GNU ARM Embedded toolchain) and LibGen for GCC ARM Embedded) are selected. The additional software list is automatically populated according to the selected device families. The selected software's installer will be invoked by the e<sup>2</sup> studio installer (for more information, see chapter 2.3). If you want to use a version of GCC ARM Embedded which is not included in the list, please download and install it separately (see chapter 1.3). Click the **Next** button to continue.



Figure 2-18. Additional Software Page (32-bit e<sup>2</sup> studio)



#### 8. License Agreement

Read and accept the software license agreement by selecting the **Next** button. Please note that you have to accept the license agreement, otherwise installation cannot be continued.

🔜 e² studio Setup		— <b>D</b> X
e <sup>²</sup> studio 7.8.0.R2020032	1-2326 Setup	RENESAS
<ul> <li>Welcome</li> </ul>	Please read and accept the following Soft	ware Agreements
<ul> <li>Install Folder</li> <li>Device Families</li> <li>Extra Components</li> <li>Components</li> <li>Additional Software</li> <li>Additional Software</li> <li>Shortcuts</li> <li>Summary</li> <li>Installing</li> <li>Results</li> </ul>	WHIS StateViewer Eclipse Foundation Software User Agr Eclipse Foundation Software User Agr License Terms and Conditions for REN IAR Plugin Manager	STATEVIEWER LICENSE AGREEMENT ATTENTION: PLEASE READ THIS DOCUMENT CAREFULLY BEFORE INSTALLING THE SOFTWARE. THE INDIVIDUAL OR ENTITY INSTALLING THIS SOFTWARE (THE "USER") AGREES TO BE BOUND BY THE TERMS OF THIS LICENSE. IF YOU DO NOT AGREE TO THE TERMS OF THIS LICENSE, DO NOT INSTALL OR USE THE SOFTWARE. This License Agreement (the 'Agreement') is entered into at the date of installation of the Software (the 'Effective Date') by and between the USER and WITTENSTEIN aerospace & simulation Itd, a corporation registered under the laws of England and trading
	I accept the terms of the Software Agree	ments
<u>v202003220126</u>	< <u>B</u> ack	Next > Install Cancel

Figure 2-19. Additional Software Page (32-bit e<sup>2</sup> studio)

### 9. Shortcuts

Select a shortcut name for the start menu and click the **Next** button to continue.

🔜 e² studio Setup				_	
e <sup>²</sup> studio 7.8.0.R2020032	1-2326 Setup		RE	NES	572
Welcome	Shortcuts to important pro	grams and files will be	created in the following	g locations:	
Install Folder	In start menu group:	Renesas Electronics e2	studio		
Device Families				<b>1</b>	estore Default
Extra Components					-
Components					
Additional Software					
Licenses					
Shortcuts					
<ul> <li>Summary</li> <li>Installing</li> </ul>					
Results					
0 1000					
		c De els	Nexts	la stall	Consel
<u>v202003220126</u>		< <u>B</u> ack	<u>N</u> ext >	Install	Cancel

Figure 2-20. Shortcuts Page (32-bit e<sup>2</sup> studio)

#### 10. Summary

Click the Install button to install the Renesas e<sup>2</sup> studio IDE.

🔜 e² studio Setup	- D X
e <sup>²</sup> studio 7.8.0.R2020032	21-2326 Setup
Welcome	Review the information and click Install to continue.
<ul> <li>Install Folder</li> </ul>	Required Software that will be installed:
<ul> <li>Device Families</li> </ul>	Components that will be installed:
Extra Components	Renesas e2 studio (7.8.0.R20200321-2326) Renesas e2 studio Common Core (6.12.0.v20200305-1438)
<ul> <li>Components</li> </ul>	IAR Plugin Manager (1.1.0.201906261345)
<ul> <li>Additional Software</li> </ul>	GCC for Renesas RZ Build Support (7.8.0.v20200313-1056) Renesas RZ Debug Support (7.8.0.v20191127-2112)
<ul> <li>Licenses</li> </ul>	Renesas e2 studio Common Build (6.10.0.v20200313-1019) Renesas e2 studio Common Build for GCC Toolchains (6.8.2.v20200312-1245)
<ul> <li>Shortcuts</li> </ul>	Renesas e2 studio Memory Usage View (6.11.0.v20200224-1628) Renesas e2 studio Stack Analysis View (6.9.0.v20200305-0646)
🖒 Summary	Renesas e2 studio Common Debug (6.10.1.v20200312-1118) Renesas e2 studio ARM Common Debug (6.11.0.v20200312-0639)
O Installing	Renesas Debug Views (6.11.0.v20200312-0842) Renesas Smart Help (6.11.0.v20200206-0641)
Results	GCC for Renesas RZ Support Files (1.1.7.v20200311-1145) Renesas RZ Debug Support Files (1.1.1.0.v20200322-0311)
	Renesas R2 Debug Support Files (1.11.0./2020/0522-0511) Renesas e2 studio Optimization Assistant (1.5.6.v20191125-1406) Renesas e2 studio Conflicts Checker (6.0.1.v20191120-1446)
<u>v202003220126</u>	< <u>B</u> ack <u>N</u> ext > <u>Install</u> Cancel

Figure 2-21. Summary Page (32-bit e<sup>2</sup> studio)

#### 11. Installing...

The installation is performed. Depending on selected items of additional software, new dialog prompts may appear during the installation process. Please see chapter 2.2 for more detailed information.

#### 12. Results

Click the **OK** button to complete the installation.

e <sup>2</sup> studio 7.8.0.R20200321-2326 Setup
• Welcome        Installation of e2 studio is complete. Please click OK to close.             • Install Folder           _ Launch e2 studio?             • Device Families           _ Launch e2 studio?             • View Release Notes?           · View What's New?             • Components           · View What's New?             • Additional Software           · View What's New?             • Licenses           · View Release Notes?             • Shortcuts           · View What's New?             • Shortcuts           · View Release Notes?             • Shortcuts           · View What's New?             • Shortcuts           · View Release Notes?             • Shortcuts           · View What's New?             • Shortcuts           · View Release Notes?             • Shortcuts           · View Release Notes?             • Shortcuts           · View Release Notes?             • Installing           · View Release Notes?             • Notes           · View Release Notes?

Figure 2-22. Summary Page (32-bit e<sup>2</sup> studio)

### 2.3 Installation of Additional Software

The e<sup>2</sup> studio installer invokes other installers which were selected within the **Additional Software** pane (such as GNU Compilers or **Libgen Update for GNU ARM Embedded Toolchains**).

#### 2.3.1 GNU ARM Embedded Toolchain

If it was selected in the **Additional Software** pane of e<sup>2</sup> studio, you will see the installation wizard for the GNU ARM Embedded Toolchain during the installation process.

- 1. Click Next.
- 2. After reading the license agreement, click **I Agree**.
- 3. Select the destination folder. The default folder is recommended.
- 4. At the end of the installation, you can select options such as adding the path to the Windows environment variable, and adding registry information.

💮 GNU Tools for ARM Ember	dded Processors 6-2017-q2-update — 🗆 🗙	🌍 GNU Tools for ARM Embedded Processors 6-2017-q2-update – 🗌 🗙				
	Welcome to the GNU Tools for ARM Embedded Processors 6-2017-q2-update 6 2017 Setup	License Agreement Please review the license terms before installing GNU Tools for ARM Embedded Processors 6-2017-q2-update 6 2017.				
	Wizard This wizard will guide you through the installation of GNU	Press Page Down to see the rest of the agreement.				
	Tools for ARM Embedded Processors 6-2017-q2-update 6 2017.	Contains code from project GNU Binutils ( <u>https://www.qnu.org/software/binutils</u> ), GNU Debugger ( <u>https://www.qnu.org/software/gdb</u> ) under the following license(s).				
	It is recommended that you close all other applications before starting Setup. This will make it possible to update relevant system files without having to reboot your computer.	GNU GENERAL PUBLIC LICENSE Version 3, 29 June 2007				
	Click Next to continue.	Copyright (C) 2007 Free Software Foundation, Inc. < <u>http://fsf.org</u> /> Everyone is permitted to copy and distribute verbatim copies of this learned boundaring it is not allowed.				
		If you accept the terms of the agreement, click I Agree to continue. You must accept the agreement to install GNU Tools for ARM Embedded Processors 6-2017-q2-update 6 2017.				
		Nullsoft Install System v2.46-7				
	1 <u>Next</u> Cancel	< Back I Agree Cancel				
GNU Tools for ARM Ember	dded Processors 6-2017-q2-update — 🗆 🗙	🕞 GNU Tools for ARM Embedded Processors 6-2017-q2-update — 🗆 🛛				
6-2017-q2-update 6 2017.	install GNU Tools for ARM Embedded Processors	Completing the GNU Tools for ARM Embedded Processors 6-2017-q2-update 6 2017 Setup Wizard				
following folder. To install in a Install to start the installation	r ARM Embedded Processors 6-2017-q2-update 6 2017 in the different folder, dick Browse and select another folder. Click h.	GNU Tools for ARM Embedded Processors 6-2017-q2-update 6 2017 has been installed on your computer.				
		Click Finish to dose this wizard.				
Destination Folder		Show Readme       Constrained       Image: State of the state				
ogram Files (x86)¥GNU To	ools ARM Embedded¥6 2017-q2-update Browse	Add registry information				
Space required: 433.3MB Space available: 8.3GB						
Nullsoft Install System v2.46-7 -	3					

Figure 2-23. Installation of GNU ARM Embedded Toolchain



#### 2.3.2 Libgen Update for GNU ARM Embedded Toolchains

If it was selected in the **Additional Software** pane of  $e^2$  studio, you will see the installation wizard for the **Libgen Update for GNU ARM Embedded Toolchains** during the installation process. If you do not wish to update the GNU ARM Embedded Toolchain, terminate the **Libgen Update for GNU ARM Embedded Toolchains** by clicking the  $\times$  (close) button.

- 1. Click Click here to select your GNU ARM Embedded Toolchain installation folder.
- 2. Select the folder for the GNU ARM Embedded Toolchain to update.

😻 Libgen Update for GNU ARM Embedded Toolchains – 🗆 🗙
Libgen Update for GNU ARM Embedded Toolchains Version 1.2018.02 Copyright c 2017-2018 CyberThor Studios Ltd. All Rights Reserved. Click here to visit our support platform at gcc-renesas.com Click here to visit our website
This Libgen update installer will be making changes to your existing GNU ARM Embedded Toolchain installation(s). We recommend that you only apply this update to GNU ARM Embedded release 5-2016-q1-update or later, to avoid incompatibilities. <u>Click here to download the GNU ARM Embedded Toolchain installer(s)</u> [ Toolchain folder not selected. Please select the toolchain folder to continue. ] 1 Click here to select your GNU ARM Embedded Toolchain installation folder
Install Libgen Update for GNU ARM Embedded Toolchains
😻 Libgen Update for GNU ARM Embedded Toolchains – 🗆 🗙
Libgen Update for GNU ARM Embedded Toolchains         Version 1.201         Copyright c 20         Click here to vis         Click here to vis         This Libgen update installer will be recommend that you only apply         Click here         Click here

Figure 2-24. Libgen Update for GNU ARM Embedded: Select Toolchain Folder

3. Next, click Install Libgen Update for GNU ARM Embedded Toolchains.

🗢 Libgen Update for GNU ARM Embedded Toolchains	– 🗆 X
Libgen Update for GNU ARM Embedded Toolchair Version 1.2018.02 Copyright c 2017-2018 CyberThor Studios Ltd. All Rights	Reserved.
Click here to visit our support platform at gcc-renesas.com Click here to visit our website	
This Libgen update installer will be making changes to your existing GNU ARM Embed recommend that you only apply this update to GNU ARM Embedded release 5-20 incompatibilities. Click here to download the GNU ARM Embedded Toolchain in	16-q1-update or later, to avoid
C:\Program Files (x86)\GNU Tools ARM Embedded Toolchain installation Click here to select your GNU ARM Embedded Toolchain installation	017-q2-update
Install Libgen Update for GNU ARM Embedded To	oolchains
💗 Libgen Update for GNU ARM Embedded Toolchains	– 🗆 X
Libgen Update for GNU ARM Embedded Toolchair Version 1.2018.02 Copyright c 2017-2018 CyberThor Studios Ltd. All Rights Click here to visit our support platform at gcc-renesas.com	Reserved.
Click here to visit our website	
Libgen Update Completed X	
i Installation completed successfully!	
ОК	

#### Figure 2-25. Libgen Update for GNU ARM Embedded: Update Toolchain

If you want to update another toolchain, download **Libgen Update for GNU ARM Embedded** separately and run it (refer to Chapter 1.4).

## 2.4 Uninstallation of the e<sup>2</sup> studio IDE

The e<sup>2</sup> studio IDE can be uninstalled by following the usual steps to uninstall a program in a Windows OS.

- 1. Search for Apps & features in the Windows Search Box. Click on the search result to go to Apps & features.
- 2. From the currently installed programs list, choose 'e2 studio' and click the Uninstall button.
- 3. Click the **Uninstall** button again to confirm the deletion of e<sup>2</sup> studio.

At the end of the uninstallation, the e<sup>2</sup> studio IDE will be deleted from the installed location and Windows shortcut menus will be removed.

## 2.5 Major Version Upgrade for the e<sup>2</sup> studio IDE

The **Check for Updates** operation does not work for major version upgrades (a major digit version number increase, for example from version 6.3.0 to version 7.0.0), you need to use the e<sup>2</sup> studio installer for major upgrades. Please note that you should not overwrite the existing installation. Before installing the new version, you must uninstall the old one. However, to keep both the old and new IDE versions, you can install the new version to a different location.

## 2.6 Update e<sup>2</sup> studio plugins

The  $e^2$  studio IDE supports online and offline updates for minor version updates (i.e. minor digit number increases, for example going from version 7.3.0 to version 7.4.0). This allows the available software components to be updated over the internet or without an internet connection.

#### 2.6.1 Online Minor Version Update

This chapter shows how to perform a minor version update online:

1. From the Help menu, click Check for Updates to display the Available Updates panel.



Figure 2-26. Check for Updates Menu



2. All the software components are selected by default in the **Available Updates** panel. This allows you to update them all to the latest version (an example is shown in Figure 2-2). Click the **Next** button to proceed.

Name	Version	ld	^
🗹 🖗 GCC for Renesas RL78 Build Support	6.1.0.v20171011-0	com.renesas.e2studio.device.rl78.build.g	
🗹 🖗 GCC for Renesas RX Build Support	6.1.0.v20171010-0	com.renesas.e2studio.device.rx.build.gcc	
🗹 🖗 GCC for Renesas RX Support Files	1.1.0.v20171010-0	com.renesas.ide.supportfiles.rx.gcc.featu	
🗹 🖗 GCC for Renesas RZ Build Support	6.1.0.v20171011-1	com.renesas.e2studio.device.rz.build.gcc	
🗹 🖗 Renesas CCRL Build Support	6.1.0.v20170927-0	com.renesas.e2studio.device.rl78.build.re	
🗹 🖗 Renesas CCRL CS+ Import/Export	6.1.0.v20170927-0	com.renesas.e2studio.device.rl78.build.re	
🗹 🖗 Renesas CCRL Support Files	1.0.1.v20170912-0	com.renesas.ide.supportfiles.rl78.ccrl.feat	
🗹 🖗 Renesas CCRX Build Support	6.1.0.v20170913-1	com.renesas.e2studio.device.rx.build.ren	
🗹 🖗 Renesas CCRX CS+ Import/Export	6.1.0.v20170913-1	com.renesas.e2studio.device.rx.build.ren	
🗹 🖗 Renesas CCRX HEW Import	6.1.0.v20170913-1	com.renesas.e2studio.device.rx.build.ren	
Renesas CCRX Support Files	1.0.1.v20170912-0	com.renesas.ide.supportfiles.rx.ccrx.featu	
Select All Deselect All Details			0

Figure 2-27. e<sup>2</sup> studio – Available Updates Panel (1/3)

3. Select the **Next** button to continue the update.

Name	Version	ld é
GCC for Renesas RL78 Build Support	6.1.0.v20171011-0631	com.renesas.e2studio.device.rl78.b
> 🚯 GCC for Renesas RX Build Support	6.1.0.v20171010-0849	com.renesas.e2studio.device.rx.bui
GCC for Renesas RX Support Files	1.1.0.v20171010-0832	com.renesas.ide.supportfiles.rx.gc
GCC for Renesas RZ Build Support	6.1.0.v20171011-1311	com.renesas.e2studio.device.rz.bui
> 🌆 Renesas CCRL Build Support	6.1.0.v20170927-0303	com.renesas.e2studio.device.rl78.b
> 🚯 Renesas CCRL CS+ Import/Export	6.1.0.v20170927-0303	com.renesas.e2studio.device.rl78.b
🚯 Renesas CCRL Support Files	1.0.1.v20170912-0749	com.renesas.ide.supportfiles.rl78.c
> 🚯 Renesas CCRX Build Support	6.1.0.v20170913-1022	com.renesas.e2studio.device.rx.bui
> 🖗 Renesas CCRX CS+ Import/Export	6.1.0.v20170913-1022	com.renesas.e2studio.device.rx.bui
> 🖗 Renesas CCRX HEW Import	6.1.0.v20170913-1022	com.renesas.e2studio.device.rx.bui
🚯 Renesas CCRX Support Files	1.0.1.v20170912-0755	com.renesas.ide.supportfiles.rx.ccr
> 🚯 Renesas Debug Views	6.1.0.v20170928-0927	com.renesas.e2studio.debug.views
> 🚯 Renesas e2 studio	6.1.0.R20171011-1340	com.renesas.ide.e2studio.product
Reneras e2 studio ARM Common Debug	6.1.0.v20171003_0005	com reneras elstudio device arm
Size: 185,587 KB		
Details		

Figure 2-28. e<sup>2</sup> studio – Available Updates Panel (2/3)

4. Read and accept the software license agreement. Click the Finish button to complete the update.

icenses:	License text:
<ul> <li>A. HISTORY OF THE SOFTWARE</li> <li>Eclipse Foundation Software User Agreement</li> <li>license</li> <li>License for RENESAS e2 studio</li> <li>Renesas Pin Configurator</li> </ul>	Eclipse Foundation Software User Agreement April 9, 2014         Usage Of Content         THE ECLIPSE FOUNDATION MAKES AVAILABLE SOFTWARE, DOCUMENTATION, INFORMATION AND/OR OTHER MATERIALS FOR OPEN SOURCE PROJECTS (COLLECTIVELY "CONTENT").         USE OF THE CONTENT IS GOVERNED BY THE TERMS AND CONDITIONS OF THIS AGREEMENT AND/OR THE TERMS AND CONDITIONS OF LICENSE AGREEMENTS OR NOTICES INDICATED OR REFERENCED BELOW. BY USING THE CONTENT, YOU         I accept the terms of the license agreements         I do not accept the terms of the license agreements

Figure 2-29. e<sup>2</sup> studio – Available Updates Panel (3/3)

5. Click the Help  $\rightarrow$  About e<sup>2</sup> studio menu option to check the version number and verify the update.





#### 2.6.2 Offline Minor Version Update

This chapter shows how to update  $e^2$  studio using the update function of the installer.

1. Download the desired new version of e<sup>2</sup> studio offline installer from the following Renesas URL: http://www.renesas.com/e2studio\_download

# Note: Offline update using the 'Differential Update Program' is only applicable for e<sup>2</sup> studio version 3.X and below.

- 2. Double-click to run the installer file downloaded in step 1. The installer will detect the existing version and you can choose to upgrade the current version, or install the new version of e<sup>2</sup> studio to a different folder.
- 3. Click **Upgrade** followed by **Next** to start the update.



Figure 2-31. Upgrade e<sup>2</sup> studio from Offline Installer

4. Follow the steps shown in Chapter 2.1 **Installation of e<sup>2</sup> studio IDE**. Step 2 **Install Folder** will be skipped since the upgrade option will use the same destination folder as the existing e<sup>2</sup> studio installation.



## 2.7 Register Toolchain to e<sup>2</sup> studio

The  $e^2$  studio installer can install toolchains (such as GCC ARM Embedded) automatically during  $e^2$  studio installation with a valid Internet connection. However, in situations where an Internet connection is not available during  $e^2$  studio installation, toolchains that are needed can be installed later by using installer files from the web site and then registered to  $e^2$  studio. For the toolchain download site for RZ, please refer to chapter 1.3.

e<sup>2</sup> studio scans for installed toolchains on start up. After a new toolchain installation, e<sup>2</sup> studio will pop up a toolchain integration window to allow for toolchain selection.

Toolchain Integration <ol> <li>New toolchains available for integration</li> </ol>	C
Toolchain Registry	
Toolchain Type           V         Image: Constraint of the second seco	Installation Path
GCC ARM Embedded - 4.9.3.20150529	C:\Program Files (x86)\GNU Tools ARM E
<     Select all     Deselect all     Enable 'Toolchain integration' on startup	Register Cancel

Figure 2-32. e<sup>2</sup> studio Toolchain Integration

To check for installed compilers, click the **Integrate non-integrated toolchains** icon from the toolbar, or click **Help**  $\rightarrow$  **Add Renesas Toolchains** to open Renesas Toolchain Management as shown below. Check the desired toolchain to integrate it with e<sup>2</sup> studio.

<u>E</u> il	e <u>E</u> dit	<u>S</u> ource	Refactor	<u>N</u> avigate	Se <u>a</u> rch	<u>P</u> roject	Renesas <u>V</u> iews	<u>R</u> un	<u>W</u> indow	<u>H</u> el	p
4	*	•	Debug	~ 🖸 T	utorial H	ardware	Debu 🗠 🌞 💋	<b>-</b>	88	3	Welcome
										?	Help Contents
										22	Search
6											Show Contextual Help
											Show Active Keybindings Ctrl+Shift+L
											Cheat Sheets
										R	RenesasRulz Community Forum
										Ø	Add Renesas Toolchains
										49	Perform Setup Tasks
										<b>0</b> 2	Check for Updates
										₽.	Install New Software
											Renesas e2 studio feedback
										a	IAR Embedded Workbench plugin manager
										0	About e <sup>2</sup> studio

Figure 2-33. e<sup>2</sup> studio Add Renesas Toolchains



If the desired toolchain is not listed, Click Add... and specify the location where it is installed.

Renesas Toolcha 🔺	Scan for installed toolchains on startu	n
Smart Browser		•
Smart Configura	Disable warning if no toolchains are in	Istalled
> Smart Manual	Toolchain Type	Installation Path
Support Folders	✓ ☐ GCC ARM Embedded	
Tracealyzer	4.9.3.20150529	C:\Program Files (x86)\GNU Tools ARM Embedded\4.9 2015q3\
TraceX	KPIT GNUARM-NONE-EABI Tool	chain
Task Tags		
Template Default Va		
> Help		
> Install/Update > Java		
Library Hover		
> LinkerScript		
> MCU		
> Oomph		
> Remote Development		
Renesas QE		
> Run/Debug		
> Scripting		
> Team		
Terminal		
> Tracing 🗸 🗸	S	can Add Remove
?		Apply and Close Cancel
? (		
	is not already registered.	
(?) () y 0.5. 1.20 1.00.20 Integrate a new toolchain which	<b>is not already registered.</b> /12.02-ELF\rx-elf	
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX\</toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse DAUM
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse DAUM e-stamp_pro
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse For Folder
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse For Folder
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse For Folder
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse For Folder
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX. [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse Br
OSTILOTIOLO  Integrate a new toolchain which [GNU] e.g. <toolchain_path>\GNURX: [Renesas] e.g. <toolchain_path>\RX\1</toolchain_path></toolchain_path>	<b>is not already registered.</b> /12.02-ELF\rx-elf	Browse Browse DAUM e-stamp_pro fftp GNU Tools ARM Embedded A 9 2015q3 G 6 2017-q2-update Bin
OULTION OF CONTRACT OF CO	is not already registered. /12.02-ELFyrx-elf _2_1	Browse Browse DAUM e-stamp_pro fftp GNU Tools ARM Embedded A 9 2015q3 Gournel Cancel
OULTION OF CONTRACT OF CO	is not already registered. 1/2.02-ELPrx-elf _2_1	Browse Browse Cancel Cancel Browse DAUM Cancel Canc
	is not already registered. 1/2.02-ELPrx-elf _2_1	Browse Browse Cancel Cancel Browse Browse Browse Browse Browse Browse Browse Browse Browse DAUM Cancel Browse Browse Browse Browse DAUM Cancel Browse Browse Browse Browse Browse DAUM Cancel C
	is not already registered. 1/2.02-ELFyrx-elf 2_1 OK M Add Remove	Browse Browse Cancel Cancel Browse DAUM Cancel Canc

Figure 2-34. Toolchain Management: Register New Toolchain


# 3. Import / Generate Project

This chapter describes how to import existing projects (such as sample code in software packages) and how to generate a new project.

# Note: Multi-byte characters cannot be used for the e<sup>2</sup> studio installation folder name, project name, project folder, or source file names.

# 3.1 Import Existing Project

Renesas provides various useful sample projects in the RZ device software package. These sample projects can easily be imported into e<sup>2</sup> studio. You can also import your own projects this way.

- 1. Download the software package.
- Open the Import dialog with a click on "Import projects..." link in Project Explorer (if there is no projects in the workspace) or select the menu File → Import.

	<u>F</u> ile	<u>E</u> dit <u>S</u> ource Refactor <u>N</u> aviga	ite Se <u>a</u> rch <u>P</u> roject Rer
	<b>C</b> 3	New Open File Open Projects from File System Recent Files	Alt+Shift+N >
		Close Close All	Ctrl+W Ctrl+Shift+W
Project Explorer ≅		Save Save As Save All	Ctrl+S Ctrl+Shift+S
There are no projects in your workspace. To add a project:		Revert Move	
Create a new Makefile project in a directory containing existing code Create a new C or C++ project	\$	Rename Refresh	F2 F5
<ul> <li>☐ <u>Create a project</u></li> <li><u>Import projects</u></li> </ul>	Ð	Convert Line Delimiters To Print	> Ctrl+P
		Import Impor	t
		Properties	Alt+Enter

Figure 3-1. Import Menu



### 3. Select General → Existing Projects into Workspace and click Next.

4. Select the folder or project archive file then click **Browse** and select the project folder in the workspace.

Select Create new projects from an archive file or dire	Import Projects Select a directory to search for existing Eclipse projects.	
Select an import wizard:	Select root directory:          Select archive file:       ph\Downloads\an_r01an2511eg0200_rza1h_rsk.zip	B <u>r</u> owse B <u>r</u> owse
<ul> <li>Existing Projects into Workspace</li> <li>File System</li> <li>GNUARM-NONE/RZ(DS-5) project co</li> <li>Preferences</li> <li>Projects from Folder or Archive</li> <li>Rename &amp; Import Existing C/C++ Proj</li> <li>Renesas CCRX project conversion to R</li> <li>Renesas CS+ Project for CA78K0R/CA1</li> <li>Renesas GS+ Project for CC-RX and CO</li> <li>Renesas GitHub Amazon FreeRTOS Pro</li> <li>C/C++</li> <li>Install</li> </ul>	Projects: Projects: RZ_A1H_Tutorial_RSK (an_r01an2511eg0200_rza1h_rsk/Workspace/RZ_ ^ RZ_A1H_USB_Host_Func_RSK (an_r01an2511eg0200_rza1h_rsk/Workspace/RZ_A RZ_A1H_VDC5_RSK (an_r01an2511eg0200_rza1h_rsk/Workspace/RZ_A <b>Options</b> Search for nested projects Copy projects into workspace Clgse newly imported projects upon completion Hide projects that already exist in the workspace Working sets Add project to working sets Working sets:	Select All Deselect All Refresh New Select
⑦ < <u>B</u> ack <u>N</u> ext >	? < <u>B</u> ack <u>N</u> ext > <u>Finish</u>	Cancel

Figure 3-2 Select Folder or Archive File Included Project Files

 Projects generated by legacy e<sup>2</sup> studio (before V6.0) will need to be upgraded. Click the **Project Upgrade Required** pop-up message (continued on the next page).



Figure 3-3. Project Upgrade



Alternatively, run **Upgrade Legacy e2 studio Projects...** from the project right-button context menu.



Figure 3-4. Manual Project Upgrade

6. Select the projects to upgrade and click **Finish** (Figure 3-5).

Upgrade Legacy e2 studio Projects Select projects to upgrade	
RZ_A1H_Tutorial_RSK [Relea	
? <u>Einish</u> Cancel	

Figure 3-5. Migration Required Dialog Box



### 3.2 New Project Generation

When creating a new project, the method of creating the project differs between RZ/A2M and other RZ groups. This chapter explains how to generate a project for each device.

### 3.2.1 Project Generation of RZ/A2M

This chapter illustrates an example when using the RZ/A2M Evaluation Board Kit. The RZ/A2M project type can be selected from 'Loader Project' or 'Application Project'. On the RZ/A2M, both types of project need to be built and downloaded when debugging.

A 'Loader Project' is a project for generating a loader program, and an 'Application Project' is a project for creating a user application program. A 'Loader Project' sets the serial flash memory of the RZ/A2M Evaluation Board Kit so that it can be accessed at high speed, and then branches to the 'Application Project' (if the loader program has not been downloaded, the application program cannot be executed).

When using it on your own system, change the program according to the specifications of the serial flash memory being used.

To create a new project, launch the e<sup>2</sup> studio IDE from the Windows start menu and specify a workspace directory.

1. Click File  $\rightarrow$  New  $\rightarrow$  C/C++ Project to open the New Project Creation Wizard.

File	Edit	Source	Refactor	Navigate	Search	Project	Renes	as Vie	ws Run	Window	Help	
	New					Alt+Shif	t+N>	<b>C</b>	RZ Linux	C/C++ proj	ject	
	Open	File						C++	Makefile	Project with	h Existing Code	
۵,	Open	Projects f	rom File Sy	stem				Ċ	C/C++ F	Project		
	Close					Ctr	l+W		Project			

Figure 3-6. Open New Project Creation Wizard



 Select the template for the new project (for example Renesas RZ: GCC for Renesas RZ C/C++ Executable Project). Click Next to proceed.

Templates for New	r C/C++ Project
Make	GNU for Renesas RZ C/C++ Executable Project A C/C++ Executable Project for Renesas RZ using the GNU ARM Embedded toolchain.
Renesas Debug Renesas RE Renesas RL78 Renesas RX Renesas RZ	GNU for Renesas RZ C/C++ Library Project A C/C++ Library Project for Renesas RZ using the GNU ARM Embedded toolchain.
0	< <u>B</u> ack <u>N</u> ext > <u>F</u> inish Cancel

Figure 3-7. Select Project Template

3. First, create a loader project. Enter the project name ('Loader'). Click **Next** to proceed.

New GCC for Renesas RZ Executable Project New GCC for Renesas RZ Executable Project Design teacher								
Project name: Loader								
Location: C:¥Workspace¥Loader Browse Create Directory for Project Choose file system: default ✓								
Working sets       New         Working sets:       ~         Select								
Sack Next > Finish Cancel								

Figure 3-8. Specify Project Name



- 4. Select the following settings:
  - Language: 'C'
  - Toolchain: 'GCC ARM Embedded'
  - Toolchain Version: '6.3.1.20170620'
  - Target Device: 'R7S921053'
  - Project Type: 'Loader Project'

Select Create Hardware Debug Configuration and then J-Link ARM. Click Next to proceed.

New GCC for Renesas RZ Executable Proj Select toolchain, device & debug settings	ect	Ź
Toolchain Settings Language: Toolchain: Toolchain Version: 6.3.1.20170620	v v Toolchains	
RTOS: None RTOS Version:	✓	
Device Settings Target Device R7S921056	Configurations Coeate Hardware Debug Configurations Create Hardware Debug Configurations Cock Devices	iration ~
Endian: Little Project Type: Loader Project	Create Release Configuration	
⑦ < Back	Next > Einish Cancel	I

### Figure 3-9. Select Toolchain, Device & Debug Settings: e.g. RZ/A2M Renesas Starter Kit

5. Click **Next** to proceed because Loader Projects do not support Smart Configurator.

New GCC for Renesas RZ Executable Project     Image: Coding Assistant Settings
Use Smart Configurator Use Peripheral Code Generator The e2 studio peripheral code generator and smart configurator automatically generates programs (device drivers) for MCU peripheral functions (clocks, timers, serial interfaces, A/D converters, DMA controllers, etc.) based on settings entered via a graphical user interface (GUI). Functions are provided as application programming interfaces (APIs) and are not limited to initialization of peripheral functions.
Image: Second

Figure 3-10. Select Coding Assistant Settings



6. In the initial settings, USB and RTC are not used. In 'Loader Project', click Next to proceed.

New GCC for Renesas RZ Executable Project Select Peripheral Usage Options	
Select Peripheral Usage Options Use USB0 peripheral Use USB1 peripheral Use RTC0 peripheral Use RTC1 peripheral	
Output Search	

Figure 3-11. Select Peripheral Usage Options

7. Keep default settings for additional CPU options and click **Next** to proceed.

<b>New GCC for Renesas</b> Select Additional CPU Op	-	t	Ď
Select Additional CPU Op			
Instruction Set	Arm		~
Target FPU	neon		~
Floating Point ABI	Hard		~
Warn if stack size exceeds	the limit, in bytes 100		
? < <u>B</u> ack	<u>N</u> ext >	<u>F</u> inish	Cancel

Figure 3-12. Select Additional CPU Options



8. Select 'Project-Built' and click Next to proceed.

<ul> <li>○ Optimized         <ul> <li>○ Optimized</li> <li>○ Newlib</li> </ul> </li> <li>Use this for further code size optimization.</li> <li>Select Library Type:             <ul> <li>○ Project-Built</li> <li>○ Pre-Built</li> <li>○ Pre-Built</li> </ul> </li> </ul> <li>Build the library with user specified options</li>	○ Optimized     ● Newlib       Use this for further code size optimization.       Select Library Type:       ● Project-Built	New GCC for Renesas RZ Executable Select library generator settings	e Project	2
Project-Built     O Pre-Built	Project-Built     O Pre-Built	O Optimized	-	
		Project-Built	-	

Figure 3-13. Library Generator Settings: Select Project Built

9. A project summary is displayed. Click **Finish** to generate the project.

	C for Renesas RZ Ex y of project "Loader"	ecutable P	roject			Ď
Toolchai Files add gene gene gene	t7S921053 n Version: 6.3.1.201706 ed: rate/system/ rate/compiler/ rate/drivers/ rate/system/ rate/linker_script.ld	20				~
?			< <u>B</u> ack	<u>N</u> ext >	<u> </u>	Cancel

Figure 3-14. New Project Creation Wizard (Summary)

10. A new C project named 'Loader' will be created.

* • • • • • • • • • • • • • • • • • • •	Project Explorer ⊠       Image: r_startup_config.h ≥       Im			<u>×∲</u> :∥:⊐ ▼ 🛛 🗟 ♥ 🔦 ▼ 🖩	¥ : ♥ : ♥ ♥ ♥ ₩ ₩ ₩ % %) & : '		
Project Explorer with the D block with the	Project Explorer 50       Image: Contrigut and percenting in the percenting in t	▼ 隆 ▼ 🥭 🛷 ▼ 🚺 🖗 🗐 🗉 🖗 🖛				ୟ 🖻 🗟 C/	/C++ 🎋 Debu
<sup>©</sup> Loader [HardwareDebug] <sup>24</sup> * File Name : r_startup_config.h <sup>#</sup> R_STARTUP_CONFIG_H <sup>&gt;</sup> <sup>©</sup> generate <sup>27</sup> <sup>•</sup> <sup>#</sup> ifndef R_STARTUP_CONFIG_H <sup>#</sup> R_STARTUP_CFG_DISA <sup>&gt;</sup> <sup>©</sup> generate <sup>20</sup> /* example of configured macro definitions */ <sup>#</sup> STARTUP_CFG_DISA <sup>&gt;</sup> <sup>©</sup> compiler <sup>30</sup> /* example of configured macro definitions */ <sup>#</sup> STARTUP_CFG_DISA <sup>&gt;</sup> <sup>©</sup> compiler <sup>30</sup> /* select whether RTC0(secure) use or not <sup>#</sup> STARTUP_CFG_DISA <sup>©</sup> drivers <sup>32</sup> ·* /* select whether RTC0 (do not touch RTC0 in startup <sup>#</sup> STARTUP_CFG_DISA <sup>©</sup> system <sup>34</sup> ·* 1: Not use RTC0 (startup routine disable: <sup>#</sup> STARTUP_CFG_PRO <sup>©</sup> src <sup>(</sup> * select <sup>*</sup> /* <sup>*</sup> (* startup routine disable:	24       ** File Name       : r_startup_config.h         >        >        >        * File Name       : r_startup_config.h         >        >        >        >        *          >        >        >        >        *        *          >        >        >        >        *        *        *          >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >        >	Project Explorer ≈ □	r_startup_config.h	×		🗄 Outline 🛛	- E
Problems Console 24 to Smart Browser Memory Usage Memory		<ul> <li>➡ ✿ ♥ ●</li> <li>► Loader [HardwareDebug]</li> <li>&gt; Description</li> <li>&gt; Descr</li></ul>	2 24 27 28 29 30 31 32 33 34 35	<pre>** DISCLAIMER[] ** File Name : r_startup #define R_STARTUP_CONFIG_H #define R_STARTUP_CONFIG_H /* example of configured m */* select whether RTC0(sec * 0 : Use RTC0 (do not to * 1 : Not use RTC0 (start */ &lt;</pre>	_config.h] acro definitions *, ure) use or not uch RTC0 in startu; up routine disable; >	<ul> <li># R_STAR</li> <li># STARI</li> <li></li> </ul>	RTUP_CONFI TUP_CFG_DISA TUP_CFG_DISA TUP_CFG_DISA TUP_CFG_DISA TUP_CFG_USB TUP_CFG_BOC TUP_CFG_PRO >

Figure 3-15. New C Project Created

11. Next, create an Application project.

Click File  $\rightarrow$  New  $\rightarrow$  C/C++ Project to open the New Project Creation Wizard.

File	Edit	Source	Refactor	Navigate	Search	Project	Renes	as Vie	iews Run Window Help
	New					Alt+Shi	ft+N >	C	RZ Linux C/C++ project
	Open	File						C#	Makefile Project with Existing Code
	Open	Projects f	rom File Sy	stem				C	C/C++ Project
	Close					Ctr	l+W	Ċ	Project

Figure 3-16 Open New Project Creation Wizard



12. Select the template for the new project (for example, Renesas RZ: 'GCC for Renesas RZ C/C++ Executable Project'). Click **Next** to proceed.

All Make Renesas Debug	GCC for Renesas RZ C/C++ Executable Project A C/C++ Executable Project for Renesas RZ using the GCC ARM Embedded toolchain.					
Renesas RL78 Renesas RX Renesas RZ Renesas Synergy	GCC for Renesas RZ C/C++ Library Project A C/C++ Library Project for Renesas RZ using the GCC ARM Embedded toolchain.					
0	< Back Next > Finish Cancel					

Figure 3-17. Select Project Template

13. Now, create the Application Project. Enter the project name ('Application'). Click Next to proceed.

Use defau	t location		
Location:	C:¥Workspace¥Application	В	rowse
Working sets	Create Directory for Project tem: default v ct to working sets	Ν	ew
Working sets			lect

Figure 3-18. Specify Project Name

14. Select the following settings.

- Language: 'C' or 'C++'
- Toolchain: 'GCC ARM Embedded'
- Toolchain Version: '6.3.1.20170620'
- Target Device: 'R7S921053'
- Project Types: 'Application Project'

Select Create Hardware Debug Configuration and then J-Link ARM. Click Next to proceed.

Toolchain Settir Language: Toolchain:	C      C++ GNU ARM Embedded	~	
RTOS: RTOS Version:	on: <mark>6.3.1.20170620 <u>Manage To</u> None</mark>		
Device Settings Target Device:	R7S921053	Configurations Create Hard Devices J-Link ARM	ware Debug Configuration ~
Endian:   Project Type:			se Configuration

Figure 3-19. Select Toolchain, Device & Debug Settings: e.g. RZ/A2M Renesas Starter Kit



15. The Coding Assistant feature can be selected if required.

#### Note:

• *Smart Configurator* supports a single user interface that combines the functionalities of Code Generator and FIT Configurator. Smart Configurator encompasses a unified clock configuration view, an interrupt configuration view and a pin configuration view.

Select Use Smart Configurator, click Next to proceed.

New GCC for Renesas RZ Executable Project Select Coding Assistant Settings	
Use Peripheral Code Generator ▲         The e2 studio peripheral code generator and smart configurator automatically generates programs (device drivers) for MCU peripheral functions (clocks, timers, serial interfaces, A/D converters, DMA controllers, etc.) based on settings entered via a graphical user interface (GUI). Functions are provided as application programming interfaces (APIs) and are not limited to initialization of peripheral functions.         User Application       User Application         Driver and Middleware       FIT Modules         Configured in GUI and Generated       Selected in GUI and Imported         MCU Hardware       MCU Hardware         MCU Hardware       MCU Hardware	
(?) < <u>B</u> ack <u>N</u> ext > <u>Finish</u> Cancel	

Figure 3-20. Select Coding Assistant Settings

16. In the initial settings, USB and RTC are not used. When using USB and RTC, select the peripheral module / channel to be used.

New GCC for Renesas RZ Executable Project Select Peripheral Usage Options	
Select Peripheral Usage Options	
Use USB0 peripheral	
Use USB1 peripheral	
Use RTC1 peripheral	
< Back         Next >         Finish         Cancel	





17. Keep the default settings for additional CPU options and click **Next** to proceed.

New GCC for Renesas RZ Executable Project         Select Additional CPU Options								
Select Additional CPU Options	Arm							
Target FPU	neon	~						
Floating Point ABI Warn if stack size exceeds the limit, in bytes	Hard 100	~						
⑦ < <u>B</u> ack <u>N</u> ext >	<u>Einish</u> Cancel							

Figure 3-22. Select Additional CPU Options

### 18. Select **Project-Built** and click **Next** to proceed.

New GCC for Renesas RZ Executable Project Select library generator settings
Select Library Source:         O Optimized         Use this for further code size optimization.
Select Library Type: Project-Built Build the library with user specified options
?       < Back     Next >     Einish     Cancel

Figure 3-23. Select Library Generator Settings



19. A project summary is displayed. Click Finish to generate the project.

New GCC for Renesas RZ Executable Summary of project "Application"	Project				
Device: R75921053 Toolchain Version: 6.3.1.20170620 Files added: generate/system/ generate/comfiguration/ generate/configuration/ generate/drivers/ generate/s_drivers/ generate/s_drivers/ generate/system/ generate/linker_script.ld					
?	< <u>B</u> ack	Next >	<u>F</u> inish	Cancel	

Figure 3-24. New Project Creation Wizard (Summary)

### 20. Click Open Perspective.



### Figure 3-25. Open Associated Perspective Wizard



21. A new C project named 'Application' is created.

<u>Eile E</u> dit <u>N</u> avigate Se <u>a</u> rch <u>P</u> roject Renesas <u>V</u> iew	s <u>R</u> un <u>W</u> indow <u>H</u> elp							
🔦 🛧 🔳 🌾 Debug 🛛 🗸 🖻 Application H	ardwareC 🗸 🌞 🗄 🖋 🗟 🐨 🗮 🕼   😒	▼ 🔨 ▼ 📸 🗄 0 <sub>0</sub> ▼ 🎭	01 IO 18 3	* 🕺 🍐	<b>-</b> 9	· ▼		
				۹ 🖻 🖬	C/C+	++ 🎋 Debug 📓 Smart Configurator		
Project Explorer ≈ 🛛 🖻 🕏 🍸 🕴 🗖 🗖	Application 🛛 🖻 r_startup_co.	🖻 Application	<b>»</b> 1	-	° 🗆	■ MCU Pa ※ ■ MMU L		
<ul> <li>✓          <sup>™</sup> Application         <ul> <li>→</li></ul></li></ul>	<b>Overview information</b>			C	1 🖻			
✓ <sup>⊕</sup> generate	<ul> <li>General Information</li> </ul>			?	^			
<ul> <li>&gt; compiler</li> <li>&gt; configuration</li> </ul>	> > configuration							
<ul> <li>&gt; Image: constraint of the second seco</li></ul>	Clocks Allow clock configuration							
<ul> <li>inker_script.ld</li> <li>e src</li> <li>e renesas</li> </ul>	Components Allow software component selec	ion and configuration	I		1			
<ul> <li>&gt; ≥ user_prog</li> <li>Poplication HardwareDebug.launch</li> <li>Application.scfg</li> </ul>	Pins Allow general pin configuration a	nd pin configuration f	for selecte	d software c	)			
> 😂 Loader	MMU							
	Allow MMU configuration Overview Clocks Components Pir				~			
Console      X		Configuration Prob	lems 🛛			Д 8 — П		
No consoles to display at this time.		0 items Description	/	`		Туре		

### Figure 3-26. New C Project Created

This project consists of an application file Application.c and a set of automatically generated files. All these project and source files are listed in the **Project Explorer** panel reflecting the folder structure of the project, just as seen in the standard file explorer.

### Notes for backing up projects:

- Project properties are stored in files or folders with filenames or folder names that are prefixed with a '.' (dot), for example '.project'. It is necessary to include these files or folders when archiving the project for backup purposes.
- In order to restore properties shared among projects, for instance when one project makes reference to another project's files, please backup the whole workspace folder.



### 3.2.2 Project Generation for RZ groups (RZ/A1, RZ/T)

This chapter illustrates an example when using the RZ/A1H Renesas Starter Kit.

The created project runs within the large-capacity internal RAM of the RZ/A1. When executing with flash memory, change the program according to the specifications of the serial flash memory and memory placement.

*RZ/T* project generation is almost same with RZ/A1H.

For RZ/G, e<sup>2</sup> studio supports **RZ Linux C/C++ project** for developing Linux application. The **Support for RZ/G** option has to be selected during e<sup>2</sup> studio installation. Refer to **Figure 2-2**.

To create a new project, invoke e<sup>2</sup> studio IDE from the Windows Start menu and specify a workspace directory.

1. Click **File**  $\rightarrow$  **New**  $\rightarrow$  **C/C++ Project** to open the new project creation wizard.

Fi	le	Edit	Source	Refactor	Navigate	Search	Project	Renes	as Vie	ws Run Window Help
		New					Alt+Shi	ft+N >	C	RZ Linux C/C++ project
		Open	File						C++	Makefile Project with Existing Code
	2	Open	Projects f	rom File Sy	stem				C	C/C++ Project
		Close					Ctr	l+W		Project

Figure 3-27. Open New Project Creation Wizard

 Select template for the new project (for example, Renesas RZ: 'GCC for Renesas RZ C/C++ Executable Project'). Click Next to proceed.

Templates for No	ew C/C++ Project
All Make Renesas Debug	GCC for Renesas RZ C/C++ Executable Project A C/C++ Executable Project for Renesas RZ using the GCC ARM Embedded toolchain.
Renesas RL78 Renesas RX	GCC for Renesas RZ C/C++ Library Project C/C++ Library Project for Renesas RZ using the GCC ARM Embedded toolchain.
?	< <u>B</u> ack <u>N</u> ext > <u>F</u> inish Cancel

Figure 3-28. Select Project Template



3. Enter the project name. Click Next to proceed.

	w GCC for Renesas RZ Executable Project	
Pro	oject name	
	Use <u>d</u> efault location	
Lo	cation: C:\Renesas\Workspace\v2020_04\Tutorial	B <u>r</u> owse
Ch	oose file system: default	
W	orking sets	
	Add projec <u>t</u> to working sets	Ne <u>w</u>
V	/ <u>o</u> rking sets: ∽	S <u>e</u> lect
(?	Seck Next > Einish	Cancel

Figure 3-29. Specify Project Name

Select the following settings.

For example using the RZ/A1H Renesas Starter Kit:

- Language: 'C'
- Toolchain: 'GCC ARM Embedded'
- Toolchain Version: '4.9.20150529'
- Target Device: 'R7S721001'

Select Create Hardware Debug Configuration and then J-Link ARM. Click Next to proceed.

Figure 3-30. Select toolchain, device & debug settings: e.g. RZ/A1H Renesas Starter Kit

4. The Coding Assistant feature can be selected if required.

#### Note:

- The *Peripheral Code Generator (CG)* supports the generation of driver and peripheral function code based on GUI settings. Functions are provided as APIs and are not limited to peripheral initialization.
- Smart Configurator supports a single user interface that combines the functionalities of Code Generator and Middleware Configurator. Smart Configurator encompasses a unified clock configuration view, an interrupt configuration view and a pin configuration view.
- 5. Peripheral Code Generator and Smart Configurator may not be available for some devices.
- 6. Click Next to proceed.



Figure 3-31. Select Coding Assistant Settings

7. Keep the default settings for additional CPU options and click **Next** to proceed.

	New GCC for Renesas RZ Executa Select Additional CPU Options	ble Project	
	Select Additional CPU Options		
	Instruction Set	Arm	~
	✓ Inter Working		
	Target FPU	vfpv3-d16	~
	Floating Point ABI	Hard	~
	Warn if stack size exceeds the limit (H')	100	
	? < <u>B</u> a	ck <u>N</u> ext > <u>Finish</u> Ca	ancel

Figure 3-32. Select Additional CPU Options

#### e<sup>2</sup> studio Getting Started Guide

8. If the selected compiler was not updated by Libgen Update for GNU ARM Embedded Toolchains, a warning message about the Library Generator will be shown in the next dialog box. Without the Library Generator, there may be project build errors during compilation.

Refer to chapter 2.2.2 and run Libgen Update for GNU ARM Embedded Toolchains.

Figure 3-33. Library Generator Settings Warning Message

9. A project summary is displayed. Click Finish to generate the project.

New GCC for Renesas RZ Executable Project	
Summary of project "Tutorial"	
Device: R7S721001	^
Toolchain Version: 7.3.1.20180622	
Files added:	
generate/start.S	
generate/inthandler.c	
generate/interrupt_handlers.h	
generate/typedefine.h	
generate/vects.c	
generate/hwsetup.c generate/iodefine.h	
generate/linker_script.ld	
generate/iodefines/	
<u> </u>	
	¥
? < Back Next > Einish	h Cancel
	Cancer

Figure 3-34. New Project Creation Wizard (Summary)

10. A new C project named 'Tutorial' will be created.



### Figure 3-35. New C Project Created

This project consists of an application file Tutorial.c and a set of automatically generated files. All these project and source files are listed in the **Project Explorer** panel reflecting the folder structure of the project, just as seen in the standard file explorer.

### Notes for backing up projects:

- Project properties are stored in files or folders with filenames or folder names that are prefixed with a '.' (dot), for example '.project'. It is necessary to include these files or folders when archiving the project for backup purposes.
- In order to restore properties shared among projects, for instance when one project makes reference to another project's files, please backup the whole workspace folder.



# 4. Build

This chapter describes the build configurations and key build features of the e<sup>2</sup> studio IDE.

# 4.1 Build Option Settings

A new project generated with default options should run well, but it is possible to change build options with the following configuration dialogs.

1. Right click on project 'Tutorial' and select **Properties** or use shortcut keys **Alt** + **Enter** to open the **Properties** window.

The **Properties** window is supported at the workspace, project and source level. The **Properties** window for project supports more configurations which apply across all the files within the same project workspace.



Figure 4-1. Opening the Properties Window



2. Click C/C++ Build  $\rightarrow$  Settings  $\rightarrow$  Toolchain to view or change the toolchain version.

type filter text	Settings 🗢 🔻 🔿 🔻
<ul> <li>Resource</li> <li>Builders</li> <li>C/C++ Build</li> <li>Build Variables</li> </ul>	Configuration: HardwareDebug [Active]
Environment Logging Settings Tool Chain Editor > C/C++ General > MCU Project References Renesas OF	<ul> <li>Tool Settings Toolchain</li> <li>Device Build Steps Build Artifact Binary Parsers For Parsers</li> <li>Current Toolchain</li> <li>Toolchain: GCC ARM Embedded</li> <li>Version: 4.9.3.20150529</li> <li>Change Toolchain</li> <li>Toolchain: GCC ARM Embedded</li> </ul>
Run/Debug Settings	Version: 4.9.3.20150529   Additional Tools  Create Library generator  Create Flash image
?	Apply and Close Cancel

### Figure 4-2. Change Toolchain Version

 Click C/C++ Build → Environment to select the current build configuration and add or edit environment variables.

type filter text	Environment			⇔ ▼ ⇔ ▼ ▼
> Resource				
Builders	Configuration: Hardw	uaraDahug [Activa]	Managa Ca	nfigurations
✓ C/C++ Build	Configuration.		ivialiage CO	inigurations
Build Variables				
Environment	Environment variables	to set		
Logging				Add
Settings	Variable	Value	Origin	Select
Tool Chain Editor	AMS_KEEP_FILE	\${synergyKeepFile}	USER: PREFS	Edit
> C/C++ General	AMS_LICENSE_PATH	\${synergyLicenseFile}	USER: PREFS	Eult
> MCU	CWD	C:\Renesas\Workspace\e2_v700\Tutorial\HardwareDebug	BUILD SYSTEM	Delete
Project References	GCC_VERSION	4.9.3	BUILD SYSTEM	Undefine
Renesas QE	PATH	C:\Program Files (x86)\GNU Tools ARM Embedded\4.9 2015q3\	BUILD SYSTEM	ondenne
Run/Debug Settings	PWD	C:\Renesas\Workspace\e2_v700\Tutorial\HardwareDebug	BUILD SYSTEM	
> Task Repository	TCINSTALL	C:\Program Files (x86)\GNU Tools ARM Embedded\4.9 2015q3\	BUILD SYSTEM	
	TC_VERSION	4.9.3.20150529	BUILD SYSTEM	
	<ul> <li>Append variables t</li> </ul>	to native environment		J
	○ Replace native envi	ironment with specified one		
		Re	store <u>D</u> efaults	<u>A</u> pply
0		Appl	y and Close	Cancel

Figure 4-3. Build Settings for Compiler: Environment

### e<sup>2</sup> studio Getting Started Guide

4. The build option allows user to retain all the toolchain configuration settings, including the path name specified by the environment variables. The current build configuration is **HardwareDebug**, as shown in Figure 4-3.

Build option details are described in the compiler user manual which is stored at {Compiler installation directory}\share\doc. For example, it can be found in C:\Program Files (x86)\GNU Tools ARM Embedded\4.9 2015q3\share\doc.

*Note:* As seen above, there is a **Tool Chain Editor** setting under **C/C++ Build**, but please do not change the configuration in this option. The **Tool Chain Editor** is used for toolchains which are NOT supported by Renesas build support plugins.

# 4.2 Building the Sample Project

A project can be built by one of the ways below:

- 1. Right click on the project and select Build Project
- 2. Click on the project to set focus and select Project → Build Project
- 3. Click on the project to set focus and click on the S icon
- 4. Click on the project to set focus and press Ctrl + B

	<u> </u>	14500 04		
<u>File</u> <u>E</u> dit <u>S</u> ource <u>Refactor</u> <u>N</u> av	×	Delete De	lete	<u>H</u> elp
K 😻 🔳 🎋 Debug 🕥		Source	>	▼ %
		Move		
Project Explorer 🛛		Rename	F2	
> 😕 Tutorial [HardwareDebug	2	Import		ОЈЕСТ
	⊿	Export		typed iodef
		Build Project		Todel
	Ł	Clean Project Refresh Close Project Close Unrelated Project	15	APP ze gl dm
		Build Targets Index Build Configurations	> >	nt in itial def v
	0	Run As	>	rn pf
	*	Debug As		rn_pf
		Profile As	>	c *p;
		Team	>	ializ
		Compare With		(p =
		Restore from Local History		();
			1	

Figure 4-4. Building the Sample 'Tutorial' Project

5. The **Console** pane shows a **Build Finished** message to indicate a successful build. At the end of this build, files output to the \${CWD} directory consists of makefile, Tutorial.elf, Tutorial.map, Tutorial.hex, etc.

6. Tutorial.elf is the standard load module in ELF/DWARF format used for debugging.



Figure 4-5. Project Has Built Successfully

# 4.3 Exporting Build Configuration Settings

The **Project Reporter** feature can export project and build configuration settings from the e<sup>2</sup> studio IDE to a file for easy checking and comparison of project/build environment settings.

- 1. Right-click in the **Project Explorer** pane to bring up the context menu
- 2. Select **Save build settings report** to save the build settings report

<u>File Edit Source Refactor Navi</u>	2	Export		
<ul> <li> <p< td=""><td>S.</td><td>Build Project Clean Project Refresh Close Project Close Unrelated Project</td><td>F5</td><td></td></p<></li></ul>	S.	Build Project Clean Project Refresh Close Project Close Unrelated Project	F5	
> 🥔 generate > 🔂 src > 🔂 HardwareDebug 📄 custom.bat		Build Targets Index Build Configurations	> > >	
<ul> <li>Description.txt</li> <li>Tutorial HardwareDebug</li> <li>Tutorial HardwareDebug.</li> <li>Tutorial Release.launch</li> </ul>	0 *	Run As Debug As Profile As Team	> > >	
		Compare With Restore from Local History MISRA-C	>	1
	\$ \$	Save build settings report         Change Device         C/C++ Project Settings         Run C/C++ Code Analysis	Ctrl+Alt+P	

Figure 4-6. Project Reporter



# 5. Debug

This chapter describes the operation of the debug configuration and key debugging features of e<sup>2</sup> studio. The following illustration refers to 'Tutorial' project built (in Chapter 4.2) and is based on the hardware configuration of a Segger J-Link emulator and an RSK RZ/A1H board.

To open the specific perspective used for debugging, open 'Tutorial' project workspace in  $e^2$  studio IDE and select the **Debug** perspective.



Figure 5-1. Switch to Debug Perspective

A perspective defines the layout of views (related to development tools) on the Workbench. Each perspective consists of a combination of views, menus and toolbars that enable user to perform a specific task.

For instance, the C/C++ perspective has views that help user to develop C/C++ programs and the **Debug** perspective has views that enable user to debug the program. If you attempt to connect to the debugger in the C/C++ perspective, then the IDE will then prompt you to switch to the **Debug** perspective.

One or more perspectives can exist in a single Workbench window. Each perspective can be customized, and new ones can be added.

Note: For more information on debug, please refer to 'e<sup>2</sup> studio Debug Help' as described in chapter 6.

# 5.1 Change Existing Debug Configurations

The debug configuration has to be configured when debugging for the first time and only needs to be done once. An existing debug configuration can be changed as follows.

- 1. Click the 'Tutorial' Project in the **Project Explorer** pane to set the focus.
- Click Run → Debug Configurations... or <sup>\*</sup> icon (downward arrow) → Debug Configurations... to open the Debug Configurations window.



### Figure 5-2. Open Debug Configurations Window



3. In the Debug Configurations window, go to Renesas GDB Hardware Debugging → Tutorial HardwareDebug. Click on the Main tab to ensure the load module is Tutorial.elf.

🗎 🗙   🖻 🍄 ▼	Name: Tutorial HardwareDebug			
	🕒 Main 🔅 Debugger 🕨 Startup 💱			
C/C++ Remote Application				
JEASE Script	Project:			
GDB Hardware Debugging	Tutorial			<u>B</u> rowse
GDB OpenOCD Debugging	C/C++ Application:			
GDB Simulator Debugging (RH850)	HardwareDebug/Tutorial.elf			
Java Applet		Variables	Search Project	Browse
Java Application			Search Project	b <u>r</u> owse
s Launch Group	Build (if required) before launching			
Launch Group (Deprecated)	Build Configuration: Select Automa	itically		~
Remote Application	O Enable auto build	O Disable	auto build	
% Remote Debugger	Use workspace settings	0	Workspace Settings	
Remote Java Application	Ose workspace settings	conigure	workspace settings	
Renesas GDB Hardware Debuggin				
Tutorial HardwareDebug     Renesas Linux Application				
Renesas Linux Application				
Target Communication Framework				
	·			
>				
ter matched 18 of 20 items			Re <u>v</u> ert	Apply

Figure 5-3. Select Load Module

4. Switch to the Debugger tab, set 'J-Link ARM' as the debug hardware and 'R7S721001' as the target device.

🗎 Main 🏁 Debu	🖹 Main 🔯 Debugger 🕞 Startup 🦃 Source 🗖 Common				
Debug hardwar	re: J-Link ARM	Target Device: R7S721	001		
GDB Settings	Connection Settings	Debug Tool Settings			
GDB Connect	ion Settings				
<ul> <li>Autostart</li> </ul>	local GDB server	Host name or IP address:	localhost		
O Connect t	o remote GDB server	GDB port number:	61234		

Figure 5-4. Select Target Device



- 5. Under the **Debugger** tab, go to the **Connection Settings** sub tab to configure the following based on the settings in Segger J-Link emulator and RSK RZ/A1H board:
  - Interface
    - Type = 'SWD'
    - Speed = 'Auto'

*Note:* The debug configuration shown in Figure 5-5 is an example. Incorrect settings may cause malfunction or damage to the hardware. So please verify the board and emulator settings carefully before connection.

•  au •
 au
~
~
~
~
~
~
~
~
F
h.4

Figure 5-5. Change Connection Settings



### 6. Switch to the **Debug Tool Settings** sub tab, based on the RSK RZ/A1H board to ensure:

- Memory
  - Endian = 'Little Endian'

] 🗎 🗙   🖻 🍄 ▼	Name: Tutorial HardwareDebug		
type filter text	🖹 Main 🏁 Debugger 🕨 Startup 🗖	Common 🖳 Source	
<ul> <li>GDB Simulator Debugging (RH850 Java Applet Java Application</li> </ul>	Debug hardware: J-Link ARM	✓ Target Device: R7S721001	
🗣 Launch Group	GDB Settings Connection Settings	Debug Tool Settings	
► Launch Group (Deprecated)	~ IO		^
👒 Remote Application	Use Default IO Filename	Yes	~
🕸 Remote Debugger	IO Filename	\${eclipse_home}	
Remote Java Application	✓ General Debug		
🖌 🖻 Renesas GDB Hardware Debugging	Reset After Reload	Yes	~
Tutorial HardwareDebug	~ Memory		
Renesas Linux Application	Endian	Little Endian	$\checkmark$
🖻 Renesas Simulator Debugging (RX,	∽ Break		
Target Communication Framework	Use Flash Breakpoints	No	$\checkmark$ $\checkmark$
< · · · · · · · · · · · · · · · · · · ·			
Filter matched 18 of 21 items		Re <u>v</u> ert App	ly

Figure 5-6. Change Debug Tool Settings

- 7. Click the **Apply** button to confirm the settings. Then click **Debug** to execute the debug launch configuration to connect to the Segger J-Link and RSK RZ/A1H board.
- 8. After the successful connection, select the **Debug** view to show target debugging information in a tree hierarchy. The program entry point is set to the function \_PowerON\_Reset() in file start.S.

<b>†</b> Debug ⊠	💥   i+   🍫 🔻 🗖
🕆 🖻 Tutorial HardwareDebug [Renesas GDB Hardware Deb	ugging]
🗸 🗃 Tutorial.elf [1]	
✓ In Thread #1 1 (single core) (Suspended : Signal : SIC Image: PowerON_Reset() at start.S:51 0x20020114	GTRAP:Trace/breakpoint trap)
📕 arm-none-eabi-gdb (7.8.2)	
📕 Renesas GDB server (Host)	

Figure 5-7. User Target Connection in the Debug View



### 5.2 Create New Debug Configurations

The simplest way to create a new debug configuration is by duplicating an existing one. It can be performed by the following steps.

- 1. Repeat step 1 in chapter 5.1 to open the **Debug Configurations** window.
- Select a debug configuration (e.g. 'Tutorial HardwareDebug') and then click the icon (this duplicates the currently selected launch configuration). A new debug launch configuration (e.g. 'Tutorial HardwareDebug (1)') is created. It can be renamed to identify the settings by typing in the Name textbox and then clicking the Apply button.

	Name: Tutorial HardwareDebug 4k		
type filter text	■ Main 参 Debugger ► Startup ♥ So		
C/C++ Application	Main > Debugger > Startup > So		
C/C++ Remote Application	Debug hardware: J-Link ARM	Target Device: R7S721001	
EASE Script			
🖻 GDB Hardware Debugging	GDB Settings Connection Settings D	ebug Tool Settings	
GDB OpenOCD Debugging	✓ J-Link		^
GDB Simulator Debugging (RH850)	Туре	USB	~
Java Applet	J-Link Serial	(Auto)	
Java Application	Settings File	{workspace_loc:\\${ProjName}}\\${	Lau
🕏 Launch Group	Script File		
► Launch Group (Deprecated)	Low Power Handling	No	$\sim$
👒 Remote Application	✓ Interface		
🕸 Remote Debugger	Туре	JTAG	$\sim$
Remote Java Application	Speed (kHz)	4000	~
🗝 🖻 Renesas GDB Hardware Debugging	✓ JTAG Scan Chain		
Tutorial HardwareDebug	Multiple Devices	No	$\sim$
Tutorial HardwareDebug 4k	IRPre	0	
Renesas Linux Application	DRPre	0	
🖻 Renesas Simulator Debugging (RX, RL	~ Connection		
Target Communication Framework	Register initialization	No	$\sim$
-	Reset on connection	No	~
	Reset before run	No	$\sim$
	ID Code (Bytes)	FFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF	FF
		N1_	
< >>			
ilter matched 19 of 22 items		Re <u>v</u> ert A	pply

Figure 5-8. Duplicate A Selected Debug Launch Configuration

3. The debug configuration can be configured as described in chapter 5.1. For example, change the interface speed to '4000'.

4. The launch configuration is marked with '[local]' and a '\*' marker indicating that it is not yet attached to any project. Please specify project name in the **Common** tab.

Figure 5-9. Attach Launch Configuration to a Specific Project

# 5.3 Launch Bar

•

This chapter explains the usage of the **Launch Bar**, which is supported from V6.0.0 or later. The **Launch Bar** is located in the toolbar area of  $e^2$  studio's main window.

The **Launch Bar** is located in the tooldar area of e<sup>2</sup> studio's main window.

This simple interface includes build and debug buttons for the selected launch target.

	File	Edit	Source	Refactor	Navigate	Search	Project	Renesas Views	Run	Window	Help
	٨	苓		🎋 Deb	g	¥ (	<mark>c</mark> ≊ myprj	HardwareDebug		~ {	<b>x</b>
	4	4	a waa la u il al	//aumah dal							
В	ullons	to peri	orm build	launch der	oug for sele	cled large	el	Select build/de	ebug ta	irget	

### Figure 5-10. The Launch Bar Interface

The Launch Bar buttons behave as follows:

The build button | 🔦 | builds the load module of the selected launch configuration.

Note: There is another build button 4 in the project management toolbar which builds the active

build configuration in **Project Explorer**. Note that the launch bar does not reflect the active build state of **Project Explorer**, so care must be taken to ensure that you're not building one configuration, and then launching another.

- The debug 🔅 button triggers the launch of the debugger.
  - The stop **e** button terminates the selected launch target.

The Launch Bar and build button can be hidden through the following dialog.

• Click Window menu  $\rightarrow$  Preferences, then click Run/Debug  $\rightarrow$  Launching  $\rightarrow$  Launch Bar



# 5.4 Basic Debugging Features

This chapter explains the debug views supported by the e<sup>2</sup> studio IDE.

- Standard GDB Debug (supported by Eclipse IDE framework): breakpoints, expressions, registers, memory, disassembly and variables
- Renesas Extension to Standard GDB Debug: eventpoints, I/O registers and trace.

The following are some useful buttons which exist in the **Debug** view:



Figure 5-11. Useful Toolbars in Debug Views

The program is run by clicking the button or by pressing F8.

The program can be paused on a breakpoint or by clicking the you can perform the following operations:

- The \_\_\_\_\_ button or **F5** can be used for stepping into the next method call at the currently executing line of code.
- The 😰 button or **F6** can be used for stepping over the next method call (executing but without entering it) at the currently executing line of code.
- The Ib button can be clicked again to resume running.

The Button can be clicked to end the selected debug session and/or process, and the M button can be clicked to disconnect the debugger from the selected process.

Some other operations are as following:

- The 🔥 utton can be clicked to start new debug session.
- The X button can be clicked to reset the program to the power-on-reset entry point.
- The button is used for re-downloading the binary file to the target system.



*Note:* To demonstate the features in following section, please use the sample code for the RZ/A1H from the Renesas website as follows:

- 1. Download the Starter Kit Sample Code for RZ/A1H from the Renesas website: https://www.renesas.com/sg/en/software/D6000665.html.
- 2. Follow the instructions in chapter 3.1 to import the project "RZ\_A1H\_Tutorial\_RSK" to the workspace.
- 3. Open the project properties, select C/C++ Build → Settings in the left pane. Select tab Toolchain and select the GNU ARM Embedded toolchain for the project. Click Apply and Close.

Builders		
<ul> <li>C/C++ Build</li> <li>Build Variables</li> <li>Environment</li> <li>Logging</li> <li>Settings</li> </ul>	Configuration: HardwareDebug [ Active ] Tool Settings Toolchain Device Puild Steps Build Artifact Binary Pa Current Toolchain	Manage Configurations rsers  • Error Parsers
Tool Chain Editc	Toolchain: KPIT GNUARM-NONE-EABI Toolchain	
> C/C++ General	Version: v14.02	
<ul> <li>MCU</li> <li>Project References</li> <li>Renesas QE</li> <li>Run/Debug Settin</li> </ul>	Change Toolchain Toolchain: GCC ARM Embedded Version: 4.9.3.20150529	
> Task Repository	Additional Tools	
	Create Library generator	
	Create Flash image	
< >		

Figure 5-12. Update Project Toolchain



4. Right click on the project name in **Project Explorer**, select **Change Device** to select the correct device.

ile Edit Source Refactor Navigate Search ■ Copy CH+V Debete Poste Poste Poste Poste Poste Debete Source Nove Rename F2 ■ RathawarDebug ■ Individes ■ Copy CH+V Debete Source Nove Rename F2 ■ Individes ■ Compiler specific ■ Calar Project Clean Project © Charge Debug Command ■ makefile.init ■ LoadTutorialTOQSPIRelesse.Command ■ makefile.init ■ LoadTutorialTOQSPIRelesse.Command ■ makefile.init ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Rat_AtH_Tutorial_RSK Relesse.launch ■ Compare With Restore from Local History MISRA-C Sove build settings report Change Device Change Device Rat_SZ_2100 Target Device: RTS72100 Target Device: Target Device: Target Device: Device: Target Device: Target Device: Device: Target Device: Target Device: Device: Target Device:	Image: Source       Paste       Curl+V         Project Explorer 12       Image: Source       Move         Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       So
Image: Source       Delete       Source         Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       Move       Rename       F2         Image: Source       Image: Source       Image: Source       Rename       F2         Image: Source       Image: Source       Rename       F2         Image: Source       Image: Source       Rename       F2         Image: Source       Image: Source       Refersh       F5         Close Uncladed Project       Reide Consult Consul	Image: Second
Project Explorer ≥ <ul> <li>Bolerie</li> <li>Surce</li> <li>Move</li> <li>Rename</li> <li>F2</li> <li>Import</li> <li>Expert</li> <li>Build Project</li> <li>Clear Project</li> <li>Clear Project</li> <li>Clear Project</li> <li>Clear Project</li> <li>Clear Project</li> <li>Build Targets</li> <li>Index</li> <li>Build Targets</li> <li>Build Targets</li> <li>Build Targets</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>Build Stripers</li> <li>Compare With</li> <li>Rescription</li> <li>Clare Project Explorer</li> <li>Compare With</li> <li>Rescription</li> <li>Clare Project Strips</li> <li>Current: R7572100</li> <li>Current: R7572100</li> <li>Target Device: R75721001</li> <li>Unlock Devices</li> </ul>	Project Explorer 31     Detete     Detete       Source     Source       Source     Move       Rename     F2       Source     Move       Rename     F2       Source     Source       Source     Move       Rename     F2       Source     Source       Source     Source       Source     Rename       Source     Source       Source     Rename       Source     Refersh       Close Unrelated Project     Source       Build Configurations     Source       Build Sonfigurations     Source       Refersh     Source for Configurations       Build Configurations     Source for Norce       Source for Norce     Source for Norce
Project Explorer S       Move         Rename       F2         With Binarias       F2         Import       Build Project         Import       Build Targets         Import       Project Project         Build Targets       >         Import       Project Project         Build Targets       >         Index       >         Import       Project Project         Build Targets       >         Import       Project Project         Build Targets       >         Import       Project Project         Build Configurations       >         Import       Project Project         Compare With	Project Explorer 3       Move         F Binaries       F2         I Move       Rename         F Binaries       F2         I Move       Rename         F Binaries       F2         I Indudes       F2         I Induducial CoSPIRelease.Command       F2         I Index       F2         <
Implement       F2         Implement       F2 <td>File File   File File   File<!--</td--></td>	File File   File </td
<ul> <li># Binaries</li> <li>Includes</li> <li>Compiler specific</li> <li>Godo</li> <li>HardwareDebug</li> <li>Godo</li> <li>HardwareDebug</li> <li>Sinc</li> <li>LoadTutorialToQSPIbebug.Command</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>Makefile.init</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>Change Device</li> <li>Command Prompt</li> <li>Validate</li> </ul>	<ul> <li># Binaries</li> <li>Import</li> <li>Export</li> <li>Build Project</li> <li>Export</li> <li>Build Project</li> <li>Claim Project Projec</li></ul>
<ul> <li>Import</li> <li>Import</li> <li>Export</li> <li>Build Project</li> <li>Gan Project</li> <li>Gan Project</li> <li>Change Device</li> <li>Corrent: R7572100</li> <li>Current: R7572100</li> </ul>	<ul> <li>efficiency in the second sec</li></ul>
<ul> <li>includes</li> <li>icompler specific</li> <li>idco</li> <li>icompler specific</li> <li>idco</li> <li>inc</li> <li>isr</li> <li>isr&lt;</li> <li>isr</li> <li>isr&lt;</li> <li>isr</li> <li>isr&lt;</li> <li>isr</li> <li>isr</li> <li>isr</li> <li>isr&lt;</li> <li>isr</li> <li>isr</li> <li>isr&lt;</li> <l< td=""><td><ul> <li>Bindudes</li> <li>Compiler specific</li> <li>Geore</li> <li>HardwareDebug</li> <li>Firefresh</li> <li>LoadTutorialToQSPI.bat</li> <li>LoadTutorialToQSPI.bat</li> <li>LoadTutorialToQSPI.Betagese.Command</li> <li>Index</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>Change Device</li> <li>Cele the new device for RZ_A1H_Tutorial_RSK</li> <li>Current: R7572100</li> <li>Target Device: R75721001</li> </ul></td></l<></ul>	<ul> <li>Bindudes</li> <li>Compiler specific</li> <li>Geore</li> <li>HardwareDebug</li> <li>Firefresh</li> <li>LoadTutorialToQSPI.bat</li> <li>LoadTutorialToQSPI.bat</li> <li>LoadTutorialToQSPI.Betagese.Command</li> <li>Index</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>Change Device</li> <li>Cele the new device for RZ_A1H_Tutorial_RSK</li> <li>Current: R7572100</li> <li>Target Device: R75721001</li> </ul>
<ul> <li>Complet_specific</li> <li>HardwareDebug</li> <li>HardwareDebug</li> <li>Inc</li> <li>Str</li> <li>Custombat</li> <li>LoadTutorialToQSPIDab</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>Makefile.init</li> <li>RZ_A1H_Tutorial_RSK Release.loaned</li> <li>RRZ_A1H_Tutorial_RSK Release.loaned</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>Select the new device for RZ_A1H_Tutorial_RSK</li> <li>Current: R7S72100</li> <li>Target Device:</li> <li>RRZ_ST2100</li> <li>Unlock Devices</li> </ul>	<ul> <li>Se complet_specific</li> <li>Se dat</li> <li>HardwareDebug</li> <li>In c</li> <li>Se sc</li> <li>Case Droject</li> <li>Case Project</li> <li>Cose Unclaited Project</li> <li>Build Targets</li> <li>Index</li> <li>Index</li></ul>
<ul> <li>&gt; e doc</li> <li>e HardwareDebug</li> <li>e inc</li> <li>e src</li> <li>e custom.bat</li> <li>l LoadTutorialToQSPILbat</li> <li>l LoadTutorialToQSPIRelease.Command</li> <li>makefile.init</li> <li>R RZ_A1H_TutoriaL RSK HardwareDebugI</li> <li>R RZ_A1H_TutoriaL RSK Release.launch</li> <li>R RZ_A1H_TutoriaL RSK Release.launch</li> <li>R RZ_A1H_TutoriaL RSK Release.launch</li> <li>Change Device</li> <li>Select the new device for RZ_A1H_TutoriaL RSK</li> <li>Current: R7S72100</li> <li>Target Device: R7S721001</li> <li>Current: R7S72100</li> <li>Target Device: R7S721001</li> <li>Unlock Devices</li> </ul>	<ul> <li>e doc</li> <li>e HardwareDebug</li> <li>e inc</li> <li>e src</li> <li>custom.bat</li> <li>LoadTutorialToQSPLbat</li> <li>b coadTutorialToQSPIDebug.Command</li> <li>makefile.init</li> <li>RZ_AHT_TutoriaLRSK HardwareDebug1</li> <li>RZ_AHT_TutoriaLRSK Release.launch</li> <li>RZ_AHT_TutoriaLRSK Release.launch</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Command Prompt</li> <li>Validate</li> </ul>
<ul> <li>HardwareDebug</li> <li>Inc</li> <li>Refresh</li> <li>Refresh</li> <li>Refresh</li> <li>Clean Project</li> <li>Refresh</li> <li>Refresh</li> <li>Close Project</li> <li>Close Unrelated Project</li> <li>Build Targets</li> <li>Index</li> <li>BrZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RCC++ Project Settings</li> <li>Ctrl+Alk+P</li> <li>Run As</li> <li>Gommand Prompt</li> <li>Validate</li> </ul>	<ul> <li>e HardwareDebug</li> <li>e inc</li> <li>e sc</li> <li>Clean Project</li> <li>Refresh</li> <li>Refresh</li> <li>LoadTutorialToQSPI.bat</li> <li>LoadTutorialToQSPIRelease.Command</li> <li>Index</li> <li>Bidd Configurations</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MiSRA-C</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>
Perinc <p< td=""><td><ul> <li>init</li> <li< td=""></li<></ul></td></p<>	<ul> <li>init</li> <li< td=""></li<></ul>
<ul> <li>e src</li> <li>Custom.bat</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>makefile.init</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RR_A1H_Tutorial_RSK Release.launch</li> <li>RR Ra As</li> <li>Debug As</li> <li>Profile As</li> <li>Team</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>	<ul> <li>Str</li> <li>Custom.bat</li> <li>LoadTutorialToQSPI.bat</li> <li>Build Targets</li> <li>Build Targets</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>Build Configurations</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug1</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>REX_A1H_Tutorial_RSK Release.launch</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>Validate</li> </ul>
Custom.bat LoadTutorialToQSPI.bat LoadTutorialToQSPDeleg.Command LoadTutorialToQSPDeleg.Command RZ_A1H_Tutorial_RSK HardwareDebug1 RZ_A1H_Tutorial_RSK Release.launch Compare With Restore from Local History MISRA-C System Explorer Change Device CtH+Alt+P Run C/C++ Code Analysis System Explorer Change Device CtH+Alt+P Change Device CtH+Code Analysis Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100 Lunlock Devices Unlock Devices	<ul> <li>i custom.bat</li> <li>i LoadTutorialToQSPI.bat</li> <li>i LoadTutorialToQSPIDebug.Command</li> <li>i LoadTutorialToQSPIRelease.Command</li> <li>index</li> <li>index</li></ul>
<ul> <li>LoadTutorialToQSPIDebug.Command</li> <li>LoadTutorialToQSPIRelease.Command</li> <li>makefile.init</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>R RZ_A1H_Tutorial_RSK Release.launch</li> <li>R RZ_A1H_Tutorial_RSK</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>	<ul> <li>LoadTutorialToQSPLbat</li> <li>LoadTutorialToQSPIDebug.Command</li> <li>Index</li> <li>Index</li> <li>Index</li> <li>Index</li> <li>Build Targets</li> <li>Index</li> <li>Build Configurations</li> <li>RZ_A1H_Tutorial_RSK HardwareDebug.</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>Run As</li> <li>Debug As</li> <li>Profile As</li> <li>Taram</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build Settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>
LoadTutorialToQSPIDebug.Command     LoadTutorialToQSPIRelease.Command     makefile.ini     RZ_A1H_Tutorial_RSK HardwareDebug.     Rin As     Tobug As     Profile As     Team     Compare With     Restore from Local History     MISRA-C     Save build settings report     Change Device     C(C++ Project Settings     C(C++ Project Settings     System Explorer     Command Prompt     Validate     Change Device     Select the new device for RZ_A1H_Tutorial_RSK     Current: R7572100     Target Device: R7572100     Target Device: R7572100	build targets LoadTutorialToQSPIRelease.Command makefile.init RZ_A1H_Tutorial_RSK HardwareDebug. RZ_A1H_Tutorial_RSK Release.launch RZ_A1H_Tutorial_RSK Release.launch RZ_A1H_Tutorial_RSK Compare With Restore from Local History MISRA-C Save build settings report Change Device C(C++ PC)et Settings System Explorer Command Prompt Validate Change Device: RTS72100 Target Device: RTS7210 Target Device: RTS72100 Target Device: RTS72100 Target Device: RTS7210 Target Device: RTS7210 Target Device: RTS7210 Target Device: RTS7210 Target Device: RTS7210 Target Device: RTS7210 Target Dev
<ul> <li>LoadTutorialToQSPIRelease.Command</li> <li>makefile.init</li> <li>RZ_A1H_Tutorial_RSK HardwareDebugt</li> <li>RZ_A1H_Tutorial_RSK Release.launch</li> <li>Debug As</li> <li>Pofile As</li> <li>Team</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>Run CC++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>	LoadTutorialToQSPIRelease.Command     makefile.init     Quid Configurations     Quid Configurati
Build Configurations	
<ul> <li>Indecentering</li> <li>RZ_ATH_Tutorial_RSK Release.Jaunch</li> <li>RZ_ATH_Tutorial_RSK Release.Jaunch</li> <li>Debug As</li> <li>Profile As</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>Run C/C++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>	<ul> <li>Indextmediation</li> <li>RZ_AHT_Tutorial_RSK HardwareDebug.</li> <li>RZ_AHT_Tutorial_RSK Release.launch</li> <li>Debug As</li> <li>Profile As</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>Run C/C++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>
<ul> <li>RZ_ATH_Tutorial_RSK Release launch</li> <li>RZ_ATH_Tutorial_RSK Release launch</li> <li>Profile As</li> <li>Feam</li> <li>Compare With</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>C/C++ Project Settings</li> <li>Ctrl+Alt+P</li> <li>Run C/C++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul>	<ul> <li>Rejorn Journal Control Course</li> <li>Rejorn Journal Course</li> <li>Rejorn Journal Course</li> <li>Rejorn Journal Course</li> <li>Rejorn Journal Course</li> <li>Restore from Local History</li> <li>MISRA-C</li> <li>Save build settings report</li> <li>Change Device</li> <li>Command Prompt</li> <li>Validate</li> </ul> Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Profile As Team Compare With Restore from Local History MISRA-C Save build settings report Change Device C(C++ Project Settings CylC++ Project Settings CylC++ Project Settings CylC++ Project Settings CylC++ Project Settings CylC++ Project Settings System Explorer Command Prompt Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Profile As Team Compare With Restore from Local History MISRA-C Save build settings report Change Device © C/C++ Project Settings © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Team   Compare With   Restore from Local History   MISRA-C   Save build settings report   Change Device   © C/C++ Project Settings   Ctrl+Alt+P   % Run C/C++ Code Analysis   © System Explorer   © Command Prompt   Validate   Change Device   Select the new device for RZ_A1H_Tutorial_RSK   Current: R7S72100   Target Device:   R7S721001	Team   Compare With   Restore from Local History   MISRA-C   Save build settings report   Change Device   © C/C++ Project Settings   Ctrl+Alt+P   % Run C/C++ Code Analysis   * System Explorer   • Command Prompt   • Validate   Change Device   Select the new device for RZ_A1H_Tutorial_RSK   Current: R7S72100   Target Device:   R7572100   Unlock Devices
Compare With Restore from Local History MISRA-C Save build settings report Change Device © C/C++ Project Settings Ctrl+Alt+P % Run C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001	Compare With Restore from Local History MISRA-C Save build settings report Change Device © C/C++ Project Settings © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Restore from Local History MISRA-C Save build settings report Change Device © C/C++ Project Settings Ctrl+Alt+P % Run C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001	Restore from Local History MISRA-C Save build settings report Change Device © C/C++ Project Settings Ctrl+Alt+P % Run C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7572100 Target Device: R75721001
MISRA-C Save build settings report Change Device © C/C++ Project Settings Wan C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001	MISRA-C Save build settings report Change Device © C/C++ Project Settings © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Save build settings report Change Device © C/C++ Project Settings Wan C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Save build settings report Change Device © C/C++ Project Settings © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7572100 Target Device: R75721001 Unlock Devices
Change Device © C/C++ Project Settings Ctrl+Alt+P P Run C/C++ Code Analysis © System Explorer © Command Prompt Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Change Device © C/C++ Project Settings Ctrl+Alt+P P Run C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Change Device © C/C++ Project Settings Ctrl+Alt+P P Run C/C++ Code Analysis © System Explorer © Command Prompt Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Change Device © C/C++ Project Settings Ctrl+Alt+P P Run C/C++ Code Analysis © System Explorer © Command Prompt © Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
<ul> <li>C/C++ Project Settings</li> <li>Run C/C++ Code Analysis</li> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul> Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100
Run C/C++ Code Analysis System Explorer Command Prompt Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices	Run C/C++ Code Analysis System Explorer Command Prompt Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7572100 Target Device: R75721001 Unlock Devices
<ul> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul> Change Device       Select the new device for RZ_A1H_Tutorial_RSK       Current: R7S72100       Target Device:       R7S721001	<ul> <li>System Explorer</li> <li>Command Prompt</li> <li>Validate</li> </ul> Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7572100 Target Device: R75721001	<ul> <li>Command Prompt Validate</li> <li>Change Device Select the new device for RZ_A1H_Tutorial_RSK</li> <li>Current: R7S72100 Target Device: R7S721001         <ul> <li>Lunlock Devices</li> </ul> </li> </ul>
Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100	Validate Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001 Unlock Devices
Change Device   Select the new device for RZ_A1H_Tutorial_RSK   Current: R7S72100   Target Device: R7S721001   Unlock Devices	Change Device Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S721001
Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100  Target Device: R7S721001  Unlock Devices	Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100 Unlock Devices
Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100  Target Device: R7S721001  Unlock Devices	Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100 Unlock Devices
Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100  Target Device: R7S721001  Unlock Devices	Select the new device for RZ_A1H_Tutorial_RSK Current: R7S72100 Target Device: R7S72100 Unlock Devices
Current: R7S72100	Current: R7S72100 Target Device: R7S72100 Unlock Devices
Current: R7S72100	Current: R7S72100 Target Device: R7S72100 Unlock Devices
Target Device: R7S721001	Target Device: R7S721001
Target Device: R7S721001	Target Device: R7S721001
Unlock Devices	Unlock Devices
Unlock Devices	Unlock Devices
(?) < Back Next > Finish Cancel	⑦ < <u>Back</u> <u>Next</u> > <u>Finish</u> Cancel
③ < Back Next > Finish Cancel	? < <u>Back Next &gt; Einish Cancel</u>
(?) < Back Next > Finish Cancel	? < <u>Back Next &gt; Einish Cancel</u>
(?) < Back Next > Finish Cancel	? < <u>Back</u> <u>Next</u> > <u>Finish</u> Cancel
③ < Back Next > Finish Cancel	? < <u>Back Next &gt; Einish Cancel</u>
(?) < Back Next > Finish Cancel	Image: Cancel
(?) < Back Next > Finish Cancel	? < <u>Back</u> <u>Next</u> > <u>Finish</u> Cancel
? < Back Next > Finish Cancel	? < <u>Back</u> <u>Next</u> > <u>Finish</u> Cancel
? < Back Next > Finish Cancel	Image: Concentration         Mext >         Einish         Cancel

Figure 5-13. Change Device

5. After selecting the new device, click **Next** → **Next** to review changed information for the new device. If everything is OK, click **Finish**.

	_		
Change Device			
Review the information provided in the list below. Click 'Next >' to view the next item or 'Finish'.			
Found problems	\$ €		
<sup> </sup>			
Lnable to load project generation settings for HardwareDebug. Some build settings r	nay not		
This change cannot be undone. Please make sure you backup this project before cont	inuing.		
<	>		
No context information available			
Image: Second			
Change Device			
The following changes are necessary to perform the refactoring.			
Changes to be performed	7 -		
✓ ✓ ֎ Build Settings       ✓ ֎ Release       ✓ ֎ Project Files       ✓ ֎ generate/start.S       ✓ ֎ generate/inthandler.c       ✓ ֎ generate/interrupt_handlers.h       ✓ ֎ generate/vects.c       ✓ ֎ generate/vect_table.S       ✓ ֎ generate/hwsetup.c       ✓ ֎ generate/bysetup.c       ✓ A Ssembly Compare Viewer       New       1			
<pre>2/* File Version: V1.00 3/* Date Generated: 19/04/20 4/************************************</pre>	>		
? < <u>Back</u> <u>Next</u> > <u>Finish</u> Cancel			

Figure 5-14. Changed Information after Changing Device

6. Switch to the **Tool Settings** tab, select **Cross ARM C Linker** → **General**, and add the script file generated by the compiler to the script file list. Click **Apply and Close**.

Configuration: HardwareDebug [Active]	×	Manage Configurations
🖲 Tool Settings 🔊 Toolchain 🕲 Device 🎤	Build Steps 😤 Build Artifact 🗟 Binary Parsers 🥺 Error Parsers	
🖉 Target Processor	Script files (-T)	(월) 월 전 월
Optimization	"\${ProjDirPath}/src/linker_scriptHardwareDebug.ld"	
避 Warnings		
🖉 Debugging		
🕆 🕸 Cross ARM GNU Assembler		
Preprocessor		
🖉 Includes 🛛 🔯 Add file pat	h	×
🖉 Warnings		
Miscellaneous File:		
✓ S Cross ARM C Compile \${workspace	_loc:/\${ProjName}/compiler_specific/GNU_IRAM.ld}	
Freprocessor		
Includes		
Optimization		
Warnings Miscellaneous		
✓ S Cross ARM C Linker	OK Cancel Workspace	File system
@ General		
Libraries	Entry Point:	
Miscellaneous	Use custom linkage order	
Other	Linkage Order List	김 최 월 집 십
✓ Scross ARM GNU Create Flash Image	"Asrc\adc.o"	<b>^</b>
🖉 General	".\src\ascii.o"	
	".\src\bsc.o" ".\src\bsc userdef.o"	
🕆 🛞 Cross ARM GNU Print Size		

Figure 5-15. Add Compiler Generated Script File

7. Build the project and make sure that it is successful.



### 5.4.1 Breakpoints View

The **Breakpoints** view stores the breakpoints that were set on executable lines of the program. If a breakpoint is enabled during debugging, the execution suspends before that line of code executes. e<sup>2</sup> studio allows software and hardware breakpoints to be set explicitly in the IDE. Any breakpoints added via double click on the marker bar are by default hardware breakpoints. If the hardware resources are not available, then the breakpoint setting will fail. In case of a hardware breakpoint setting failure, an error message will prompt you to switch to a software breakpoint.

To set the default breakpoint type (hardware or software):

 Right-click on the marker bar to bring up the context menu. For a hardware breakpoint, select Breakpoint Types → e<sup>2</sup> studio Breakpoint. For a software breakpoint, select Breakpoint Types → C/C++ Breakpoints.

To set a breakpoint:

- 1. Open main.c and double-click on the marker bar located in the left margin of the C/C++ Editor pane to set a breakpoint. A dot 🥨 (hardware breakpoint) or 📽 (software breakpoint) is displayed in the marker bar depending on the Breakpoint Type selected. Breakpoint Type is hardware breakpoint by default.
- 2. Alternatively, right-click at the marker bar to choose **Toggle Hardware Breakpoint** or **Toggle Software Breakpoint** to set a hardware breakpoint **30** or a software breakpoint **30**.
- Click Windows → Show View → Breakpoints or the breakpoints icon <sup>9</sup>
   (or use shortcut key ALT + Shift + Q, B) to open the Breakpoints view to view the corresponding software breakpoints set. Software breakpoints can be enabled and disabled in the Breakpoints view.

To disable breakpoints, you can choose to disable specific breakpoints or to skip all breakpoints:

- 1. To disable a specific breakpoint, right-click on the software breakpoint or hardware breakpoint located in the left margin of the **C/C++ Editor** pane and select **Disable Breakpoint**, or uncheck the related line in the breakpoints view. A disabled breakpoint is displayed as a white dot ( or ).
- 2. To skip all breakpoints, click on the icon in the breakpoints view. A blue dot with a backslash will appear in the editor pane as well as in the breakpoints view.



Figure 5-16. Breakpoints view
#### 5.4.2 Expressions View

The **Expressions** view monitors the value of global variables, static variables or local variables during debugging. For all RZ/A1H debuggers, these variables (including the local variables in scope) can be set for real-time refresh.



Figure 5-17. Expressions View

To watch a global variable:

- 1. Click Window  $\rightarrow$  Show View  $\rightarrow$  Expressions or the icon  $\square$  to open the Expressions view
- 2. Drag and drop a global variable over to the **Expressions** view. Alternatively, right-click on the global variable and select the **Add Watch Expression...** menu item to add it to the **Expressions** view.
- 3. In the **Expressions** view, right-click to select the **Real-time Refresh** menu item. This refreshes the expression value in real-time when program is running. The character 'R' indicates that this global variable will be updated in real-time.
- 4. To disable the real time refresh, simply right-click to select the **Disable Real-time Refresh** menu item.



## 5.4.3 Registers View

The **Registers** view lists the information about the general registers of the target device. Changed values are highlighted when the program stops.

Ariables Seakp	in Registers & 📽 Exp	oress 🗣 Eventp 🚡 Periphe 🔲 IO Reg 🦳 🦄
Name	Value	Description
🕶 🛗 General Registers		General Purpose and FPU Regi
1919 rO	0x0	
<sup>3030</sup> r1	0x22	
<sup>1919</sup> r2	<mark>0x0</mark>	
<sup>1010</sup> r3	0x2000007e	
<sup>1010</sup> r4	0x0	
<sup>1010</sup> r5	0x0	
Name : r1 Hex:0x22 Decimal:34 Octal:042 Binary:100010		
Float:4.764414 Default:34	/8e-044	
Float:4.764414	/8e-044	

## Figure 5-18. Registers View

To view the general register 'r0':

- 1. Click Window  $\rightarrow$  Show View  $\rightarrow$  Registers or the  $\ddagger$  icon to open the Registers view.
- 2. Click 'r0' to view the values in a different radix.

Values that have been changed are highlighted in yellow in the **Registers** view when the program stops.



#### e<sup>2</sup> studio Getting Started Guide

#### 5.4.4 Memory View

The **Memory** view allows users to view and edit the memory presented in 'memory monitors'. Each monitor represents a section of memory specified by its base address. The memory data in each memory monitor can be presented in different 'memory renderings', which are predefined data formats (e.g. hex integer, signed integer, unsigned integer, ASCII, image, etc.)

To view the memory of a variable (e.g. g\_data\_block\_content):

1. Click **Window** → **Show** View → Memory or the

icon to open the **Memory** view.

2. Click the icon to dependent of the variable g\_data\_block\_content.

Monitor Memory	×	(					
Enter address or express &g_data_block_content		r F	-		_	k_content is of 'Hex Integer'	
⑦ OK □ Co ③ Tas ⑧ Pro ④ Exe ④ Sm	Cancel	∜. Re ∞ Tr	a ☆ Me	🔋 Me 💿 Per	⊙Vis ≋ F	Re 0 Me 23 🔗 Se	
				_	<b>\$</b> 101	y uu 🚸 📑 🛃 🎫 🛃	<b>ii ▼</b> ▽
Monitors 🕂 🛠 🕷							
ag_data_block_content	Address	0 - 3	4 - 7	8 - B	C - F		^
T T	0000000020057B80	00000000	0000000	00000000	00000000		
	0000000020057B90		00000000	00000000	00000000		
	0000000020057BA0	00000000	00000000	00000000	00000000		
Memory Monitor for	0000000020057BB0	00000000	00000000		00000000		
"g_data_block_content" is	0000000020057BC0	00000000	00000000	00000000	00000000		
specified by the address	0000000020057BD0	00000000	00000000	00000000	00000000		
"&g_data_block_content"	0000000020057BE0	00000000	00000000	00000000	00000000		
	0000000020057BF0	00000000	00000000		00000000		
	0000000020057C00	00000000	00000000	00000000	00000000		
	0000000020057C10	00000000	00000000	00000000	00000000		$\sim$

Figure 5-19. Memory View (1/2)



To add new renderings format (e.g. ASCII) for the variable g\_data\_block\_content:

1. Click the + New Renderings... tab and select 'ASCII' to add the rendering

This creates a new tab named '&g\_data\_block\_content < ASCII>' next to the tab '&g\_data\_block\_content <Hex Integer>'.

0 Memory ¤							👙 1010 to10 🔗 📑 🛃	\$ 11 \$ 10 ▼ □
Monitors	+ × %	&g_data_block_content <	<hex inte<="" td=""><td>eger&gt;</td><td>New Renderi</td><td>ngs</td><td></td><td></td></hex>	eger>	New Renderi	ngs		
&g_data_block_content		Memory Monitor: &g_da	ata_block	_content	0x20057B84			
		Select rendering(s) to cre	eate:					
		Image						Add Rendering(s
		Raw Image						, du Hendening(
		Floating Point						
		Traditional						
		Raw Hex						
		ASCII Signed Integer			_			
		Unsigned Integer						
		onsigned integer						~
Manager							han 1019 1010 🚕 📑 🛃	∞ 🗄 🕏 👫 🕶 🗢 🗆
I Memory ⊠								
Ionitors	+ × %	&g_data_block_content	<hex int<="" td=""><td>eger&gt;</td><td>&amp;g_data_block</td><td>_conten</td><td>t : 0x20057B84 <ascii td="" 🛛<=""><td>+ New Renderings</td></ascii></td></hex>	eger>	&g_data_block	_conten	t : 0x20057B84 <ascii td="" 🛛<=""><td>+ New Renderings</td></ascii>	+ New Renderings
&g_data_block_content		Address	0 - 3	4 - 7	8 - B	C - F		
		0000000020057B80	0000	0000	0000	0000		
		0000000020057B90	0000	0000	0000	0000		
		0000000020057BA0	0000	0000	0000	0000		
		0000000020057BB0	0000	0000	0000	0000		
		0000000020057BC0	0000	0000	0000	0000		
		0000000020057BD0	0000	0000	0000	0000		
		0000000020057BE0	0000	0000	0000	0000		
		0000000020057BF0	0000	0000	0000	0000		
		000000020057C00	0000	0000	0000	0000		
				000		000		

Figure 5-20. Memory View (2/2)



# 5.4.5 Disassembly View

The **Disassembly** view shows the loaded program as assembler instructions mixed with the source code for comparison. The currently executing line is highlighted by an arrow marker in the view. In the **Disassembly** view, user can set breakpoints on assembler instructions, enable or disable these breakpoints, step through the assembler instructions and even jump to a specific instruction in the program.



Figure 5-21. Disassembly View

To view both C and assembly codes in a mixed mode:

- 1. Click Window  $\rightarrow$  Show View  $\rightarrow$  Disassembly or the  $\blacksquare$  icon to open the Disassembly view.
- 2. Click the <sup>4</sup>/<sub>5</sub> icon to enable synchronization between the assembly source and the C source (active debug context).
- 3. In the **Disassembly** view, right-click on the address column to select **Show Opcodes** and **Show Function Offsets**.
- 4. You can enable and disable source addresses within the editor using the context menu.

	Toggle Software Breakpoint	
	Toggle Hardware Breakpoint	
	Toggle Breakpoint	Ctrl+Shift+B
	Add Breakpoint	Ctrl+Double Click
	Add Dynamic Printf	
	Enable Breakpoint	Shift+Double Click
	Breakpoint Properties	Ctrl+Double Click
	Breakpoint Types	>
	Switch Default e2 studio Breakp	point type to Software
	Build Selected File(s)	Ctrl+Alt+Shift+B
	Clean Selected File(s)	Ctrl+Alt+Shift+C
Ø	Toggle Timer Start Eventpoint	
Ľ	Toggle Timer Stop Eventpoint Edit Eventpoint	
	Disable Timer Stop Eventpoint	
	Disable Timer Stop Eventpoint	
B	Clear Coverage Markers	
	Toggle Relocated Breakpoint En	nabled
	Delete Relocated Breakpoint	le main.c ∞ le lcd pmod.c
-	Go to Disassembly	15/
	Go to Annotation	139 ** End of function dela
0	Toggle Monitor Point	143 •* Function Name: main
	Enable/Disable Monitor Point	152 • int_t main(void) 153 20042b60 {
<b>\$</b>	C/C++ Project Settings	154 20042b6c char strdata[32] =
<b>~</b>	Validate	155 20042b98 uint8_t * pmptr = 0
	Add Bookmark	⇒ 157 20042ba4 R_RIIC_rza1h_rsk_i
	Add Task	158
$\overline{}$	Show Source Addresses	<pre>159</pre>
~	Show Eventpoints	161
~	Show Quick Diff	162 /* Initialise the 163 20042bac R_LCD_Init();
Ĵ	Show Line Numbers	164
•	Folding	165 /* Initialise dire 166 20042bb0 R_LED_Init();
		167 167 167
_	Preferences	<

Figure 5-22. Source Addresses Menu



# 5.4.6 Variables View

The **Variables** view displays all of the valid local variables in the current program scope.

Please refer to the **Expressions View** section to watch global variables or external variables out of the current program scope.

Name	Туре	Value	Name : strdata	
> 達 strdata	char [32]	0x2090ffc4	Details:'\0' <repeats 31="" times=""> Default:0x2090ffc4</repeats>	
> • pmptr	unsigned char *	0x200517f4 <g_rg< th=""><th>Defaul::0x20901704 Decimal:546373572 Hex:0x2090ffc4 Binary:10000010010000111111111000 Octal:04044177704</th><th>100</th></g_rg<>	Defaul::0x20901704 Decimal:546373572 Hex:0x2090ffc4 Binary:10000010010000111111111000 Octal:04044177704	100
			<	>
اً a main.c ا	t.S 🖻 lcd pmod.c			- 6
143 152 153 20042ae8 154 20042af4		d)		^
152	<pre>●int_t main(voi {</pre>		];	^
152 153 20042ae8 154 20042af4 155 20042b54 156 157 20042b60 158	<pre> int_t main(voi {     char strda     uint8_t *</pre>	<pre>d) ta[32] = ""; pmptr = &amp;g_rgb888_logo[0 1h_rsk_init();</pre>	];	^
152 153 20042ae8 154 20042af4 155 20042b54 156 157 20042b60	<pre>•int_t main(voi {     char strda     uint8_t *     R_RIIC_rza     /* Initial</pre>	<pre>d) ta[32] = ""; pmptr = &amp;g_rgb888_logo[0 1h_rsk_init(); ise SPI */</pre>	];	^

# Figure 5-23. Variables View

To observe a local variable (e.g. strdata in function main()):

- 1. Click Window  $\rightarrow$  Show View  $\rightarrow$  Variables or the  $\bowtie$  icon to open the Variables view.
- 2. Step into the function main() to view the value of local variable strdata.

## Note:

Variables which are optimized out or temporarily allocated to accumulator registers may not appear in this view. Please refer to the **Disassembly** view if necessary.



#### 5.4.7 Eventpoints View

An event refers to a combination of conditions set for executing break or trace features during program execution. The **Eventpoints** view allows you to set up or view defined events from various categories e.g. trace start, trace stop, trace record, event break, before PC, performance (timer) start and performance (timer) stop.

The number of events that can be set and the setting conditions differ with each MCU. There are two types of event:

- Execution address: The emulator detects execution of the instruction at the specified address by the CPU. It can be a 'before PC' (before Program Counter) break (the event condition is satisfied immediately <u>before</u> execution of the instruction at the specified address) or other events (where the event condition is satisfied immediately <u>after</u> execution of the instruction at the specified address).
- Data access: The emulator detects access under a specified condition to specified address or specified address range. *This allows the setup of complex address and data matching criteria.*

Event combination (e.g. OR, AND (cumulative) and Sequential) can be applied to two or more events.

To set an event break for a global variable when a variable is accessed (e.g. when g\_adc\_result is assigned a new value):

- 1. Click Window  $\rightarrow$  Show View  $\rightarrow$  Eventpoints or the eventpoints view.
- 2. Double-click the Event Break option to open Edit Event Break dialog box.
- 3. Click the **Add...** button to continue.

💡 Edit Even	t Break						×
					Trig	gger: OR	~ \$ <b>†</b>
Туре	Address	Data	Count	Timer	Handle	Comment	
Add E	dit Delete	PC: 0/0 OA: 0/4 All:	0/4				
					OK		Cancel

#### Figure 5-24. Edit Event Break

- 4. Select Data Access as the eventpoint type.
- 5. Go to the **Address Settings** tab, click the icon and browse for the symbol g\_adc\_result (the address of this global variable is &g\_adc\_result).



6. Next, switch to the Data Access Settings tab, select 'Write' for Data Settings. Click OK to proceed.

Add Event	point	×	Add Event	tpoint			×
	Type Data Access	s Settings dc_result v	Eventpoint Address Se Data Settir Read/Write Size: Bus Master	ettings C ngs: e:	)ata Acc		~
Cline Edit Even	ıt Break						×
				Trig	ger: O	R	<ul><li>↓ ↓</li></ul>
Туре	Address	Data		Count	Timer	Handle	Comment
⊡ ≜ OR	&g_adc_result	Write All					
< Add E	dit Delete f	PC: 0/0 OA: 1/4 All: 1/	4	OK			> Cancel

Figure 5-25. Add Eventpoint

7. Ensure that the event break for &g\_adc\_result is set and enabled in the **Eventpoints** view. Reset to execute the program from the start.

					4 🔍 l	🕷 🔆 🕞 📄 🖳 PC: 0,	/6 OA: 1/4	80 🖻
Туре	Address	Data	Count	Timer	Handle	Comment		
🗌 🏁 Trace Start								
🗌 🗃 Trace Stop								
🔤 🧉 Trace Record	1							
👻 🔽 🖓 Event Break								
✓ OR	&g_adc_result (0x20057b74)	Write All			7			
🗌 😥 Timer Start								
🗌 😰 Timer Stop								
Project Saved Temp	plates							
		- (						- 8
🖻 main.c 🛛 🖻 start.S	🛛 🖻 lcd_pmod.c 🛛 🖻 initsct.	S 🖬 adc.c 🛛 🖬	flashled.	С				-
301 302	/* Read the result	t register fo	n AN7 *	/				^
	g_adc_result = ((u				:			
		/		,				
303 20040308 304								
<ul> <li>303 20040308</li> <li>304</li> <li>305</li> </ul>	/* Check if the up			s hav	e been	read */		
<ul> <li>303 20040308</li> <li>304</li> <li>305</li> <li>306 20040330 €</li> </ul>	/* Check if the up			s hav	e been	read */		
<ul> <li>303 20040308</li> <li>304</li> <li>305</li> </ul>	/* Check if the up	== g_adc_resu	1t)	s hav	e been	read */		

Figure 5-26. Execution of Event Break

Figure 5-26 shows that when g\_adc\_result is assigned a new value, the program stops at code line number 303 of adc.c (where g\_adc\_result is assigned new value).

# 5.4.8 IO Registers View

**IO Registers** is also known as the Special Function Registers (SFR). The **IO Register** view displays all the registers set defined in a target-specific I/O file, including their address, hexadecimal and binary values. You can further customize your own **IO registers** view by selectively adding I/O registers to the **Selected Registers** pane.

	me	Value (Hex)	Value (Bin)	Address	Access		~
>	MMC	vulue (Hex)	value (birl)	Address	7100055		
>	MTU2						
Ý	OSTM0						
	OSTMOCMP	0x00000000	000000000	0xfcfec000	RW		1
	OSTMOCHT	Oxffffffff	1111111111		RW		
	OSTMOT	0x00		0xfcfec010	RW		
	OSTM0T:	0x00		0xfcfec014	RW		
	OSTM0T	0x00	00000000	0xfcfec018	RW		
	OSTM0C	0x00	0000000	0xfcfec020	RW		
>	OSTM1						
>	PFV0						
AIL	Registers Selection	Registers				· · · · · · · · · · · · · · · · · · ·	
<sup>x)=</sup> Va	riables 🤏 Bre po	oints 🔤 Registe	ers 🟘 Expression	ns 🔎 Eventpo		5	
		5			Ē		□
×)= Va Nar	ne	vints <sup>100</sup> Registe Value (Hex)	ers 📽 Expression Value (Bin)			5	-
Nar		5		Address	Ē	5	
Nar		Value (Hex)	Value (Bin)	Address	Access	5	~
Nar		Value (Hex)	Value (Bin)	Address	Access	5	~

# Figure 5-27. IO Registers View

To view selected I/O registers (e.g. OSTM0CMP in OSTM0):

- Click Windows → Show View → Others... In the 'Show View' dialog, click IO Registers under Debug or the icon to open the IO Registers view.
- 2. Under the All Registers tab, locate OSTM0 in the IO Registers view. Expand the OSTM0 I/O register list.
- 3. Drag and drop the 'OSTM0CMP' to the **Selected Registers** pane. A green dot <a>> besides the I/O register indicates the status of being the selected register(s).</a>
- 4. Switch to the **Selected Registers** tab to view 'OSTM0CMP' of the 'OSTM0' I/O register.

The expanded I/O register list may take a longer time to load in the **All Registers** pane. Hence, it is advisable to customize and view multiple selected I/O registers from the **Selected Registers** pane.

## 5.4.9 Trace View

Tracing means the acquisition of bus information per cycle from the trace memory during user program execution. The acquired trace information is displayed in the **Trace** view. It is helpful when tracking the program execution flow to search for and examine points where problems have arisen.

The trace buffer is limited to a size of 1 to 32 Mbytes. The oldest trace data is overwritten with the new data once the buffer has become full.

Tracing is not fully supported on the RZ/A1H.



# 5.5 How to debug (RZ/A2M)

This chapter describes how to debug referring to the 'Loader' and 'Application' projects built in Chapter 3.2.1 and based on the hardware configuration: Segger J-Link emulator and RZ/A2M Evaluation Board Kit as target board.

- 1. Click the 'Loader' project in the **Project Explorer** pane to set focus.
- 2. Build the 'Loader Project' (refer to Chapter 4.2), to create the 'Loader.elf' file.
- 3. Select 'Loader HardwareDebug' in the Launch Configuration of the Launch Bar.

N Debug V Loader HardwareDebug V 🔅		s 🐐 🔳	🎄 Debug 🗸 🗸	💽 Loader HardwareDebug	~ 🌣
------------------------------------	--	-------	-------------	------------------------	-----

#### Figure 5-28. Select Launch Configuration

4. Click the is button of Launch Bar, and connect to target board and download 'Loader.elf' file to it.

· · · · · · · · · · · · · · · · · · ·	Connecting to R7S921053VCBG, ARM Target GDBServer endian : little Target power : off
	Starting target connection
	Finished target connection
	GDB: 57853
	Target connection status - OK
	Target connection status - OK
2	Starting download
	Finished download

## Figure 5-29. Console View After Download

- 5. Click the 📕 button on the Launch Bar to stop the debug session.
- 6. Click the 'Application' project in **Project Explorer** pane to set focus.
- 7. Build the 'Application Project' (refer to Chapter 4.2), to create the 'Application.elf' file.
- 8. Select 'Loader HardwareDebug' in the Launch Configuration of the Launch Bar.

🔦 🎋 🔳 🎋 Debug	~	C Application HardwareDebug	~ 🌣	
---------------	---	-----------------------------	-----	--

#### Figure 5-30. Select Launch Configuration

- 9. Click the button on the Launch Bar, to connect to the target board and download the 'Application.elf' file to it.
- 10. After downloading the 'Application.elf' file, click the object to restart the program from the entry point of 'Loader.elf'.



# 6. Help

The help system allows you to browse, search, bookmark and print help documentation from a separate Help window or help view within the workbench. You can also access an online forum dedicated to  $e^2$  studio from here.

Click on the **Help** tab to pull down the help menu.

He	elp		
é	Welcome	(1)	
đ	Help Contents	(2)	
8	Search		
	Show Contextual Help		
	Show Active Keybindings		Ctrl+Shift+L
	Cheat Sheets		
×	RenesasRulz Community Fo	orum (3)	
ß	Add Renesas Toolchains		
â	Perform Setup Tasks		
4	Check for Updates		
6	Install New Software		
	Renesas e2 studio feedbac	¢	
8	IAR Embedded Workbench plugin manager		
C	About e <sup>2</sup> studio		

Figure 6-1. Help Menu

## Quick Help Tips

- (1) Click **Welcome** for an overview of e<sup>2</sup> studio, a link to access the IDE tutorial and sample, and to view the release notes.
- (2) Click Help Contents to open a separate Help window with search function.
- (3) Click **RenesasRulz Community Forum** to go online forum that is dedicated to topics and discussion related to e<sup>2</sup> studio IDE. Internet connection is required.



e<sup>2</sup> studio Integrated Development Environment User's Manual: Getting Started Guide

Rev.	Date	Description		
		Page	Summary	
1.00	Jun.12.19		First Edition issued	
1.01	Mar. 3.20	19-30	Add 'Project Generation of RZ/A2M'	
		64	Add 'How to debug (RZ/A2M)'	
1.02	Sep. 24.20		Updated for e <sup>2</sup> studio 7.8.0 and 2020-04 support	

e <sup>2</sup> studio Integrate	d Developme	ent Environment			
	User's Manual: Getting Started Guide				
		-			
Publication Date:	Rev.1.02	Sep 24, 2020			
		• *			
Published by:	Renesas Electronics Corporation				

e<sup>2</sup> studio

