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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Customer Notification

QB-780822

In-Circuit-Emulator

Operating Precautions

Target Device

uPD78(F)0822TM CAN ASSP3+

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(A) Table of Operating Precautions

No.	Outline	QB-780822			
		Ctrl.-Code	E	F	G
		Date	04.10.04	02.12.04	12.09.05
		IO Version	ES - V1.42	ES - V1.52	V1.63
		Evachip	0003_0041	0003_0041	0000_1056
		CPLD Version	130801903	130801903	130801901
1	P13.0 emulated by CPLD (Technical limitation)	Y	Y	N	
2	TM5 external clock input not supported (Technical limitation)	Y	Y	N	
3	TMP macro of tool is different to device (Technical limitation)	Y	Y	N	
4	Only internal clock supported (Technical limitation)	Y	Y	N	
5	Restricted range of target power (Technical limitation)	Y	Y	N	
6	Restricted range of target power for DCAN function (Technical limitation)	Y	Y	N	
7	Extended identifiers of DCAN macro don't work correctly (Technical limitation)	Y	Y	N	
8	LCD emulation setup restriction for P10/P11 (Direction for use)	N	N	N	
9	SFR write access fail in case an interrupt occurred in parallel (Technical limitation)	Y	N	N	
10	The CPUCKS bit of the ISC register has no effect for Meter Controller and LCD macro (Technical limitation)	Y	Y	N	

N : Not applicable

Y : Applicable

Note: The control code is the second letter from the left of the 10 digit serial number or in case of update the latest control code is mentioned on the version up sticker.

(B) Description of Operating Precautions

No. 1	P13.0 emulated by CPLD
<p><u>Details</u> The P13.0 couldn't be emulated by the real device. The emulation is done by CPLD and a level shifter to the target probe. Because of this the electrical port characteristic may differ from the real device but there is no difference with respect to the functional behaviour.</p> <p><u>Workaround</u> None</p>	

No. 2	TM5 external clock input not supported
<p><u>Details:</u> The external clock input terminal TIO50/P113 for TM5 is not supported.</p> <p><u>Workaround:</u> None</p>	

No. 3	TMP macro of tool is different to device
<p><u>Details:</u> Device V1.2: Synchronized operation mode not implemented, different period of first count clock not fixed. Device V1.3: Synchronized operation mode implemented, different period of first count clock fixed. Tool: Synchronized operation mode implemented, different period of first count clock not fixed.</p> <p><u>Workaround:</u> None</p>	

No. 4	Only internal clock supported
<p><u>Details:</u> None</p> <p><u>Workaround:</u> None</p>	

No. 5	Restricted range of target power
<p><u>Details:</u> The target power range is limited to 4.5V - 5.0V.</p> <p><u>Workaround:</u> None</p>	

Operating Precautions for QB-780822

No. 6	Restricted range of target power for DCAN function
	<i>Details:</i> DCAN macro functionality may fail in case the target power is above 5.0V. <i>Workaround:</i> None
No. 7	Extended identifiers of DCAN macro don't work correctly
	<i>Details:</i> None <i>Workaround:</i> None
No. 8	Wrong frequency selection for LCD and Stepper Motor macros
	<i>Details:</i> None <i>Workaround:</i> None
No. 9	SFR write access fail in case an interrupt occurred in parallel
	<i>Details:</i> Wrong data might be written to a SFR with retry timing, in case an interrupt occurred in parallel. <i>Workaround:</i> None
No. 10	The CPUCKS bit of the ISC register has no effect for Meter Controller and LCD macro
	<i>Details:</i> The clock will not be divided. <i>Workaround:</i> None

(C) Valid Specification

Item	Date published	Document No.	Document Title
1	September 2005	U17172EE1V2UM00	Preliminary User's Manual
2	June 2005	EEDT-OP-0023-9.0	This document
3	April 2005	TPS-LE-OP-0822B or later	Customer Notification uPD780822 and uPD780822B subseries

(D) Revision History

Item	Date published	Document No.	Comment
1	June 2004	EEDT-OP-0023-1.0	1st release
2	July 2004	EEDT-OP-0023-2.0	1st update
3	August 2004	EEDT-OP-0023-3.0	2nd update
4	September 2004	EEDT-OP-0023-4.0	3rd update
5	October 2004	EEDT-OP-0023-5.0	4th update
6	December 2004	EEDT-OP-0023-6.0	5th update
7	January 2005	EEDT-OP-0023-7.0	6th update
8	June 2005	EEDT-OP-0023-8.0	7th update
9	September 2005	EEDT-OP-0023-9.0	8th update