

Ethernet Dual PHY Starter Kit

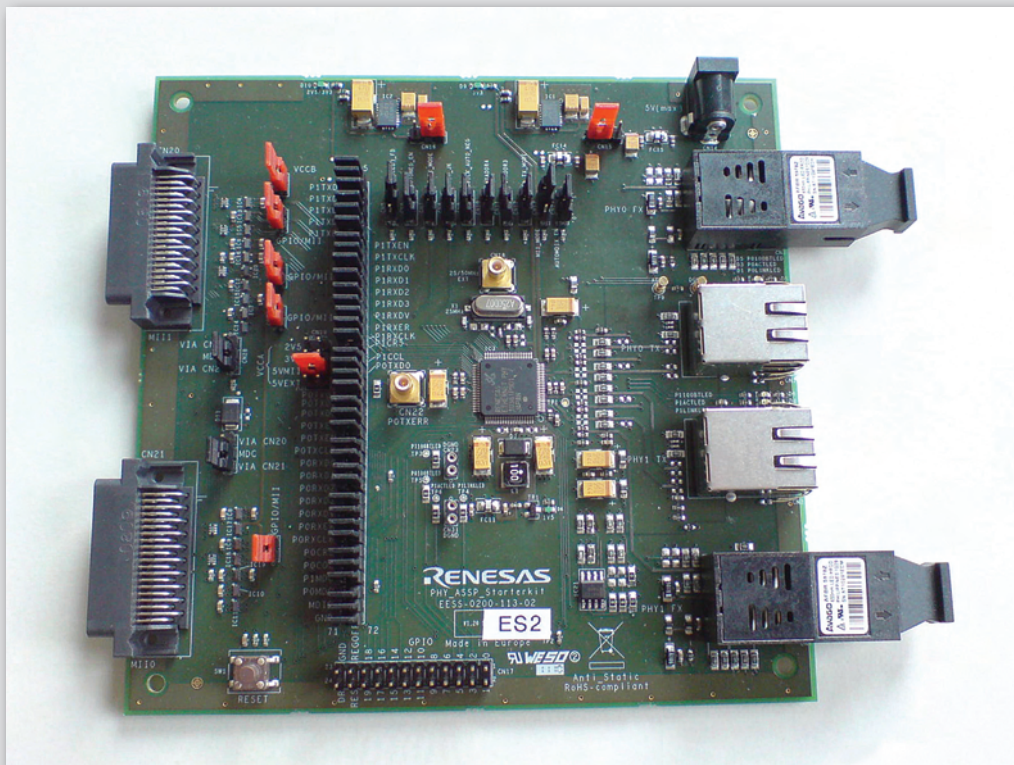
The extended 10/100 Dual Ethernet PHY is a fully integrated physical layer device to connect to standard IEEE802.3 Ethernet networks. The starter kit allows developers to investigate the physical layer for Ethernet communication without designing their own system. Especially applications related to the industrial networking require real time and secure operations. Special features support these requirements and can be tested with this starter kit.

Description of the board

The board gives access to the MII interfaces and the MDI interfaces of the PHY ASSP and supports different configuration options. MII and SMI are prepared to operate in 5 V, 3.3 V as well as 2.5 V environments. This flexibility can be controlled by jumper settings. Also a test connector is available to check and evaluate the signals on this interface.

There is a crystal oscillator available on the board. In some applications it is necessary to use system clocks which can also be inserted by a separate connector. Further various strap pins can be configured to configure the PHY in various modes like auto negotiation, fast link up and several others. On top of this there are various test pins available.

The board supports 100BASE-TX; it is prepared to support 100BASE-FX as well, however customers have to supply the optical transceivers (and some resistors) themselves.



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Key Features

- Low latency and low jitter for industrial networking
- Fast link-down detection
- Permanent cable quality tracking
- Fast link-up option during auto negotiation
- Enhanced system testability (such as bypass, loopback and cable length measurement by TDR)

Content of the Starter Kit

- Starter Kit with a Dual PHY
- CD-ROM with user manuals for the starter kit and for the device

Order Code

SK-ETHER-PHY-ASSP2



Before purchasing or using any Renesas Electronics products listed herein, please refer to the latest product manual and/or data sheet in advance.

