



Integrated Device Technology, Inc.  
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## Errata Notification

EN #: IEN02-20 Errata Revision #: 7/23/2002  
 Issue Date: July 31, 2002 Effective Date: July 23, 2002  
 Product Affected: IDT79RC32V334 and IDT79RC32V332

**ERRATA DETAILS:**

**ATTACHMENT**  Yes  No

- Data Sheet Errata
- Device Errata
- Device Manual Errata
- Application Notes Errata
- Other (Please Specify)

Added Item #26, PCI Bridge Deadlock.

**IDT Web Site Location:**

[http://www.idt.com/products/pages/Integrated\\_Processors-79RC32334.html](http://www.idt.com/products/pages/Integrated_Processors-79RC32334.html)

**CUSTOMER NOTIFICATION:**

As a result of Errata listed above, IDT has decided to notify you through this Errata Notification. We recommend you to review this notification in details prior to the use of affected product in your application. Please use acknowledgement below or E-Mail to request additional information.

*If you have any questions, please feel free to contact your local IDT distributor or sales representative.*

Customer: _____	E-Mail Address: _____
Name/Date: _____	Phone #: _____
Title: _____	Fax #: _____

**CUSTOMER COMMENTS:** \_\_\_\_\_