

Supplemental Information

This Document Errata reflects changes required for the device datasheet of 8430S07AKILF, 8430S10AYILF and 8430S10BYI-02LF.

Revision History

January 4, 2011: First version of documentation errata for the affected devices.

Errata Item

Page 1, Features - **Front Page Bullet**
- Correct one of the bullet descriptions.

CURRENT:

Differential input pair (CLK, nCLK) accepts LVPECL, LVDS, LVHSTL, SSTL, HCSL, input levels.

CHANGE TO:

Differential input pair (CLK, nCLK) accepts LVPECL, LVDS, SSTL input levels.

Page 6, Table 4C - **Differential DC Characteristics**
- Correct the V_{CMR} levels.

CURRENT:

	Minimum	Maximum
Common Mode Input Voltage (V_{CMR})	0.5 V	$V_{DD} - 0.85$ V

CHANGE TO:

	Minimum	Maximum
Common Mode Input Voltage (V_{CMR})	1.2 V	VDD