

DA14592

DA14592 Errata

The document contains the known errata for the DA14592 along with recommended workarounds.

1. Information

Table 1. Information table

Datasheet	DA14592_Datasheet_3v0.pdf
Package(s)	WLCSP39, FCQFN52

2. Identifying the Silicon Revision

Silicon Revision is 0xAE, 0xAF (Register CHIP_REVISION_REG and CHIP_TEST1_REG).

3. Errata Summary

Table 2. Errata summary

Issue #	Issue title
1	Increased current consumption when LDO_IO bypass mode is used.

4. Errata Details

Table 3. Increased current consumption when LDO_IO bypass mode is used

Issue #	Effect
1.	Increased current consumption on V _{BAT} rail.
	Conditions
	When LDO_IO bypass mode is used. (POWER_CTRL_REG[LDO_IO_BYPASS_ACTIVE] = 1, POWER_CTRL_REG[LDO_IO_RET_ENABLE_SLEEP] = 1).
	Technical description
	A hardware design flaw in the LDO_IO bypass mode causes an increased current consumption on V _{BAT} rail. The increased current is in the range of hundreds of μ A (typically around 300 μ A).
	Workaround
	When IOs are required to operate at V _{BAT} level, an external PCB trace (connection) between V _{BAT} and V _{DDIO} is required. LDO_IO should be disabled (POWER_CTRL_REG[LDO_IO_ENABLE] = 0). Note that P0_01 will be pulled up to V _{DDIO} level during hibernation mode.

5. Revision History

Revision	Date	Description
1.0	Mar 19, 2024	Initial release.

STATUS DEFINITIONS

Status	Definition
DRAFT	The content of this document is under review and subject to formal approval, which may result in modifications or additions.
APPROVED or unmarked	The content of this document has been approved for publication.

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