μ**PG2430T6Z**

GaAs Integrated Circuit SP3T Switch for Bluetooth[®] and 802.11a/b/g

R09DS0030EJ0100 Rev.1.00 Oct 24, 2011

DESCRIPTION

The μ PG2430T6Z is a GaAs MMIC SP3T switch which was developed for Bluetooth, wireless LAN. This device can operate at frequencies from 0.5 to 6.0 GHz, with low insertion loss and high isolation. This device is housed in a 8-pin plastic TSON (<u>Thin Small Out-line Non-leaded</u>) package and is suitable for highdensity surface mounting.

FEATURES

- Switch Control voltage : $V_{\text{cont}(H)} = 3.0 \text{ V TYP.}, V_{\text{cont}(L)} = 0 \text{ V TYP.}$
 - : $L_{ins} = 0.55 \text{ dB TYP}$. @ f = 2.5 GHz
 - : $L_{ins} = 0.65 \text{ dB TYP}$. @ f = 6.0 GHz
 - High isolation : ISL = 28 dB TYP. @ f = 2.5 GHz
 - : ISL = 25 dB TYP. @ f = 6.0 GHz
- Handling power : $P_{in (0.1 \text{ dB})} = +28.0 \text{ dBm TYP}$. @ $V_{cont (H)} = 3.0 \text{ V}$, $V_{cont (L)} = 0 \text{ V}$
- High-density surface mounting : 8-pin plastic TSON package $(1.5 \times 1.5 \times 0.37 \text{ mm})$

APPLICATIONS

Low insertion loss

• Bluetooth and IEEE802.11a/b/g etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2430T6Z-E2	μPG2430T6Z-E2-A	8-pin plastic TSON (Pb-Free)	G6L	 Embossed tape 8 mm wide Pin 1, 8 face the perforation side of the tape Qty 3 kpcs/reel

Remark To order evaluation samples, please contact your nearby sales office.

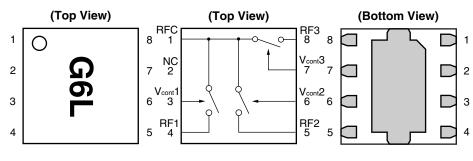
Part number for sample order: μ PG2430T6Z

CAUTION

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.



PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	RFC
2	NC Note
3	V _{cont} 1
4	RF1
5	RF2
6	V _{cont} 2
7	V _{cont} 3
8	RF3

Note: Non-Connection Remark Exposed pad : GND

TRUTH TABLE

V _{cont} 1	V _{cont} 2	V _{cont} 3	RFC-RF1	RFC-RF2	RFC–RF3
High	Low	Low	ON	OFF	OFF
Low	High	Low	OFF	ON	OFF
Low	Low	High	OFF	OFF	ON

ABSOLUTE MAXIMUM RATINGS ($T_A = +25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Ratings	Unit	
Switch Control Voltage	V _{cont}	+6.0 Note	V	
Input Power (V _{cont (H)} = 3.0 V)	Pin	+32	dBm	
Operating Ambient Temperature	T _A	-45 to +85	°C	
Storage Temperature	T _{stg}	-55 to +150	°C	
Note: $ V_{ant} _{0} = V_{ant} _{0} \le 6.0 V$				

Note: $|V_{\text{cont (H)}} - V_{\text{cont (L)}}| \le 6.0 \text{ V}$

RECOMMENDED OPERATING RANGE (T_A = +25^{\circ}C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.5	_	6.0	GHz
Switch Control Voltage (H)	V _{cont (H)}	1.6	3.0	3.6	V
Switch Control Voltage (L)	V _{cont (L)}	-0.2	0	0.2	V
Control Voltage Difference (H)	⊿V _{cont (H)} Note 1	-0.1	0	0.1	V
Control Voltage Difference (L)	∠V _{cont (L)} Note 2	-0.1	0	0.1	V

Notes: 1. $\Delta V_{cont (H)}$ is a difference between the maximum and the minimum control voltages among $V_{cont} 1_{(H)}$, $V_{cont} 2_{(H)}$ and $V_{cont} 3_{(H)}$.

2. $\Delta V_{cont (L)}$ is a difference between the maximum and the minimum control voltages among V_{cont1 (L)}, V_{cont2 (L)} and V_{cont3 (L)}.



ELECTRICAL CHARACTERISTICS 1

 $(T_A = +25^{\circ}C, V_{cont (H)} = 3.0 V, V_{cont (L)} = 0 V, Z_0 = 50 \Omega, DC blocking capacitors = 8 pF, unless otherwise specified)$

Parameter	Symbol	Path	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	L _{ins}	RFC to	f = 0.5 to 1.0 GHz Note 1	-	0.45	0.60	dB
		RF1, 2, 3	f = 1.0 to 2.0 GHz Note 1	-	0.45	0.60	dB
			f = 2.0 to 2.5 GHz	-	0.55	0.70	dB
			f = 2.5 to 4.9 GHz	-	0.60	0.80	dB
			f = 4.9 to 6.0 GHz	-	0.65	0.90	dB
Isolation	ISL	RFC to	f = 0.5 to 1.0 GHz Note 1	24	28	_	dB
		RF1, 2, 3	f = 1.0 to 2.0 GHz Note 1	24	28	_	dB
		(OFF)	f = 2.0 to 2.5 GHz	23	28	_	dB
			f = 2.5 to 4.9 GHz	23	28	_	dB
			f = 4.9 to 6.0 GHz	20	25	_	dB
Return Loss	RL		f = 0.5 to 1.0 GHz Note 1	-	23	_	dB
			f = 1.0 to 2.0 GHz Note 1	16	23	_	dB
			f = 2.0 to 2.5 GHz	16	23	_	dB
			f = 2.5 to 4.9 GHz	16	23	_	dB
			f = 4.9 to 6.0 GHz	10	23	_	dB
0.1 dB Loss Compression	Pin (0.1 dB)	RFC to	f = 2.5 GHz	+25.0	+28.0	_	dBm
Input Power Note 2		RF1, 2, 3	f = 6.0 GHz	+25.0	+28.0	_	dBm
1 dB Loss Compression	Pin (1 dB)	RFC to	f = 2.5 GHz	+28.0	+31.0	_	dBm
Input Power Note 3		RF1, 2, 3	f = 6.0 GHz	+28.0	+31.0	_	dBm
Input 3rd Order Intercept Point	IIP ₃		f = 2.5 GHz, 2 tone,	_	53	_	dBm
			5 MHz spacing				
2nd Harmonics	2f0		f = 2.5 GHz,	-	75	_	dBc
			P _{in} = +22 dBm				
3rd Harmonics	3f0		f = 2.5 GHz,	-	75	_	dBc
			P _{in} = +22 dBm				
Switch Control Current	I _{cont}		No RF input	-	0.1	5.0	μA
Switch Control Speed	t _{sw}		50% CTL to 90/10%	-	50	300	ns
			RF				

Notes: 1. DC blocking capacitors = 56 pF at f = 0.5 to 2.0 GHz

2. P_{in (0.1 dB)} is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.

3. P_{in (1 dB)} is the measured input power level when the insertion loss increases 1 dB more than that of the linear range.

CAUTION

It is necessary to use DC blocking capacitors with this device.



ELECTRICAL CHARACTERISTICS 2

(T_A = +25°C, V_{cont (H)} = 1.8 V, V_{cont (L)} = 0 V, Z_O = 50 Ω , DC blocking capacitors = 8 pF, unless otherwise specified)

Parameter	Symbol	Path	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	L _{ins}	RFC to	f = 0.5 to 1.0 GHz Note 1	-	0.45	0.60	dB
		RF1, 2, 3	f = 1.0 to 2.0 GHz Note 1	-	0.45	0.60	dB
			f = 2.0 to 2.5 GHz	-	0.55	0.70	dB
			f = 2.5 to 4.9 GHz	-	0.60	0.80	dB
			f = 4.9 to 6.0 GHz	-	0.65	0.90	dB
Isolation	ISL	RFC to	f = 0.5 to 1.0 GHz Note 1	24	28	-	dB
		RF1, 2, 3	f = 1.0 to 2.0 GHz Note 1	24	28	-	dB
		(OFF)	f = 2.0 to 2.5 GHz	23	28	-	dB
			f = 2.5 to 4.9 GHz	23	28	-	dB
			f = 4.9 to 6.0 GHz	20	25	-	dB
Return Loss	RL		f = 0.5 to 1.0 GHz ^{Note 1}	-	23	-	dB
			f = 1.0 to 2.0 GHz Note 1	16	23	-	dB
			f = 2.0 to 2.5 GHz	16	23	-	dB
			f = 2.5 to 4.9 GHz	16	23	-	dB
			f = 4.9 to 6.0 GHz	10	23	-	dB
0.1 dB Loss Compression	Pin (0.1 dB)	RFC to	f = 2.5 GHz	+20.0	+23.0	-	dBm
Input Power Note 2		RF1, 2, 3	f = 6.0 GHz	+19.0	+22.0	-	dBm
1 dB Loss Compression	Pin (1 dB)	RFC to	f = 2.5 GHz	+24.0	+27.0	-	dBm
Input Power Note 3		RF1, 2, 3	f = 6.0 GHz	+22.0	+25.0	-	dBm
Input 3rd Order Intercept Point	IIP ₃		f = 2.5 GHz, 2 tone,		50	-	dBm
			5 MHz spacing				
2nd Harmonics	2f0		f = 2.5 GHz,	-	75	-	dBc
			P _{in} = +17 dBm				
3rd Harmonics	3f0		f = 2.5 GHz,	-	75	-	dBc
			P _{in} = +17 dBm				
Switch Control Current	I _{cont}		No RF input	-	0.1	5.0	μA
Switch Control Speed	t _{sw}		50% CTL to 90/10%	-	100	600	ns
			RF				

Notes: 1. DC blocking capacitors = 56 pF at f = 0.5 to 2.0 GHz

2. P_{in (0.1 dB)} is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.

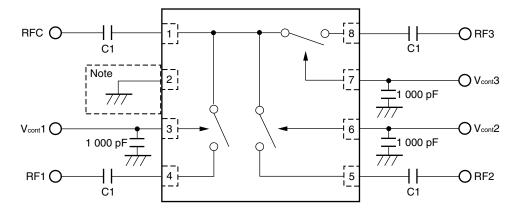
3. P_{in (1 dB)} is the measured input power level when the insertion loss increases 1 dB more than that of the linear range.

CAUTION

It is necessary to use DC blocking capacitors with this device.



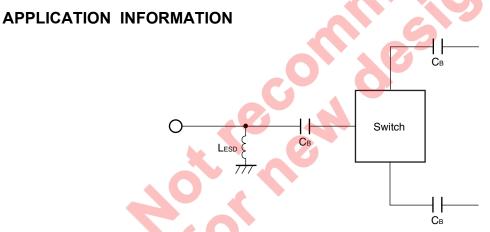
EVALUATION CIRCUIT



Note: It is recommended to connect the pin directly to the ground, or not to connect the pin to anything.

Remarks	C1 : 0.5 to 2.0 GHz	56 pF
	: 2.0 to 6.0 GHz	8 pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

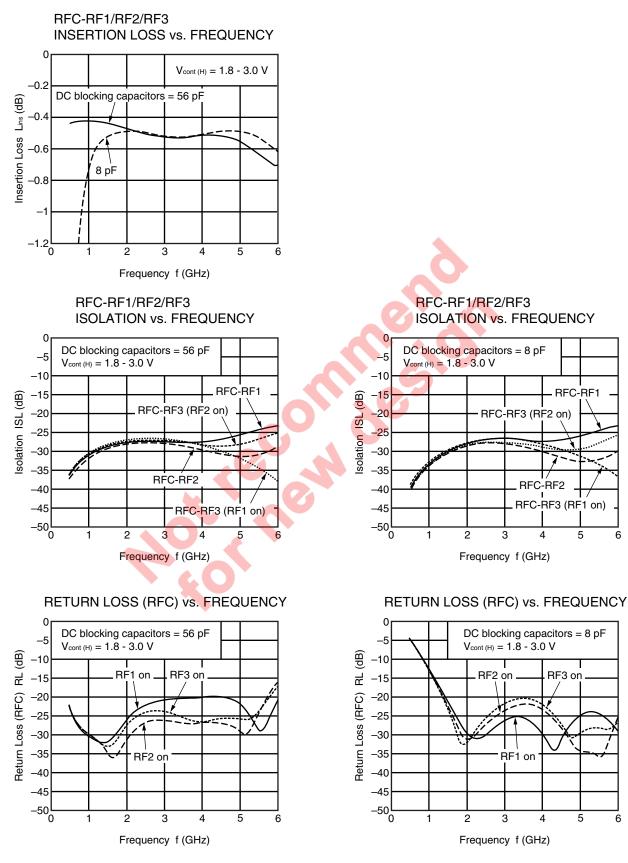


- C_B are DC blocking capacitors external to the device.
 A value of 8 pF is sufficient for operation from 2 GHz to 6 GHz bands.
 The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.
- L_{ESD} provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.



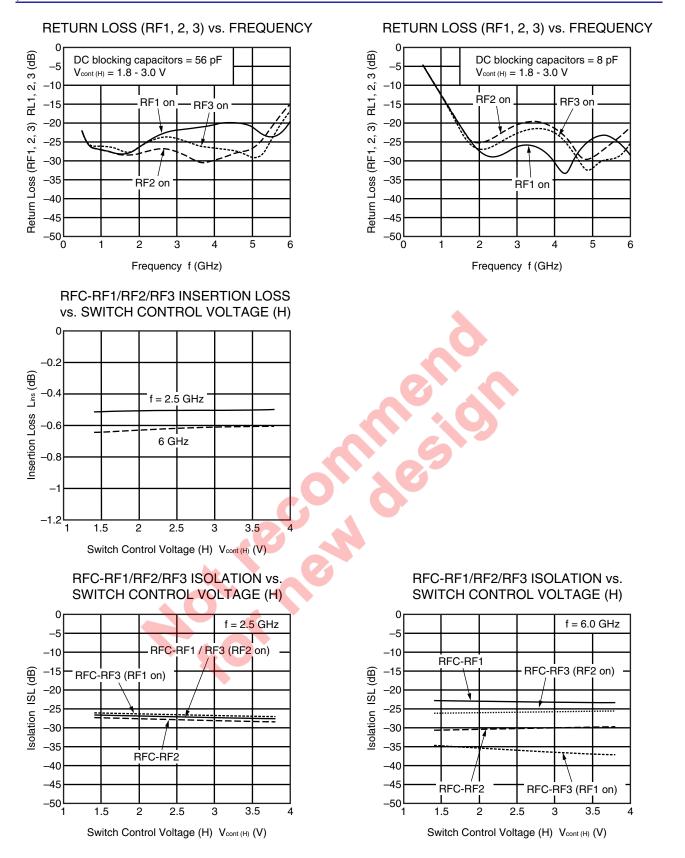
TYPICAL CHARACTERISTICS

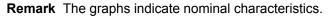
 $(V_{cont (H)} = 3.0 \text{ V}, V_{cont (L)} = 0 \text{ V}, Z_0 = 50 \Omega$, DC blocking capacitors = 8 pF, unless otherwise specified)

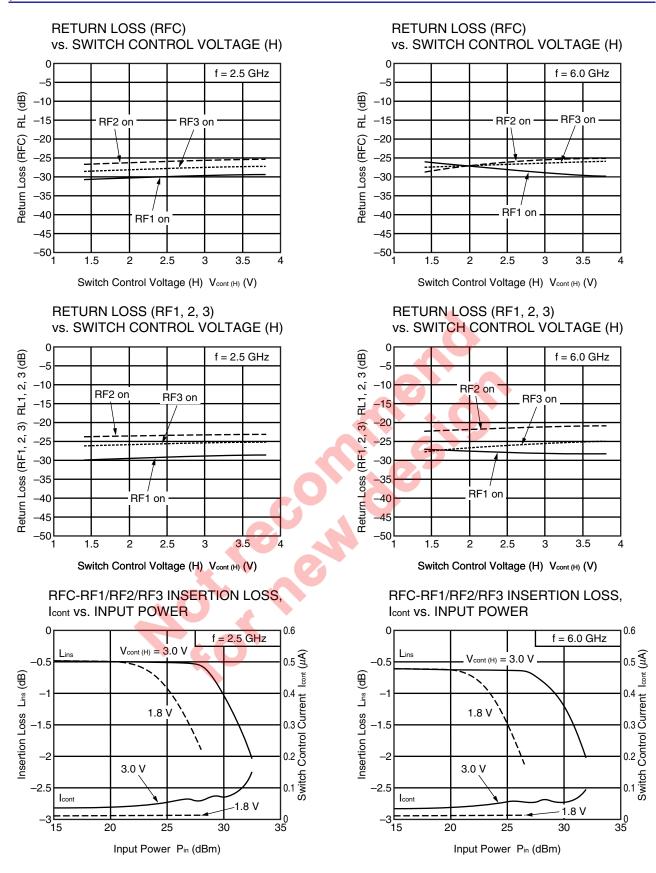


Remark The graphs indicate nominal characteristics.

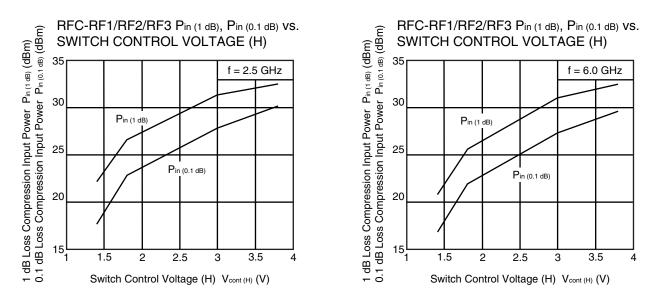










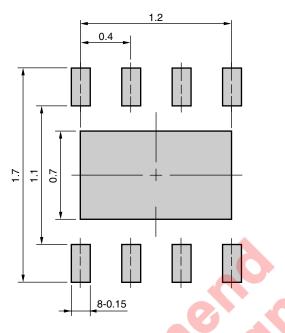


Remark The graphs indicate nominal characteristics.



MOUNTING PAD LAYOUT DIMENSIONS

8-PIN PLASTIC TSON (UNIT: mm)

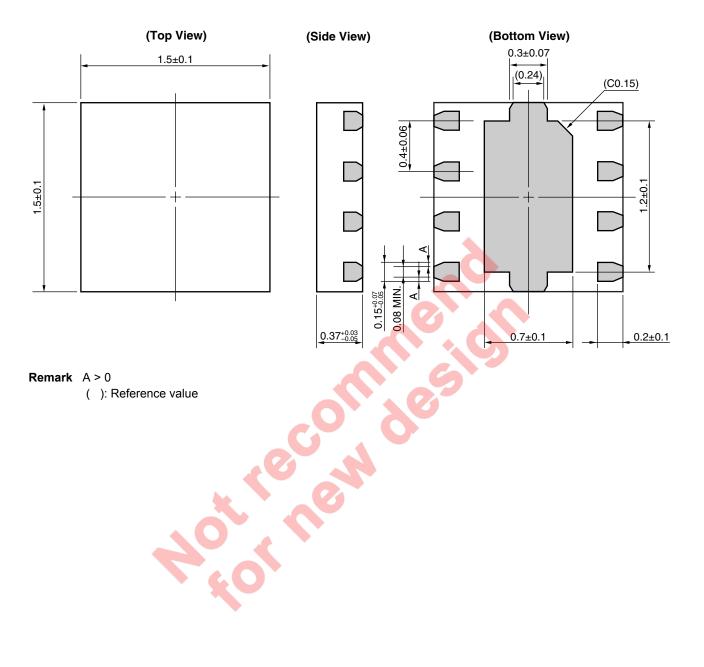


Remark The mounting pad layout in this document is for reference only. When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.



PACKAGE DIMENSIONS

8-PIN PLASTIC TSON (UNIT: mm)





RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times	IR260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less	HS350

CAUTION

Do not use different soldering methods together (except for partial heating).



Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.





Revision	History
	INSLUIY

			Description		
Rev.	Date	Page	Page Summary		
1.00	Oct 24, 2011	-	First edition issued		



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