

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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## GaAs INTEGRATED CIRCUIT

# μPG2413T6M

### SP3T SWITCH FOR Bluetooth™ AND 802.11b/g

#### DESCRIPTION

The μPG2413T6M is a GaAs MMIC SP3T switch which was developed for Bluetooth, wireless LAN.

This device can operate frequencies from 0.5 to 3.0 GHz, with low insertion loss.

This device is housed in a 12-pin plastic TSQFN (Thin Small Quad Flat Non-leaded) (T6M) package and is suitable for high-density surface mounting.

#### FEATURES

- Switch Control voltage :  $V_{\text{cont}}(\text{H}) = 3.0 \text{ V TYP.}$ ,  $V_{\text{cont}}(\text{L}) = 0 \text{ V TYP.}$
- Low insertion loss :  $L_{\text{ins}} = 0.35 \text{ dB TYP. @ } f = 1.0 \text{ GHz}$   
:  $L_{\text{ins}} = 0.45 \text{ dB TYP. @ } f = 2.0 \text{ GHz}$   
:  $L_{\text{ins}} = 0.50 \text{ dB TYP. @ } f = 2.5 \text{ GHz}$
- High isolation :  $\text{ISL} = 26 \text{ dB TYP. @ } f = 1.0 \text{ GHz}$   
:  $\text{ISL} = 20 \text{ dB TYP. @ } f = 2.0 \text{ GHz}$   
:  $\text{ISL} = 18 \text{ dB TYP. @ } f = 2.5 \text{ GHz}$
- Handling power :  $P_{\text{in}}(0.1 \text{ dB}) = +28.0 \text{ dBm TYP. @ } f = 2.5 \text{ GHz}$ ,  $V_{\text{cont}}(\text{H}) = 3.0 \text{ V}$ ,  $V_{\text{cont}}(\text{L}) = 0 \text{ V}$
- High-density surface mounting : 12-pin plastic TSQFN (T6M) package ( $2.0 \times 2.0 \times 0.37 \text{ mm}$ )

#### APPLICATIONS

- Bluetooth and IEEE802.11b/g etc.

#### ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2413T6M-E2	μPG2413T6M-E2-A	12-pin plastic TSQFN (T6M) (Pb-Free)	2413	<ul style="list-style-type: none"> <li>Embossed tape 8 mm wide</li> <li>Pin 10, 11, 12 face the perforation side of the tape</li> <li>Qty 3 kpcs/reel</li> </ul>

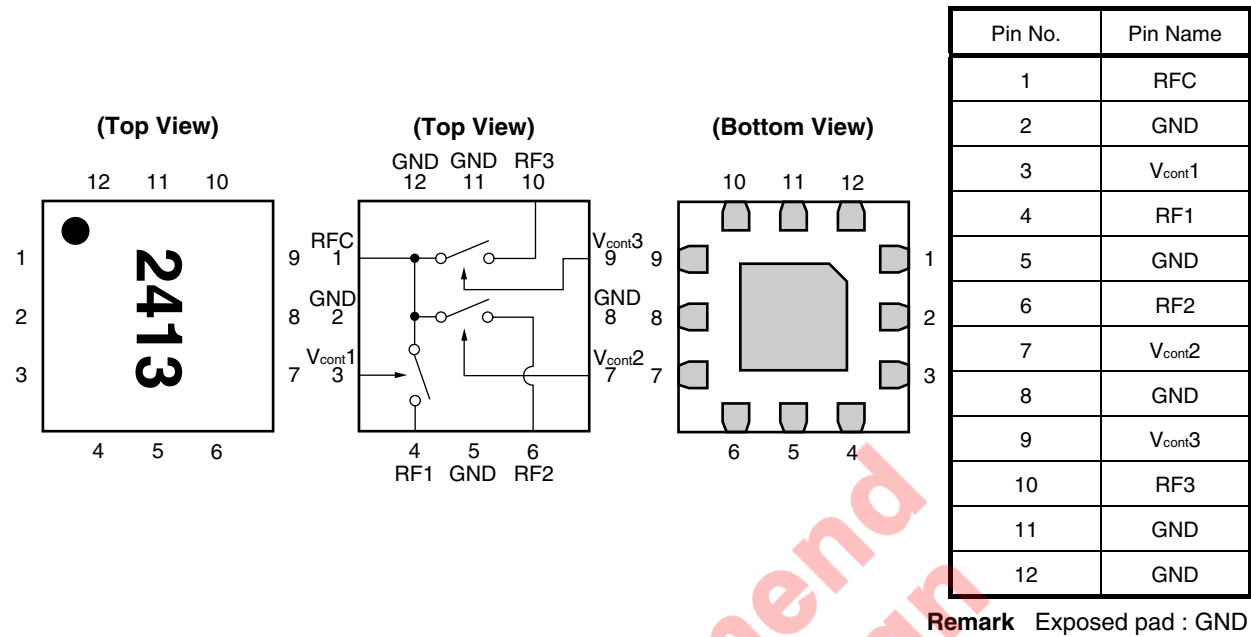
**Remark** To order evaluation samples, please contact your nearby sales office.

Part number for sample order: μPG2413T6M

**Caution** Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

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PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



TRUTH TABLE

V <sub>cont1</sub>	V <sub>cont2</sub>	V <sub>cont3</sub>	RFC–RF1	RFC–RF2	RFC–RF3
High	Low	Low	ON	OFF	OFF
Low	High	Low	OFF	ON	OFF
Low	Low	High	OFF	OFF	ON

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V <sub>cont</sub>	+6.0 <sup>Note</sup>	V
Input Power (V <sub>cont</sub> (H) = 1.8 V)	P <sub>in</sub>	+26	dBm
Input Power (V <sub>cont</sub> (H) = 2.3 V)	P <sub>in</sub>	+28	dBm
Input Power (V <sub>cont</sub> (H) = 3.0 V)	P <sub>in</sub>	+32	dBm
Input Power (V <sub>cont</sub> (H) = 3.6 V)	P <sub>in</sub>	+34	dBm
Operating Ambient Temperature	T <sub>A</sub>	−45 to +85	°C
Storage Temperature	T <sub>stg</sub>	−55 to +150	°C

**Note** | V<sub>cont</sub> (H) − V<sub>cont</sub> (L) | ≤ 6.0 V

**RECOMMENDED OPERATING RANGE (T<sub>A</sub> = +25°C)**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.5	–	3.0	GHz
Switch Control Voltage (H)	V <sub>cont</sub> (H)	1.8	3.0	3.6	V
Switch Control Voltage (L)	V <sub>cont</sub> (L)	−0.2	0	0.2	V
Control Voltage Difference (H)	ΔV <sub>cont</sub> (H) <sup>Note1</sup>	−0.1	0	0.1	V
Control Voltage Difference (L)	ΔV <sub>cont</sub> (L) <sup>Note2</sup>	−0.1	0	0.1	V

**Notes 1.** ΔV<sub>cont</sub> (H) is a difference between the maximum and the minimum control voltages among V<sub>cont</sub>1 (H), V<sub>cont</sub>2 (H) and V<sub>cont</sub>3 (H).

**2.** ΔV<sub>cont</sub> (L) is a difference between the maximum and the minimum control voltages among V<sub>cont</sub>1 (L), V<sub>cont</sub>2 (L) and V<sub>cont</sub>3 (L).

# ELECTRICAL CHARACTERISTICS 1

(T<sub>A</sub> = +25°C, V<sub>cont</sub> (H) = 3.0 V, V<sub>cont</sub> (L) = 0 V, Z<sub>o</sub> = 50  $\Omega$ , DC blocking capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Pass	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	L <sub>ins</sub>	RFC to RF1, 2, 3	f = 0.5 to 1.0 GHz	–	0.35	0.60	dB
			f = 1.0 to 2.0 GHz	–	0.45	0.70	dB
			f = 2.0 to 2.5 GHz	–	0.50	0.75	dB
			f = 2.5 to 3.0 GHz	–	0.60	–	dB
Isolation	ISL	RFC to RF1, 2, 3 (OFF)	f = 0.5 to 1.0 GHz	23	26	–	dB
			f = 1.0 to 2.0 GHz	17	20	–	dB
			f = 2.0 to 2.5 GHz	15	18	–	dB
			f = 2.5 to 3.0 GHz	–	16	–	dB
Return Loss (RFC)	RL <sub>c</sub>		f = 0.5 to 3.0 GHz	15	20	–	dB
Return Loss (RF1, 2, 3)	RL <sub>1, 2, 3</sub>		f = 0.5 to 3.0 GHz	15	20	–	dB
0.1 dB Loss Compression Input Power <sup>Note 1</sup>	P <sub>in</sub> (0.1 dB)	RFC to RF1, 2, 3	f = 2.5 GHz	+25.0	+28.0	–	dBm
1 dB Loss Compression Input Power <sup>Note 2</sup>	P <sub>in</sub> (1 dB)	RFC to RF1, 2, 3	f = 2.5 GHz, V <sub>cont</sub> (H) = 2.3 V	–	+27.0	–	dBm
			f = 2.5 GHz, V <sub>cont</sub> (H) = 3.0 V	–	+31.0	–	dBm
			f = 2.5 GHz, V <sub>cont</sub> (H) = 3.6 V	–	+33.0	–	dBm
2nd Harmonics	2f <sub>0</sub>		f = 2.5 GHz, P <sub>in</sub> = 23 dBm	–	75	–	dBc
3rd Harmonics	3f <sub>0</sub>		f = 2.5 GHz, P <sub>in</sub> = 23 dBm	–	75	–	dBc
Switch Control Current	I <sub>cont</sub>		No RF input	–	0.1	5.0	$\mu$ A
Switch Control Speed	t <sub>sw</sub>		50% CTL to 90/10% RF	–	50	–	ns

**Notes 1.** P<sub>in</sub> (0.1 dB) is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.

**2.** P<sub>in</sub> (1 dB) is the measured input power level when the insertion loss increases 1 dB more than that of the linear range.

**Caution** It is necessary to use DC blocking capacitors with this device.

## ELECTRICAL CHARACTERISTICS 2

( $T_A = +25^\circ\text{C}$ ,  $V_{\text{cont}}(\text{H}) = 1.8\text{ V}$ ,  $V_{\text{cont}}(\text{L}) = 0\text{ V}$ ,  $Z_0 = 50\ \Omega$ , DC blocking capacitors = 56 pF, unless otherwise specified)

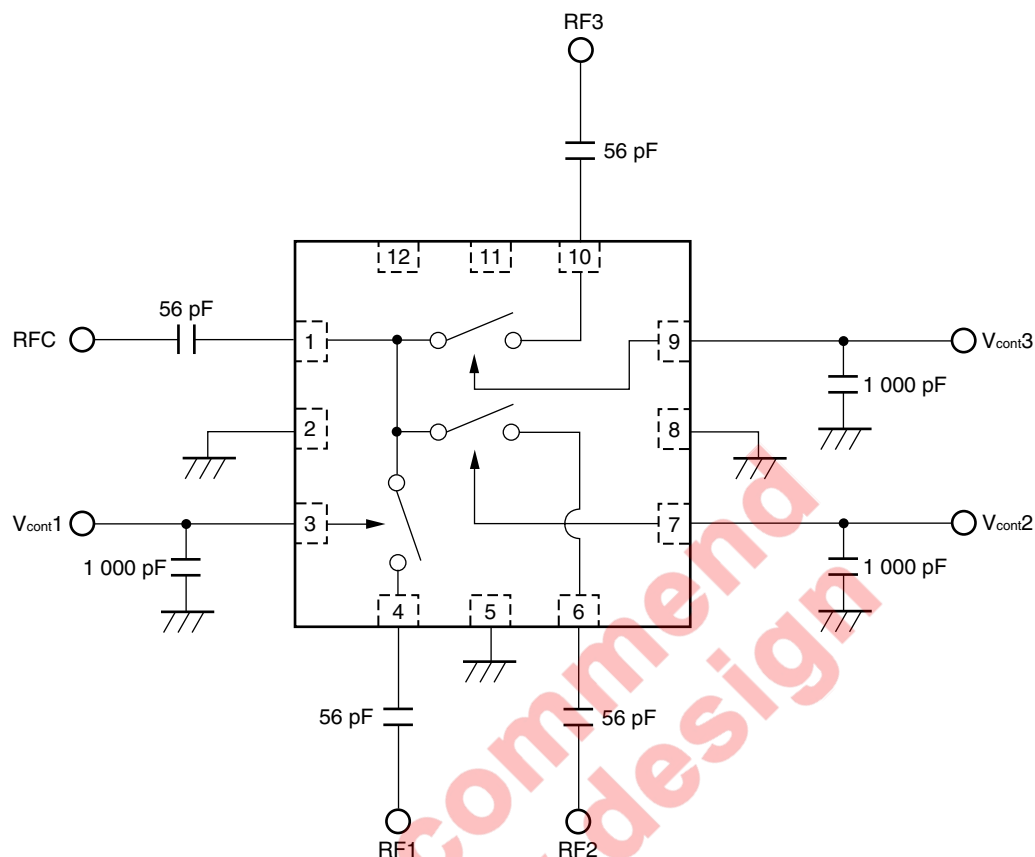
Parameter	Symbol	Pass	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	$L_{\text{ins}}$	RFC to RF1, 2, 3	$f = 0.5\text{ to }1.0\text{ GHz}$	–	0.35	0.65	dB
			$f = 1.0\text{ to }2.0\text{ GHz}$	–	0.45	0.75	dB
			$f = 2.0\text{ to }2.5\text{ GHz}$	–	0.50	0.80	dB
			$f = 2.5\text{ to }3.0\text{ GHz}$	–	0.65	–	dB
Isolation	ISL	RFC to RF1, 2, 3 (OFF)	$f = 0.5\text{ to }1.0\text{ GHz}$	22.5	25.5	–	dB
			$f = 1.0\text{ to }2.0\text{ GHz}$	16.5	19.5	–	dB
			$f = 2.0\text{ to }2.5\text{ GHz}$	14.5	17.5	–	dB
			$f = 2.5\text{ to }3.0\text{ GHz}$	–	15.5	–	dB
Return Loss (RFC)	$RL_c$		$f = 0.5\text{ to }3.0\text{ GHz}$	15	20	–	dB
Return Loss (RF1, 2, 3)	$RL_{1, 2, 3}$		$f = 0.5\text{ to }3.0\text{ GHz}$	15	20	–	dB
0.1 dB Loss Compression Input Power <sup>Note 1</sup>	$P_{\text{in}}(0.1\text{ dB})$	RFC to RF1, 2, 3	$f = 2.5\text{ GHz}$	+19.0	+22.0	–	dBm
1 dB Loss Compression Input Power <sup>Note 2</sup>	$P_{\text{in}}(1\text{ dB})$	RFC to RF1, 2, 3	$f = 2.5\text{ GHz}$	+21.0	+25.0	–	dBm
2nd Harmonics	$2f_0$		$f = 2.5\text{ GHz}$ , $P_{\text{in}} = 17\text{ dBm}$	–	75	–	dBc
3rd Harmonics	$3f_0$		$f = 2.5\text{ GHz}$ , $P_{\text{in}} = 17\text{ dBm}$	–	75	–	dBc
Switch Control Current	$I_{\text{cont}}$		No RF input	–	0.1	5.0	$\mu\text{A}$
Switch Control Speed	$t_{\text{sw}}$		50% CTL to 90/10% RF	–	50	–	ns

**Notes 1.**  $P_{\text{in}}(0.1\text{ dB})$  is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.

**2.**  $P_{\text{in}}(1\text{ dB})$  is the measured input power level when the insertion loss increases 1 dB more than that of the linear range.

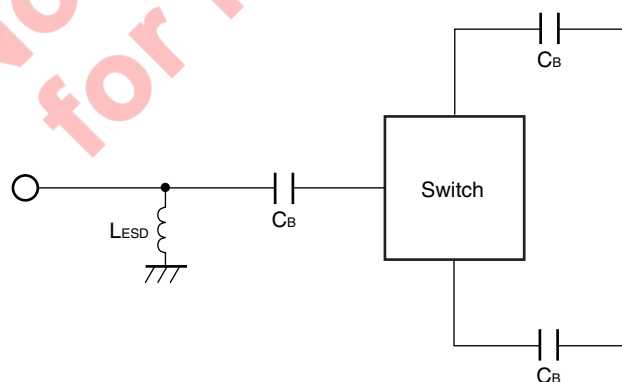
**Caution** It is necessary to use DC blocking capacitors with this device.

## EVALUATION CIRCUIT



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

## APPLICATION INFORMATION

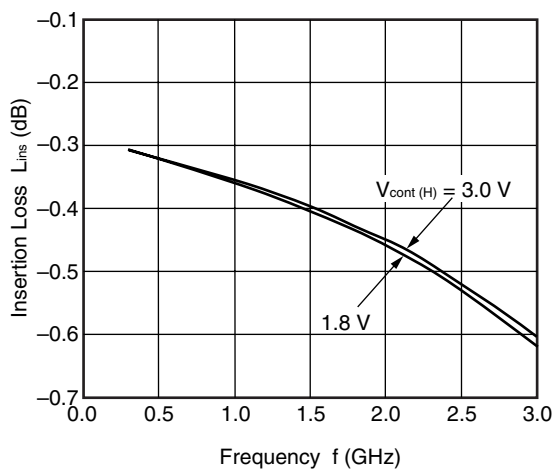


- $C_B$  are DC blocking capacitors external to the device.  
A value of 56 pF is sufficient for operation from 500 MHz to 2.5 GHz bands.  
The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.
- $L_{ESD}$  provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.

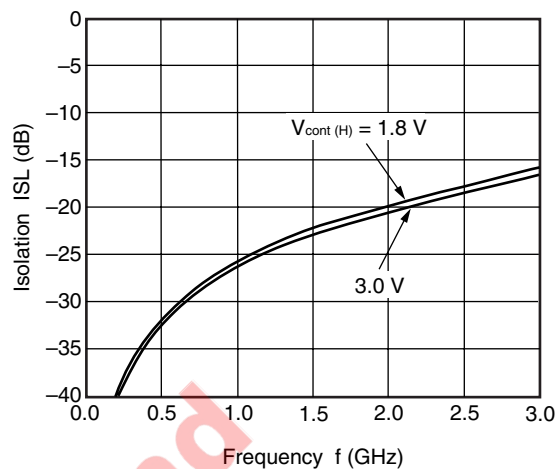


**TYPICAL CHARACTERISTICS** ( $T_A = +25^\circ\text{C}$ , DC blocking capacitors = 56 pF, unless otherwise specified)

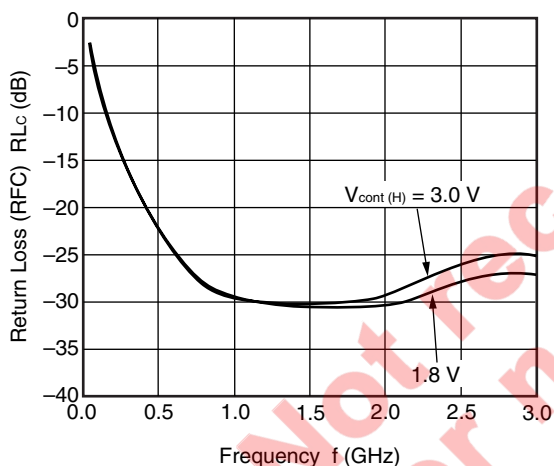
RFC-RF1/RF2/RF3  
INSERTION LOSS vs. FREQUENCY



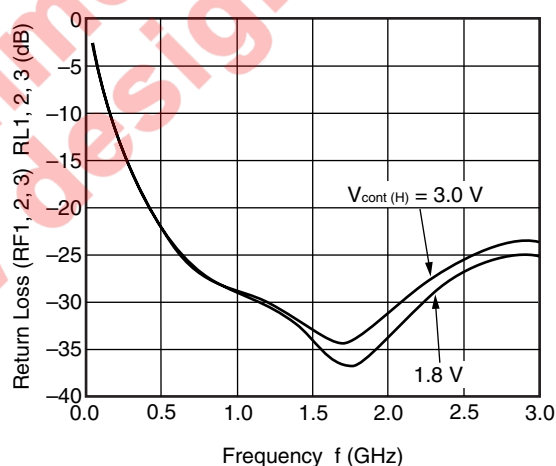
RFC-RF1/RF2/RF3  
ISOLATION vs. FREQUENCY



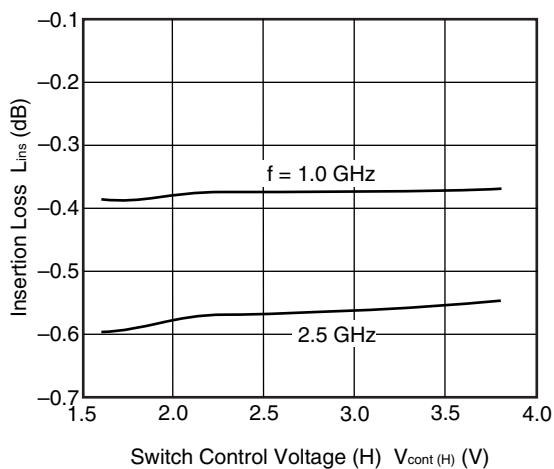
RETURN LOSS (RFC) vs. FREQUENCY



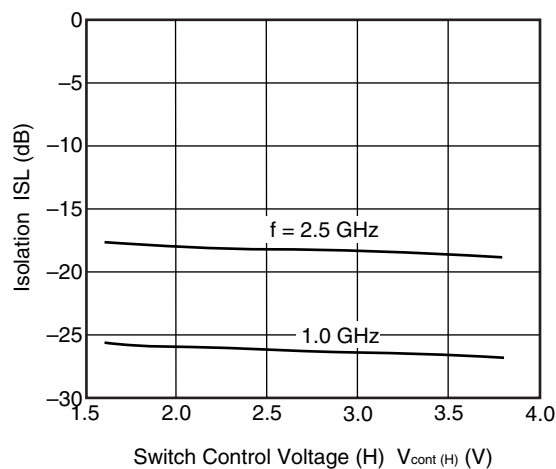
RETURN LOSS (RF1, 2, 3) vs. FREQUENCY



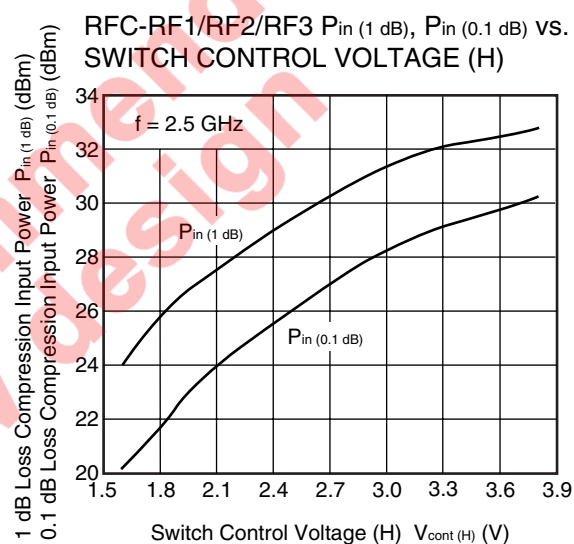
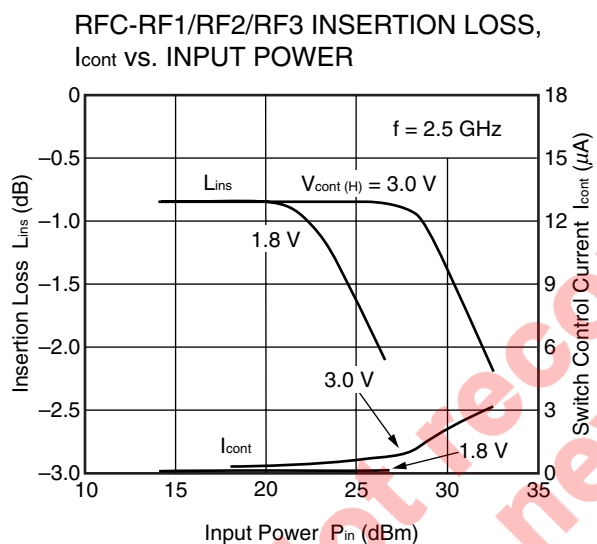
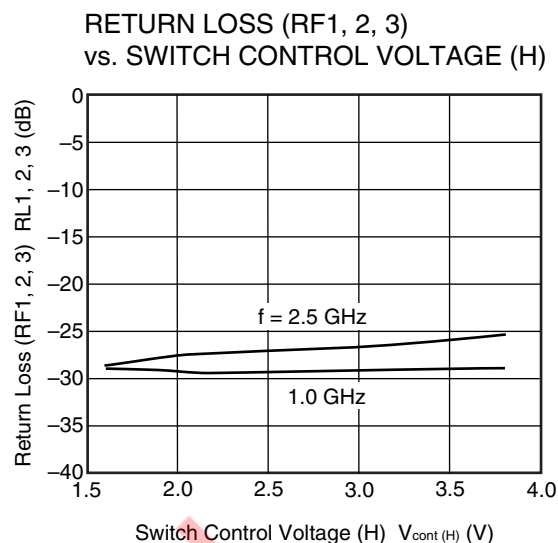
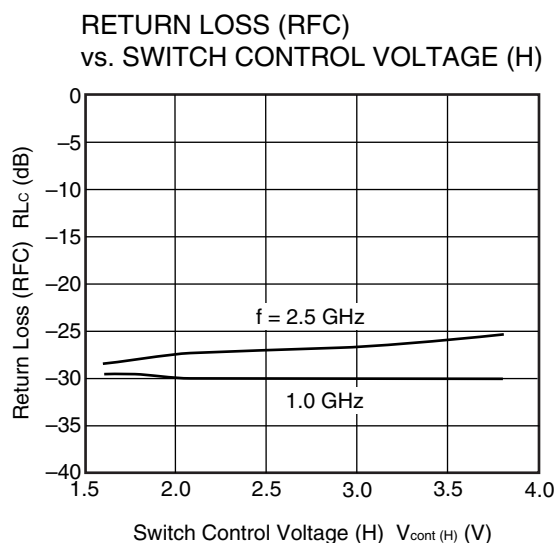
RFC-RF1/RF2/RF3 INSERTION LOSS  
vs. SWITCH CONTROL VOLTAGE (H)



RFC-RF1/RF2/RF3 ISOLATION vs.  
SWITCH CONTROL VOLTAGE (H)



**Remark** The graphs indicate nominal characteristics.

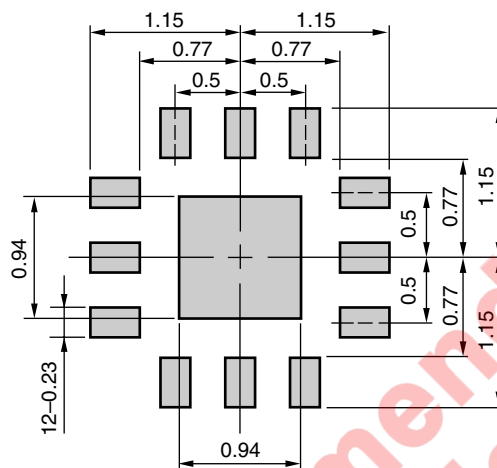


**Remark** The graphs indicate nominal characteristics.

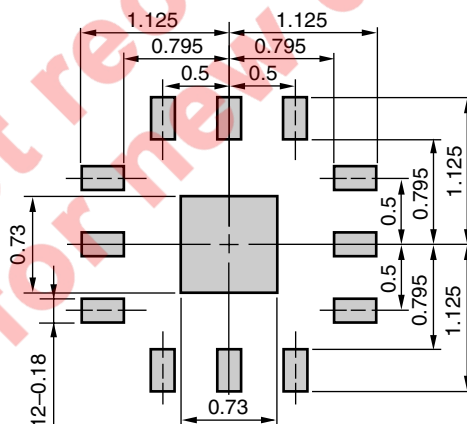
# MOUNTING PAD AND SOLDER MASK LAYOUT DIMENSIONS

12-PIN PLASTIC TSQFN (T6M) (UNIT: mm)

## MOUNTING PAD



## SOLDER MASK

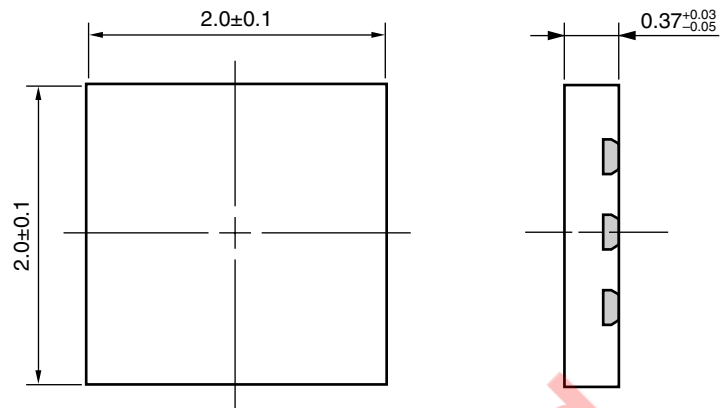


Solder thickness : 0.1 mm

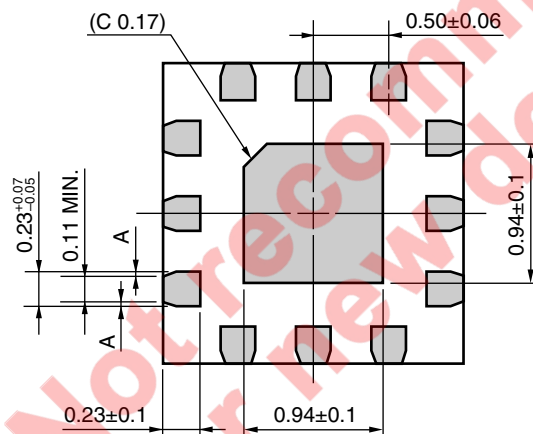
**Remark** The mounting pad and solder mask layouts in this document are for reference only.  
When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.

## PACKAGE DIMENSIONS

### 12-PIN PLASTIC TSQFN (T6M) (UNIT: mm)



**(Bottom View)**



**Remark**  $A > 0$

( ): Reference value

# RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

**Caution** Do not use different soldering methods together (except for partial heating).

Not recommended  
for new design

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M8E0904E

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Not recommend  
for new design