

To our customers,

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## Old Company Name in Catalogs and Other Documents

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April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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**Phase-out/Discontinued**

## L-BAND PA DRIVER AMPLIFIER

### DESCRIPTION

The μPG2141T5B is a GaAs MMIC for PA driver amplifier which was developed for mobile phone and another L-band application. This device realizes high gain and low distortion.

This device is housed in a 6-pin LGA (CSP Type) package. And this package is able to high-density surface mounting.

### FEATURES

- Operation frequency :  $f_{opt} = 1\,429$  to  $1\,453$  MHz (1 441 MHz TYP.)
- Supply voltage :  $V_{DD1} = 2.7$  to  $3.0$  V (2.8 V TYP.)  
:  $V_{DD2} = 3.0$  to  $3.5$  V (3.2 V TYP.)
- Circuit current :  $I_{DD} = 28$  mA TYP. @  $V_{DD1} = 2.8$  V,  $V_{DD2} = 3.2$  V,  $V_{AGC} = 2.0$  V,  $P_{in} = -17$  dBm
- Power Gain :  $G_P = 28$  dB TYP. @  $V_{DD1} = 2.8$  V,  $V_{DD2} = 3.2$  V,  $V_{AGC} = 2.0$  V,  $P_{in} = -17$  dBm
- Gain control range :  $GCR = 42$  dB TYP. @  $V_{DD1} = 2.8$  V,  $V_{DD2} = 3.2$  V,  $V_{AGC} = 0.5$  to  $2.0$  V,  $P_{in} = -17$  dBm
- Low distortion :  $P_{adj1} = -60$  dBc TYP. @  $V_{DD1} = 2.8$  V,  $V_{DD2} = 3.2$  V,  $V_{AGC} = 2.0$  V,  $P_{in} = +12$  dBm,  $f = 1\,429$  to  $1\,453$  MHz,  $\Delta f = \pm 50$  kHz, 21 kHz Band width
- High-density surface mounting : 6-pin LGA (CSP Type) package ( $1.5 \times 1.5 \times 0.6$  mm)

### APPLICATION

- Digital cellular : PDC 1.5 GHz etc.

### ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
μPG2141T5B-E1	6-pin LGA (CSP Type)	G4E	<ul style="list-style-type: none"> <li>• Embossed tape 8 mm wide</li> <li>• Pin 3, 4 face the perforation side of the tape</li> <li>• Qty 3 kpcs/reel</li> </ul>

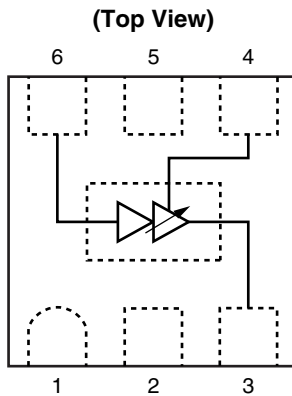
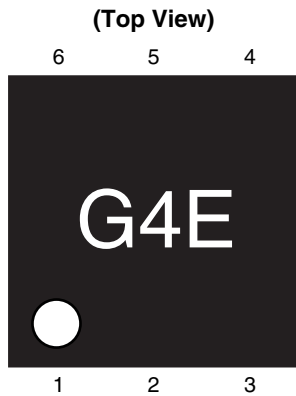
**Remark** To order evaluation samples, contact your nearby sales office.

Part number for sample order: μPG2141T5B

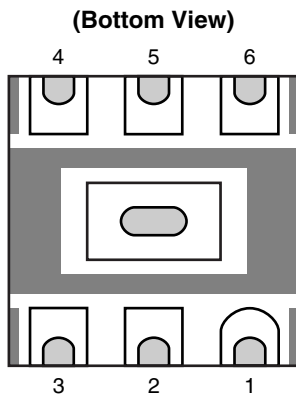
**Caution** Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

**PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM**



Pin No.	Pin Name
1	V <sub>DD1</sub>
2	GND
3	OUTPUT/V <sub>DD2</sub>
4	V <sub>AGC</sub>
5	GND
6	INPUT



**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	Ratings	Unit
Supply Voltage <sub>1, 2</sub>	V <sub>DD1, 2</sub>	6.0	V
Gain Control Voltage	V <sub>AGC</sub>	6.0	V
Input Power	P <sub>in</sub>	0	dBm
Power Dissipation	P <sub>D</sub>	140 <sup>Note</sup>	mW
Operating Ambient Temperature	T <sub>A</sub>	-30 to +85	°C
Storage Temperature	T <sub>stg</sub>	-35 to +150	°C

**Note** Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB, T<sub>A</sub> = +85°C

**RECOMMENDED OPERATING RANGE (T<sub>A</sub> = +25°C, unless otherwise specified)**

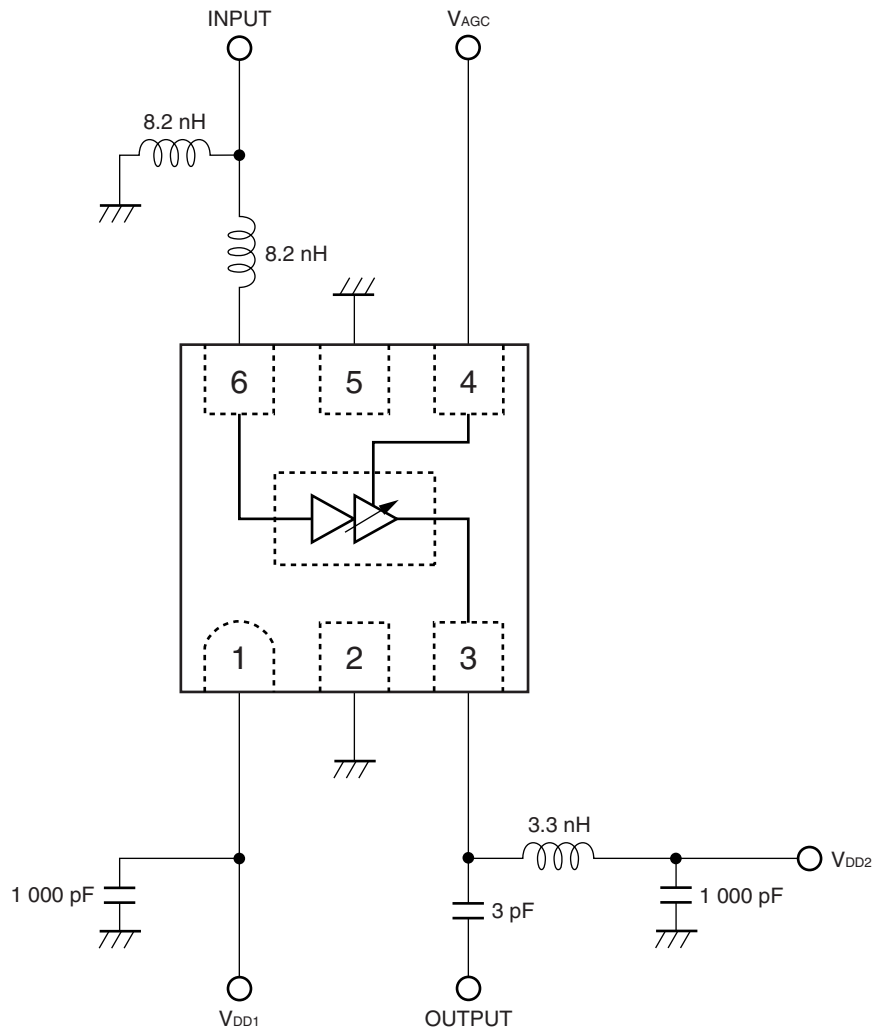
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operation Frequency	f <sub>out</sub>	1 429	1 441	1 453	MHz
Supply Voltage 1	V <sub>DD1</sub>	2.7	2.8	3.0	V
Supply Voltage 2	V <sub>DD2</sub>	3.0	3.2	3.5	V
Gain Control Voltage	V <sub>AGC</sub>	0	-	2.5	V
Input Power	P <sub>in</sub>	-	-17	-10	dBm

**ELECTRICAL CHARACTERISTICS**

( $T_A = +25^\circ\text{C}$ ,  $V_{DD1} = 2.8\text{ V}$ ,  $V_{DD2} = 3.2\text{ V}$ ,  $\pi/4\text{DQPSK}$  modulated signal input, with external input and output matching, unless otherwise specified)

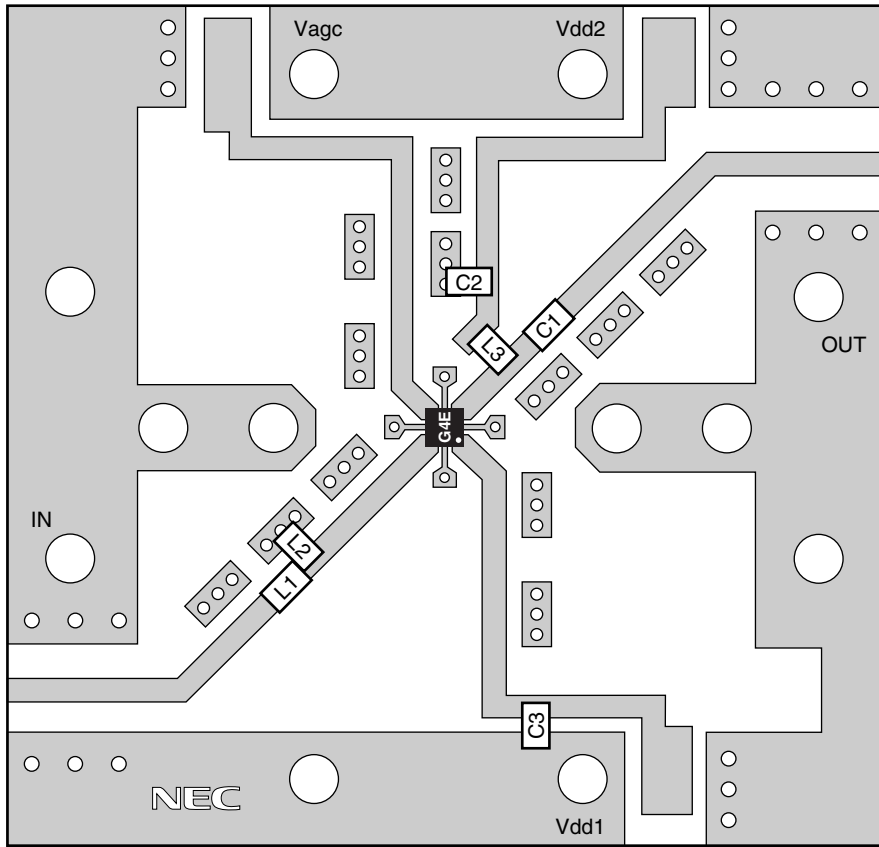
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Operating Frequency	$f_{opt}$		1 429	1 441	1 453	MHz
Circuit Current	$I_{BD}$	$P_{in} = -17\text{ dBm}$ , $V_{AGC} = 2.0\text{ V}$	–	28	35	mA
Power Gain	$G_P$	$P_{in} = -17\text{ dBm}$ , $V_{AGC} = 2.0\text{ V}$	26	28	–	dB
Gain Control Range	GCR	$P_{in} = -17\text{ dBm}$ , $V_{AGC} = 0.5\text{ to }2.0\text{ V}$	37	42	–	dB
Adjacent Channel Power Leakage1	$P_{adj1}$	$P_{out} = +12\text{ dBm}$ , $V_{AGC} = 2.0\text{ V}$ , $\Delta f = \pm 50\text{ kHz}$ , 21 kHz Band width	–	–60	–55	dBc
Adjacent Channel Power Leakage2	$P_{adj2}$	$P_{out} = +12\text{ dBm}$ , $V_{AGC} = 2.0\text{ V}$ , $\Delta f = \pm 100\text{ kHz}$ , 21 kHz Band width	–	–70	–65	dBc
Gain Control Current	$I_{AGC}$	$V_{AGC} = 0.5\text{ to }2.0\text{ V}$	–	110	200	$\mu\text{A}$

EVALUATION CIRCUIT ( $f = 1\ 429$  to  $1\ 453$  MHz,  $V_{DD1} = 2.8$  V,  $V_{DD2} = 3.2$  V,  $V_{AGC} = 0.5$  to  $2.0$  V)



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



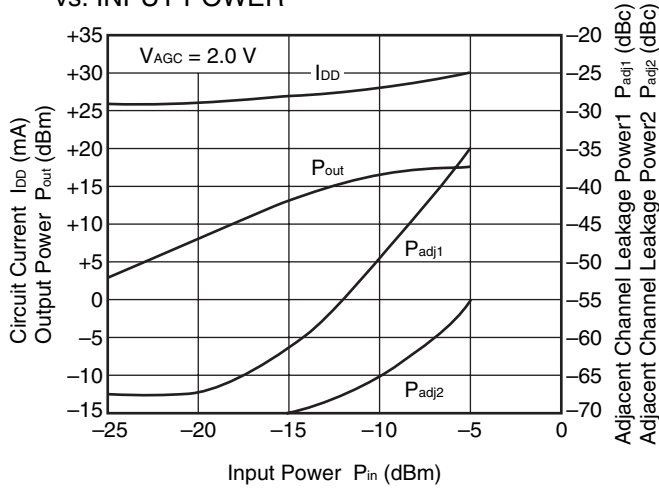
USING THE NEC EVALUATION BOARD

Symbol	Values
L1, L2	8.2 nH
L3	3.3 nH
C1	3 pF
C2, C3	1 000 pF

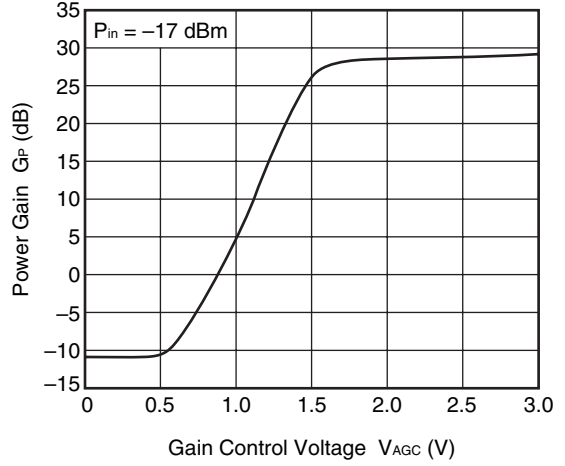
**TYPICAL CHARACTERISTICS**

( $T_A = +25^\circ\text{C}$ ,  $f = 1\,453\text{ MHz}$ ,  $V_{DD1} = 2.8\text{ V}$ ,  $V_{DD2} = 3.2\text{ V}$ , unless otherwise specified)

CIRCUIT CURRENT, OUTPUT POWER, ADJACENT CHANNEL LEAKAGE POWER1, 2 vs. INPUT POWER



POWER GAIN vs. GAIN CONTROL VOLTAGE



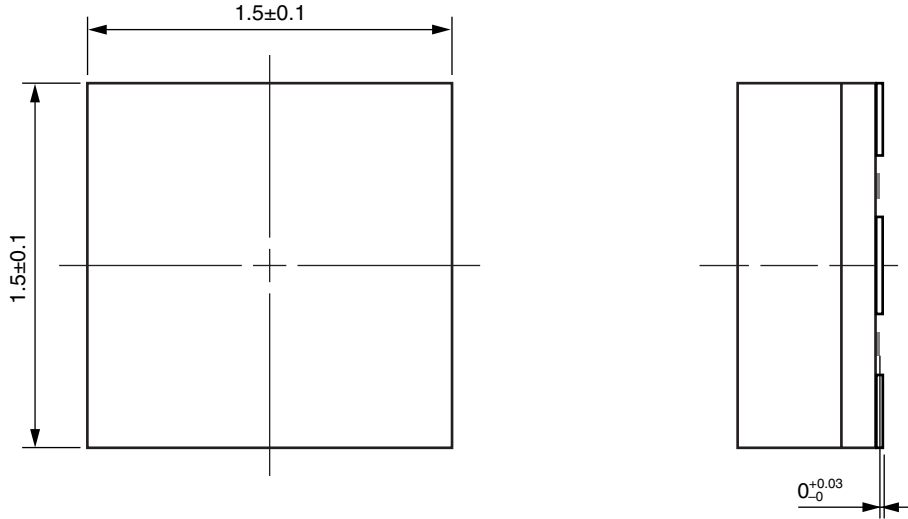
**Remark** The graphs indicate nominal characteristics.



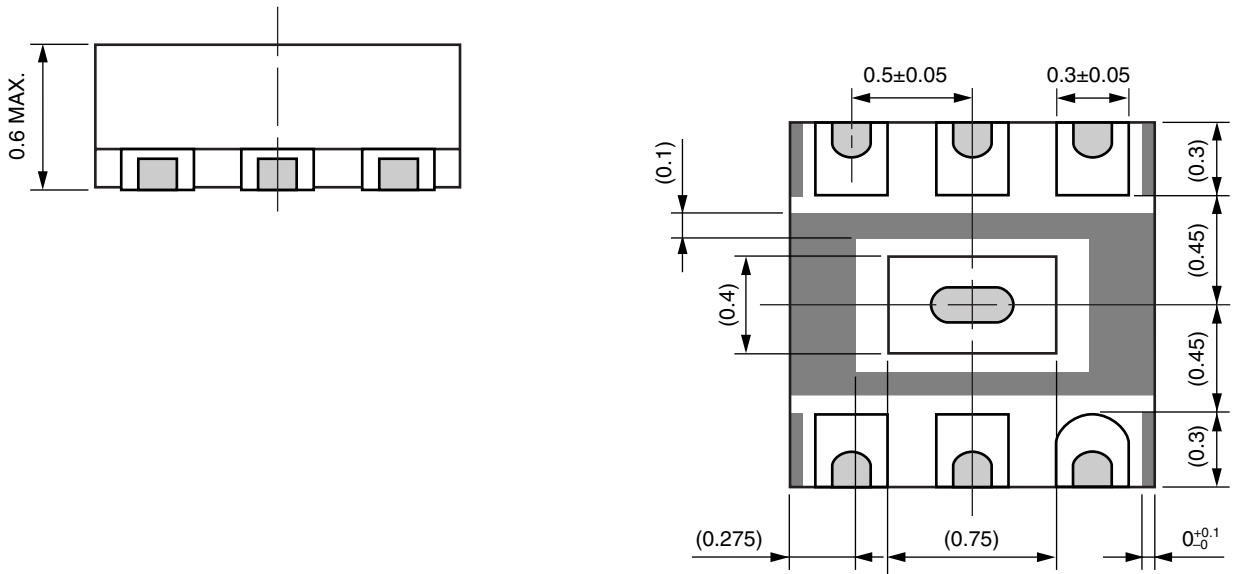
PACKAGE DIMENSIONS

6-PIN LGA (CSP TYPE) (UNIT: mm)

(Top View)



(Bottom View)



Remark ( ) : Reference value

□ : Through hole

■ : Solder resist

**RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) : 215°C or below Time at temperature of 200°C or higher : 25 to 40 seconds Preheating time at 120 to 150°C : 30 to 60 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

**Caution Do not use different soldering methods together (except for partial heating).**

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M8E 00.4-0110

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