

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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GaAs INTEGRATED CIRCUIT
μPG2005TB

L-BAND SINGLE CONTROL SPDT SWITCH

DESCRIPTION

The μPG2005TB is an L-band SPDT (Single Pole Double Throw) GaAs FET switch which was developed for digital cellular or cordless telephone application. The device can operate from 0.5 to 2.5 GHz, having the low insertion loss and high isolation by 2.8 V single bias control.

FEATURES

- Low insertion loss: $L_{INS} = 0.3 \text{ dB TYP. @}V_{CONT} = 2.8 \text{ V} / 0 \text{ V}, V_{DD} = 2.8 \text{ V}, f = 1 \text{ GHz}$
 $L_{INS} = 0.4 \text{ dB TYP. @}V_{CONT} = 2.8 \text{ V} / 0 \text{ V}, V_{DD} = 2.8 \text{ V}, f = 2.5 \text{ GHz}$
- High isolation: $ISL = 29 \text{ dB TYP. @}V_{CONT} = 2.8 \text{ V} / 0 \text{ V}, V_{DD} = 2.8 \text{ V}, f = 2 \text{ GHz}$
 $ISL = 25 \text{ dB TYP. @}V_{CONT} = 2.8 \text{ V} / 0 \text{ V}, V_{DD} = 2.8 \text{ V}, f = 2.5 \text{ GHz}$
- 6-pin super minimold package (Size: $2.0 \times 1.25 \times 0.9 \text{ mm}$)

APPLICATIONS

- L-band digital cellular or cordless telephone
- Bluetooth, W-LAN and WLL applications

ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
μPG2005TB-E3	6-pin super minimold	G2H	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 1, 2, 3 face the tape perforation side • Qty 3kpcs / reel

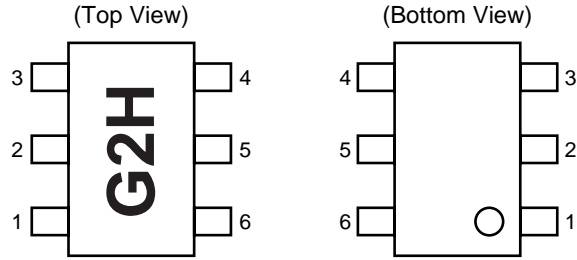
Remark To order evaluation samples, please contact your local NEC sales office.
 Part number for sample order: μPG2005TB

Caution The IC must be handled with care to prevent static discharge because its circuit composed of GaAs HJ-FET.

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 Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

PIN CONNECTIONS

Pin No.	Connection	Pin No.	Connection
1	OUT1	4	V _{CONT}
2	GND	5	IN
3	OUT2	6	V _{DD}



Discontinued Product

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C)

Parameter	Symbol	Ratings	Unit
Control Voltage	V _{CONT}	+6.0	V
Supply Voltage	V _{DD}	+6.0	V
Input Power	P _{in}	22	dBm
Total Power Dissipation	P _{tot}	0.15	W
Operating Temperature	T _{opt}	-45 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C

RECOMMENDED OPERATING CONDITIONS (T_A = +25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Control Voltage (High)	V _{CONT(H)}	2.7	2.8	3.0	V
Control Voltage (Low)	V _{CONT(L)}	0	0	0.2	V
Supply Voltage	V _{DD}	2.7	2.8	3.0	V

Discontinued Product

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $T_A = +25^{\circ}\text{C}$, $V_{\text{CONT}} = 2.8 \text{ V} / 0 \text{ V}$, $V_{\text{DD}} = 2.8 \text{ V}$, $Z_0 = 50 \Omega$, Off chip DC blocking capacitors value; 51 pF)

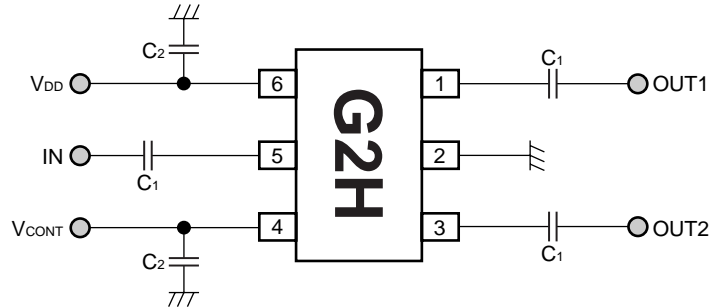
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	L_{INS}	f = 0.5 to 1.0 GHz	–	0.30	0.55	dB
		f = 2.0 GHz	–	0.35	0.60	dB
		f = 2.5 GHz	–	0.40	0.65	dB
Isolation	ISL	f = 0.5 to 2.0 GHz	25	29	–	dB
		f = 2.5 GHz	20	25	–	dB
Input Return Loss	RL_{in}	f = 1.0 to 2.5 GHz	16	21	–	dB
Output Return Loss	RL_{out}	f = 1.0 to 2.5 GHz	16	21	–	dB
Input Power at 1 dB Compression Point ^{Note}	$P_{\text{in}(1\text{dB})}$	f = 2.0 GHz	12	16	–	dBm
Input Power at 0.1 dB Compression Point ^{Note}	$P_{\text{in}(0.1\text{dB})}$	f = 2.0 GHz	–	12	–	dBm
Switching Speed	t_{sw}		–	20	–	ns
Supply Current	I_{DD}	$V_{\text{DD}} = 2.8 \text{ V}$, RF Non	–	47	80	μA
Control Current	I_{CONT}	$V_{\text{CONT}} = 2.8 \text{ V}$, RF Non	–	0.5	10	μA

Note $P_{\text{in}(1\text{dB})}$ or $P_{\text{in}(0.1\text{dB})}$ are measured the input power level when the insertion loss increase more 1 dB or 0.1 dB than that of linear range. All other characteristics are measured in linear range.

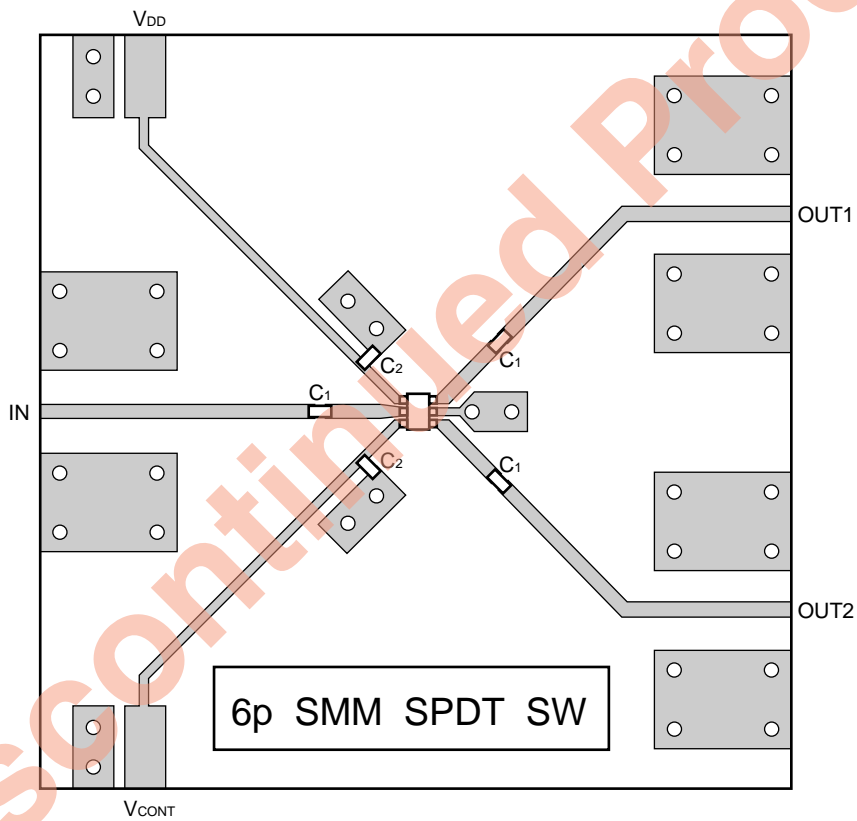
- Cautions 1.** When the μPG2005TB is used it is necessary to use DC blocking capacitors for No. 1 (OUT1), No.3 (OUT2) and No.5 (IN). The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, band width, switching speed and the condition with actual board of your system.
The range of recommended DC blocking capacitor value is less than 100 pF.
- 2.** The distance between IC's GND pin and ground pattern of substrate should be as shorter as possible to avoid parasitic parameters.

TEST CIRCUIT

$V_{CONT} = 2.8\text{ V} / 0\text{ V}$, $V_{DD} = 2.8\text{ V}$, off chip DC blocking capacitors value $C_1 = 51\text{ pF}$, $C_2 = 1\text{ 000 pF}$ (Bypass), using NEC standard evaluation board.



EVALUATION BOARD



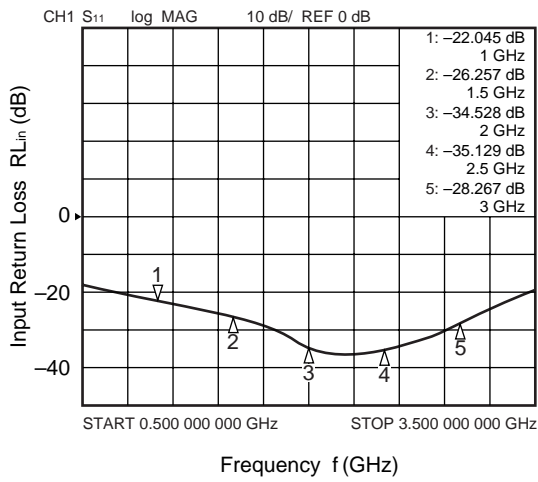
TRUTH TABLE OF SWITCHING BY CONDITION OF VOLTAGE

V_{CONT}	IN-OUT1	IN-OUT2
Low	OFF	ON
High	ON	OFF

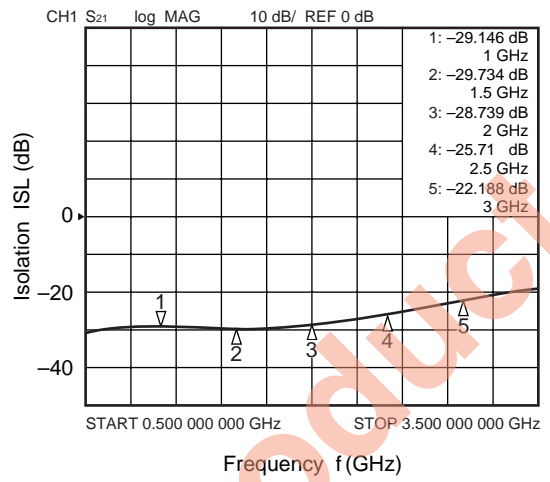
TYPICAL CHARACTERISTICS

TEST CONDITION: $V_{CONT} = 2.8\text{ V}$, $V_{DD} = 2.8\text{ V}$, $P_{in} = 0\text{ dBm}$, OUT2 side is $50\ \Omega$ termination

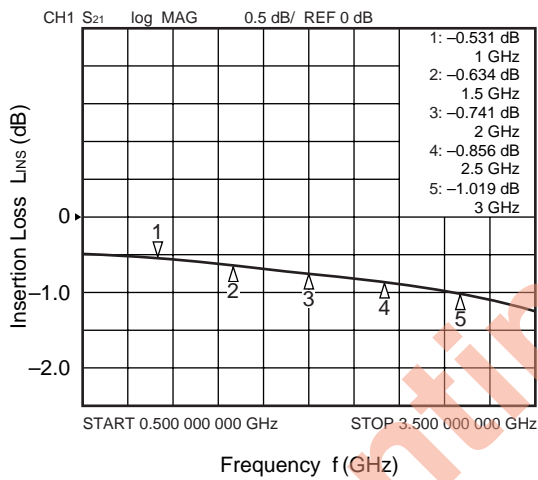
INPUT RETURN LOSS vs. FREQUENCY



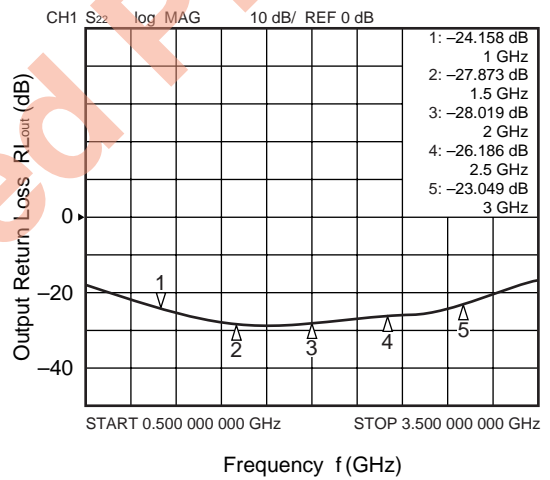
ISOLATION vs. FREQUENCY



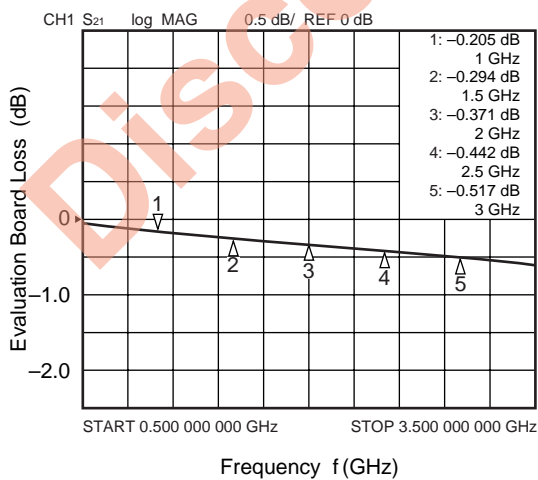
INSERTION LOSS vs. FREQUENCY



OUTPUT RETURN LOSS vs. FREQUENCY

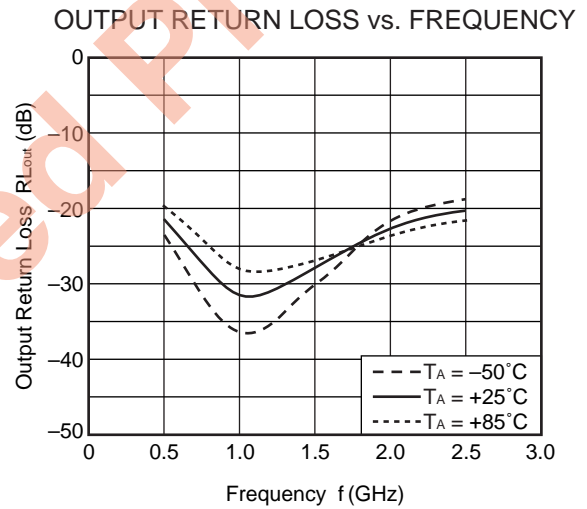
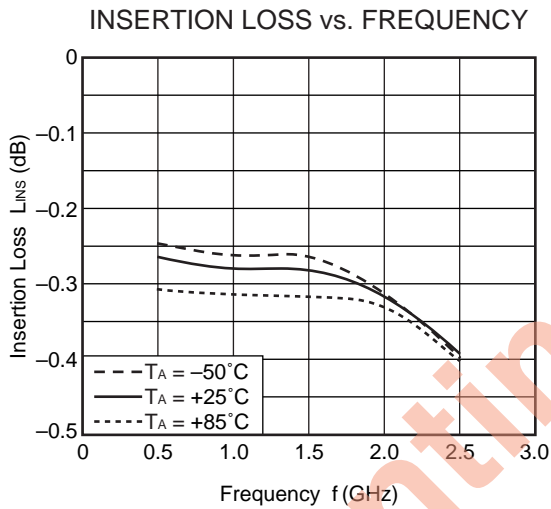
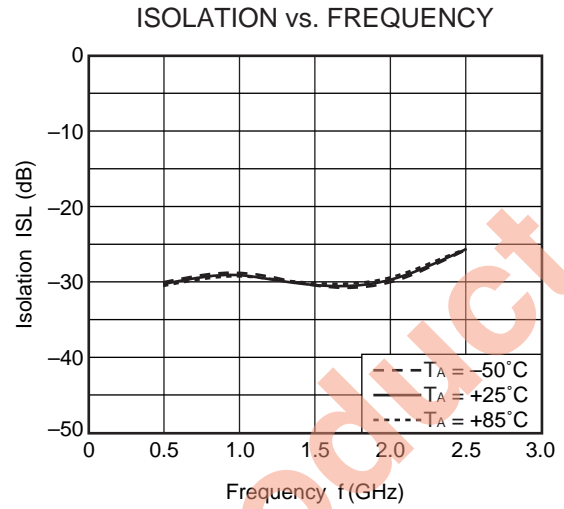
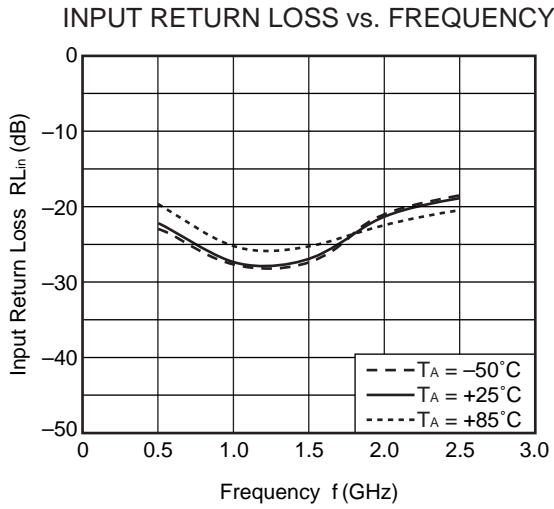


EVALUATION BOARD LOSS vs. FREQUENCY



TYPICAL CHARACTERISTICS (Temperature dependency of each frequency characteristics)

TEST CONDITION: $V_{CONT} = 2.8\text{ V}$, $V_{DD} = 2.8\text{ V}$, $P_{in} = 0\text{ dBm}$, OUT2 side is $50\ \Omega$ termination

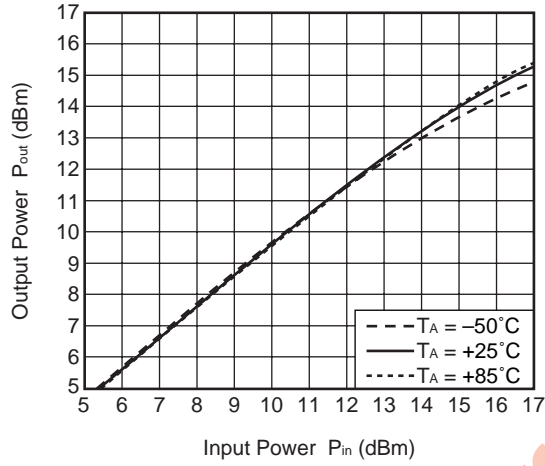


Caution This data is included value evaluation board loss.

TYPICAL CHARACTERISTICS

TEST CONDITION: $f = 2 \text{ GHz}$, $V_{\text{CONT}} = 2.8 \text{ V}$, $V_{\text{DD}} = 2.8 \text{ V}$, OUT2 side is 50Ω termination

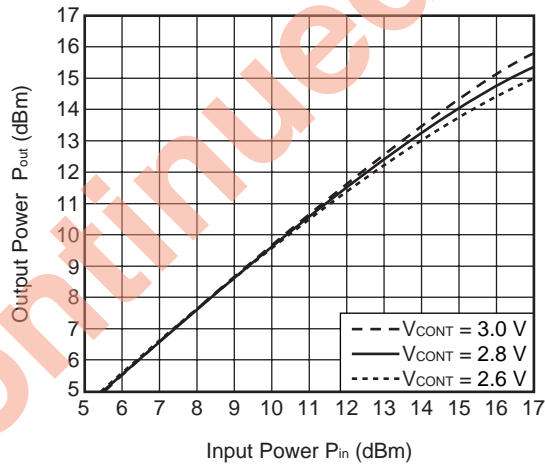
TEMPERATURE CHARACTERISTICS OF INPUT/OUTPUT



TYPICAL CHARACTERISTICS

TEST CONDITION: $f = 2 \text{ GHz}$, $T_A = +25^\circ\text{C}$, $V_{\text{DD}} = 2.6, 2.8, 3.0 \text{ V}$, OUT2 side is 50Ω termination

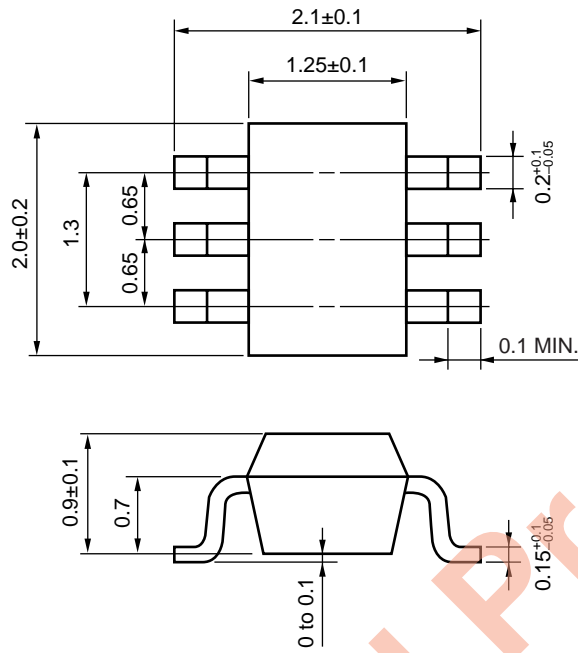
RELATION BETWEEN CONTROL VOLTAGE OF INPUT/OUTPUT



Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS

6-PIN SUPER MINIMOLD (UNIT: mm)



Discontinued Product

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your NEC sales representative.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Infrared Reflow	Package peak temperature: 235°C or below Time: 30 seconds or less (at 210°C) Count: 3, Exposure limit: None ^{Note}	IR35-00-3
VPS	Package peak temperature: 215°C or below Time: 40 seconds or less (at 200°C) Count: 3, Exposure limit: None ^{Note}	VP15-00-3
Wave Soldering	Soldering bath temperature: 260°C or below Time: 10 seconds or less Count: 1, Exposure limit: None ^{Note}	WS60-00-1
Partial Heating	Pin temperature: 300°C or below Time: 3 seconds or less (per side of device) Exposure limit: None ^{Note}	—

Note After opening the dry pack, keep it in a place below 25°C and 65% RH for the allowable storage period.

Caution Do not use different soldering methods together (except for partial heating).

For details of recommended soldering conditions for surface mounting, refer to information document **SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL (C10535E)**.

[MEMO]

Discontinued Product

SAFETY INFORMATION ON THIS PRODUCT

<p>Caution</p>	<p>GaAs Products</p>	<p>The product contains gallium arsenide, GaAs. GaAs vapor and powder are hazardous to human health if inhaled or ingested.</p> <ul style="list-style-type: none"> • Do not destroy or burn the product. • Do not cut or cleave off any part of the product. • Do not crush or chemically dissolve the product. • Do not put the product in the mouth. <p>Follow related laws and ordinances for disposal. The product should be excluded from general industrial waste or household garbage.</p>
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