To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

DATA SHEET

BIPOLAR ANALOG INTEGRATED CIRCUIT

μPC3225TB

5 V, SILICON GERMANIUM MMIC MEDIUM OUTPUT POWER AMPLIFIER

DESCRIPTION

The μPC3225TB is a silicon germanium (SiGe) monolithic integrated circuits designed as IF amplifier for DBS tuners. This IC is manufactured using our 50 GHz f_{max} UHS2 (Ultra High Speed Process) SiGe bipolar process.

FEATURES

- Wideband response : f_u = 2.8 GHz TYP. @ 3 dB bandwidth
- Low current : I_{CC} = 24.5 mA TYP.
- Medium output power : $P_{O\,(sat)}$ = +15.5 dBm TYP. @ f = 0.95GHz
  : $P_{O\,(sat)}$ = +12.5 dBm TYP. @ f = 2.15 GHz
- High linearity : $P_{O\,(1dB)}$ = +9.0 dBm TYP. @ f = 0.95 GHz
  : $P_{O\,(1dB)}$ = +7.0 dBm TYP. @ f = 2.15 GHz
- Power gain : G_P = 32.5 dB TYP. @ f = 0.95 GHz
  : G_P = 33.5 dB TYP. @ f = 2.15 GHz
- Noise Figure : NF = 3.7 dB TYP. @ f = 0.95 GHz
  : NF = 3.7 dB TYP. @ f = 2.15 GHz
- Supply voltage : V_{CC} = 4.5 to 5.5 V
- Port impedance : input/output 50 Ω

APPLICATIONS

- IF amplifiers in LNB for DBS converters etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPC3225TB-E3	μPC3225TB-E3-A	6-pin super minimold (Pb-Free) Note	C3M	Embossed tape 8 mm wide. 1, 2, 3 pins face the perforation side of the tape. Qty 3 kpcs/reel.

Note With regards to terminal solder (the solder contains lead) plated products (conventionally plated), contact your nearby sales office.

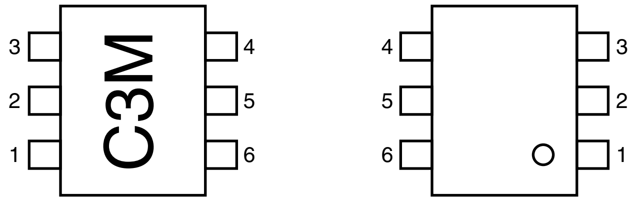
Remark To order evaluation samples, please contact your nearby sales office
Part number for sample order: μPC3225TB.

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

Document No. PU10500EJ01V0DS (1st edition)
Date Published December 2004 CP(K)
Printed in Japan

PIN CONNECTIONS

(Top View) (Bottom View)



Pin No.	Pin Name
1	OUTPUT
2	GND
3	V_{CC}
4	INPUT
5	GND
6	GND

PRODUCT LINE-UP OF 5 V-BIAS SILICON MMIC MEDIUM OUTPUT POWER AMPLIFIER
(T_A = +25°C, f = 1 GHz, V_{CC} = V_{out} = 5.0 V, Z_S = Z_L = 50 Ω)

Part No.	f_u (GHz)	$P_{O\,(sat)}$ (dBm)	G_P (dB)	NF (dB)	I_{CC} (mA)	Package	Marking
μPC2708TB	2.9	+10.0	15	6.5	26	6-pin super minimold	C1D
μPC2709TB	2.3	+11.5	23	5.0	25		C1E
μPC2710TB	1.0	+13.5	33	3.5	22		C1F
μPC2776TB	2.7	+8.5	23	6.0	25		C2L
μPC3223TB	3.2	+12.0	23	4.5	19		C3J
μPC3225TB	2.8	+15.5 Note	32.5 Note	3.7 Note	24.5		C3M

Note f = 0.95 GHz

Remark Typical performance. Please refer to **ELECTRICAL CHARACTERISTICS** in detail.

PIN EXPLANATION

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)Note	Function and Applications
4	INPUT	–	0.98	Signal input pin. A internal matching circuit, configured with resistors, enables 50 Ω connection over a wide band. A multi-feedback circuit is designed to cancel the deviations of h_{FE} and resistance. This pin must be coupled to signal source with capacitor for DC cut.
1	OUTPUT	Voltage as same as V_{CC} through external inductor	–	Signal output pin. The inductor must be attached between V_{CC} and output pins to supply current to the internal output transistors.
3	V_{CC}	4.5 to 5.5	–	Power supply pin. Which biases the internal input transistor. This pin should be externally equipped with bypass capacitor to minimize its impedance.
2 5 6	GND	0	–	Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All the ground pins must be connected together with wide ground pattern to decrease impedance defference.

Note Pin voltage is measured at V_{CC} = 5.0 V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Ratings	Unit
Supply Voltage	V_{CC}	T_A = +25°C, Pin 1 and 3	6	V
Total Circuit Current	I_{CC}	T_A = +25°C	45	mA
Power Dissipation	P_D	T_A = +85°C **Note**	270	mW
Operating Ambient Temperature	T_A		–40 to +85	°C
Storage Temperature	T_{stg}		–55 to +150	°C
Input Power	P_{in}	T_A = +25°C	0	dBm

Note Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{CC}	The same voltage should be applied to pin 1 and 3.	4.5	5.0	5.5	V
Operating Ambient Temperature	T_A		–40	+25	+85	°C

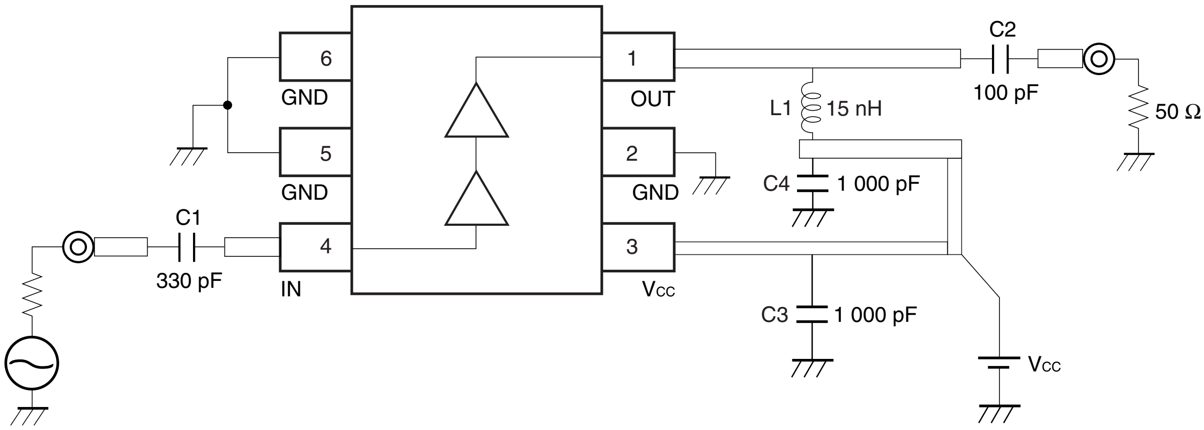
ELECTRICAL CHARACTERISTICS (T_A = +25°C, V_{CC} = V_{out} = 5.0 V, Z_S = Z_L = 50 Ω)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	I_{CC}	No input signal	20.0	24.5	31.0	mA
Power Gain	G_P	f = 0.95 GHz, P_{in} = –35.0 dBm	30.0	32.5	35.0	dB
		f = 2.15 GHz, P_{in} = –35.0 dBm	30.5	33.5	36.0	
Saturated Output Power	$P_{O\,(sat)}$	f = 0.95 GHz, P_{in} = −5.0 dBm	+13.5	+15.5	–	dBm
		f = 2.15 GHz, P_{in} = −5.0 dBm	+10.5	+12.5	–	
Gain 1 dB Compression Output Power	$P_{O\,(1\,dB)}$	f = 0.95 GHz	+7.0	+9.0	–	dBm
		f = 2.15 GHz	+5.0	+7.0	–	
Noise Figure	NF	f = 0.95 GHz	–	3.7	4.5	dB
		f = 2.15 GHz	–	3.7	4.5	
Upper Limit Operating Frequency	f_u	3 dB down below flat gain at f = 0.95 GHz	–	2.8	–	GHz
Isolation	ISL	f = 0.95 GHz, P_{in} = –35.0 dBm	36.0	41.0	–	dB
		f = 2.15 GHz, P_{in} = –35.0 dBm	36.0	45.0	–	
Input Return Loss	RL_{in}	f = 0.95 GHz, P_{in} = –35.0 dBm	7.0	8.5	–	dB
		f = 2.15 GHz, P_{in} = –35.0 dBm	8.0	11.0	–	
Output Return Loss	RL_{out}	f = 0.95 GHz, P_{in} = –35.0 dBm	7.0	10.5	–	dB
		f = 2.15 GHz, P_{in} = –35.0 dBm	9.5	13.0	–	
Gain Flatness	ΔG_P	f = 0.95 to 2.15 GHz	–	2.5	4.0	dB

OTHER CHARACTERISTICS, FOR REFERENCE PURPOSES ONLY
(T_A = +25°C, V_{CC} = V_{out} = 5.0 V, Z_S = Z_L = 50 Ω)

Parameter	Symbol	Test Conditions	Reference Value	Unit
Output intercept point	OIP_3	f = 0.95 GHz	21.0	dBm
		f = 2.15 GHz	16.0	

TEST CIRCUIT



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

	Value	Maker	Type code
C1	330 pF	Murata	GMR36CH
C2	100 pF	Murata	GMR36CH
C3	1 000 pF	Murata	GMR39CH
C4	1 000 pF	Murata	GMR36B
L1	15 nH	Susumu	TFL0816

INDUCTOR FOR THE OUTPUT PIN

The internal output transistor of this IC consumes 24.5 mA, to output medium power. To supply current for output transistor, connect an inductor between the Vcc pin (pin 3) and output pin (pin 1). Select inductance, as the value listed above.

The inductor has both DC and AC effects. In terms of DC, the inductor biases the output transistor with minimum voltage drop to output enable high level. In terms of AC, the inductor makes output-port impedance higher to get enough gain. In this case, large inductance and Q is suitable.

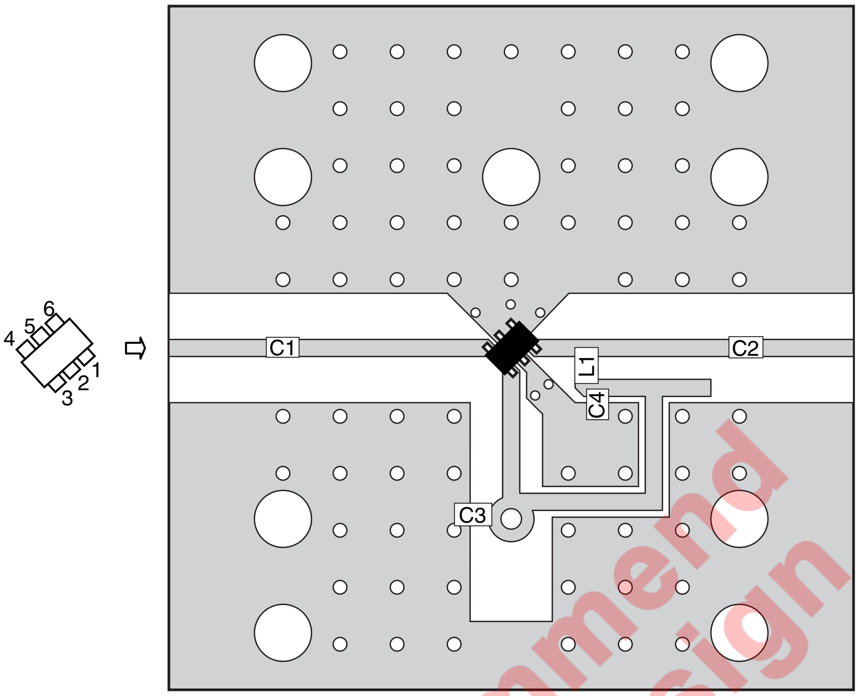
CAPACITORS FOR THE Vcc, INPUT AND OUTPUT PINS

Capacitors of 1 000 pF are recommendable as the bypass capacitor for the Vcc pin. Capacitors of 330 pF for the input pin and 100 pF for the output pin are recommendable as the coupling capacitors.

The bypass capacitor connected to the Vcc pin is used to minimize ground impedance of Vcc pin. So, stable bias can be supplied against Vcc fluctuation.

The coupling capacitors, connected to the input and output pins, are used to cut the DC and minimize RF serial impedance. Their capacitances are therefore selected as lower impedance against a 50 Ω load. The capacitors thus perform as high pass filters, suppressing low frequencies to DC.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



COMPONENT LIST

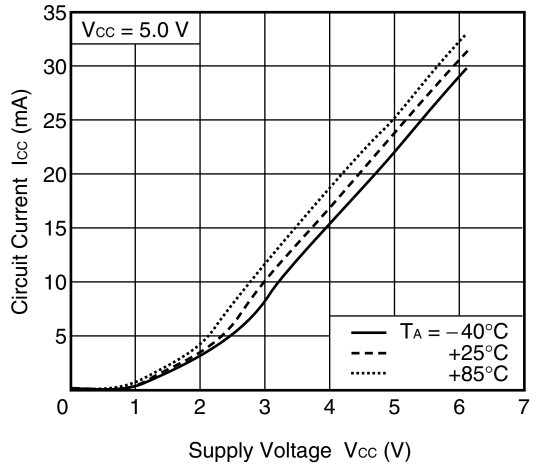
	Value
C1	330 pF
C2	100 pF
C3, C4	1 000 pF
L1	15 nH

Notes

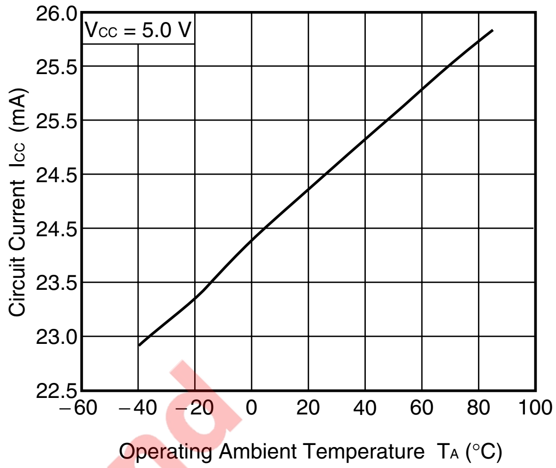
1. 30 × 30 × 0.4 mm double sided copper clad polyimide board.
2. Back side: GND pattern
3. Solder plated on pattern
4. ○ ◯: Through holes

TYPICAL CHARACTERISTICS (VCC = 5.0 V, TA = +25°C, unless otherwise specified)

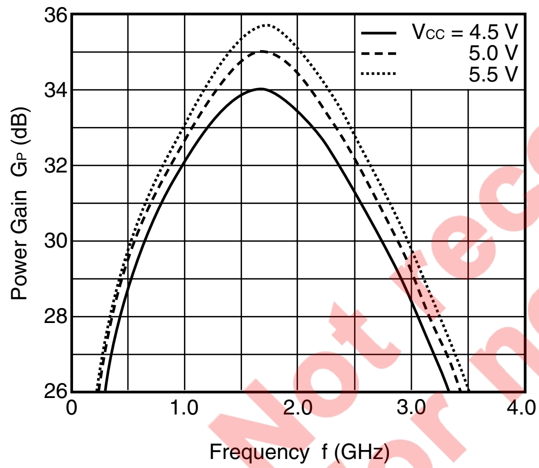
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



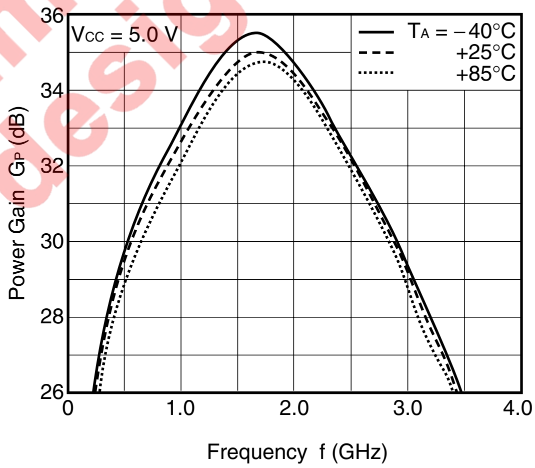
CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE



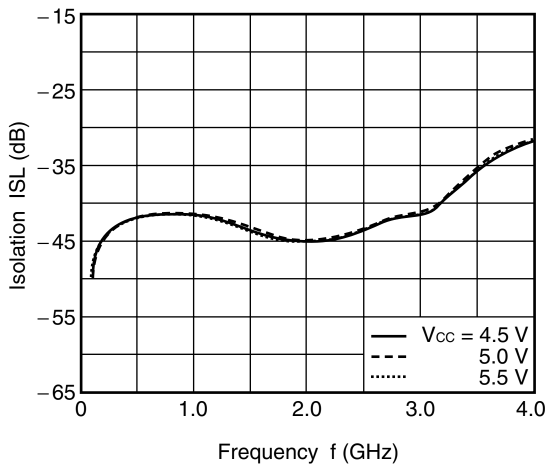
POWER GAIN vs. FREQUENCY



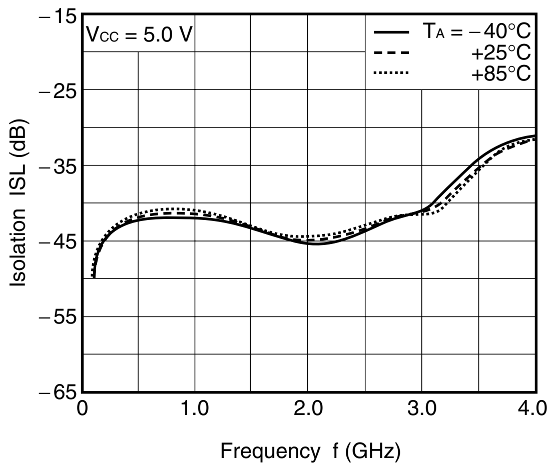
POWER GAIN vs. FREQUENCY



ISOLATION vs. FREQUENCY

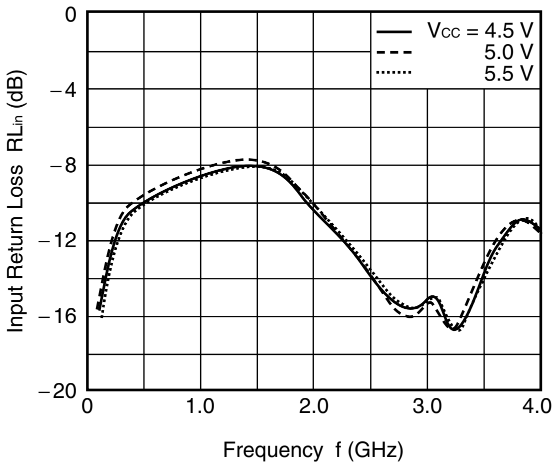


ISOLATION vs. FREQUENCY



Remark The graphs indicate nominal characteristics.

INPUT RETURN LOSS vs. FREQUENCY



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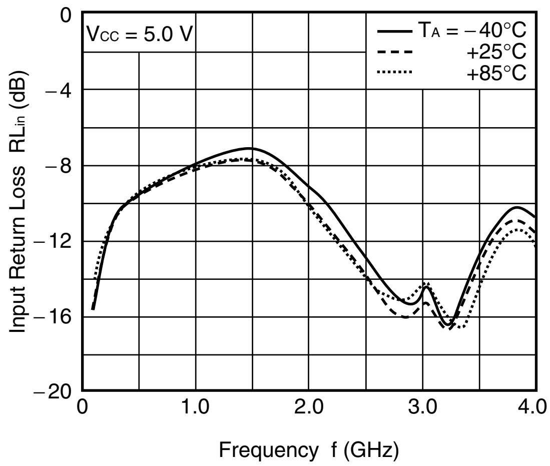
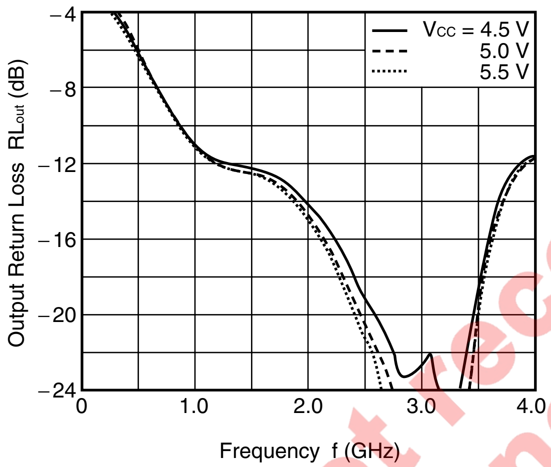
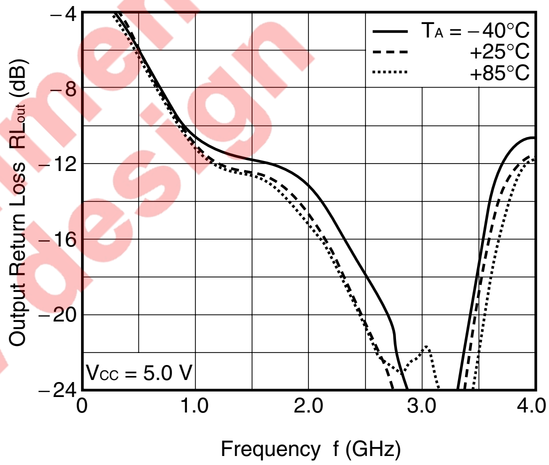


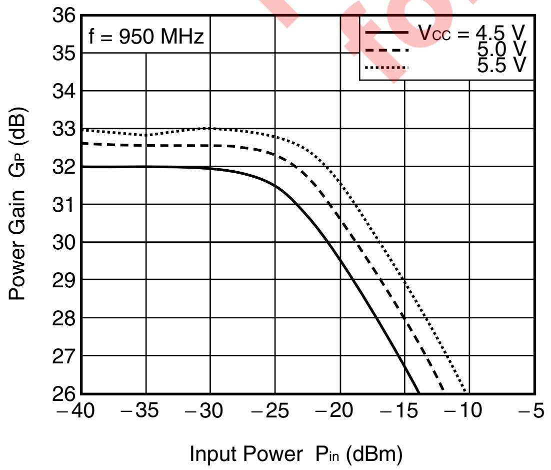
OUTPUT RETURN LOSS vs. FREQUENCY



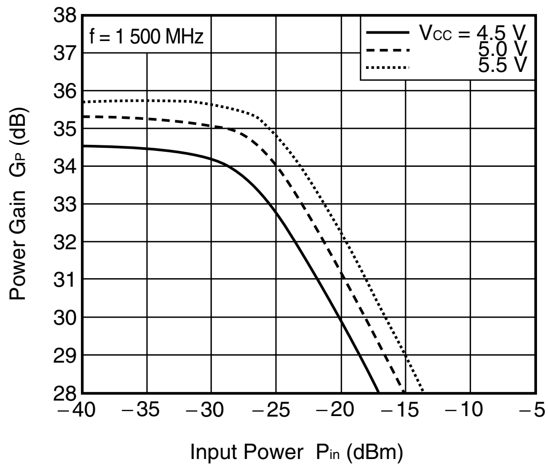
OUTPUT RETURN LOSS vs. FREQUENCY



POWER GAIN vs. INPUT POWER



POWER GAIN vs. INPUT POWER



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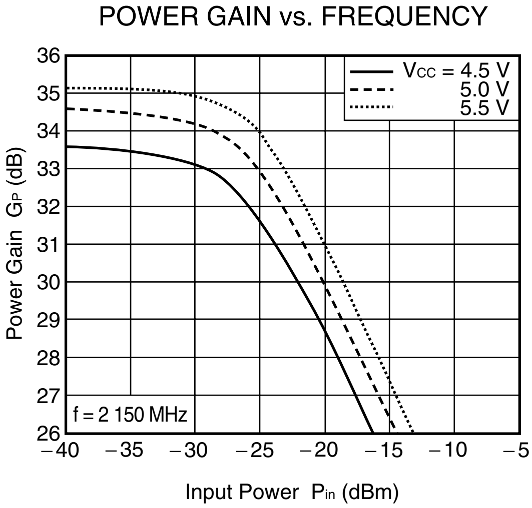
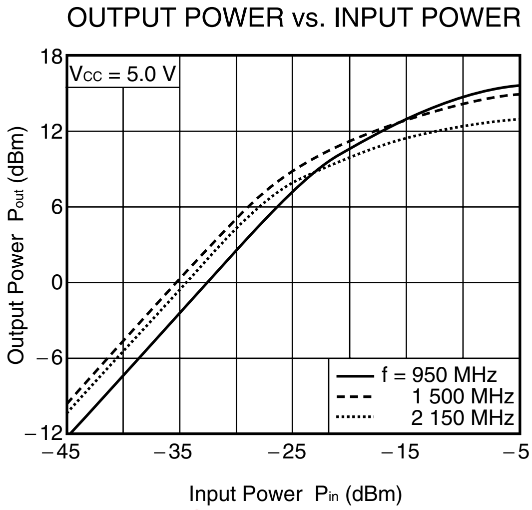
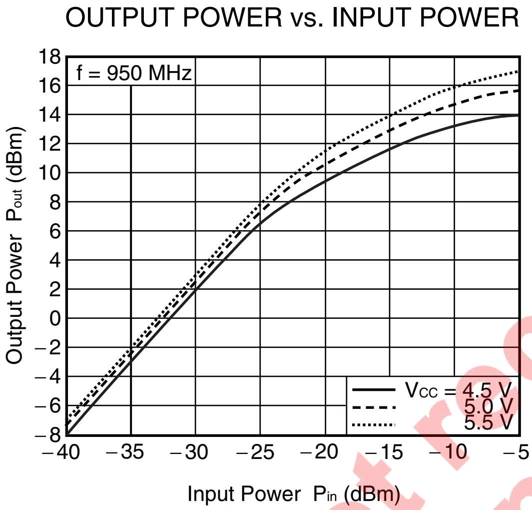
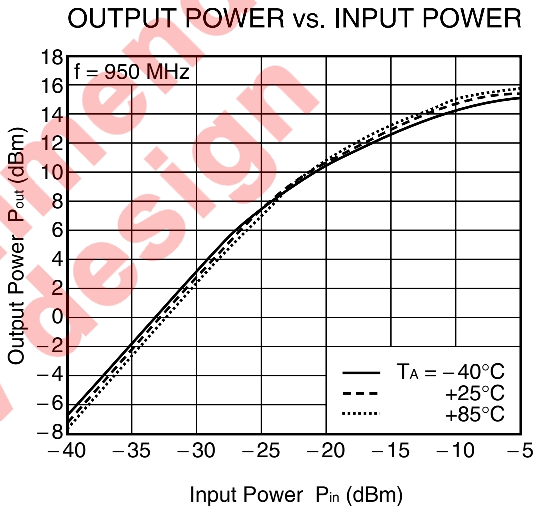
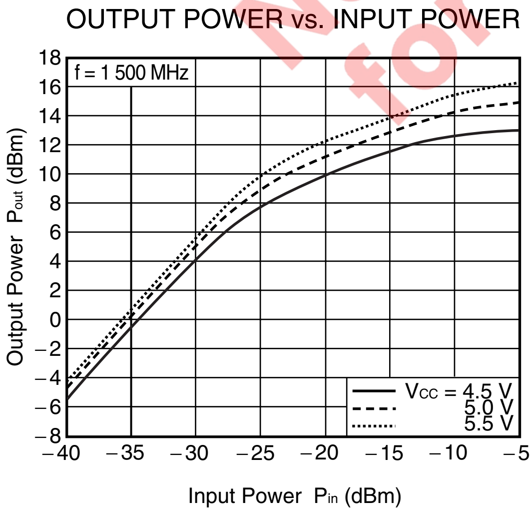
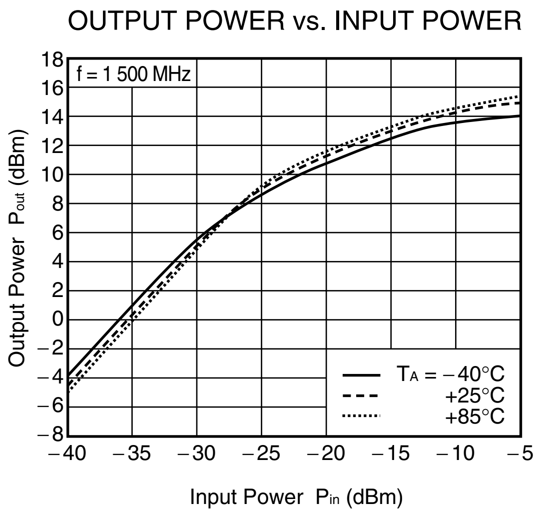






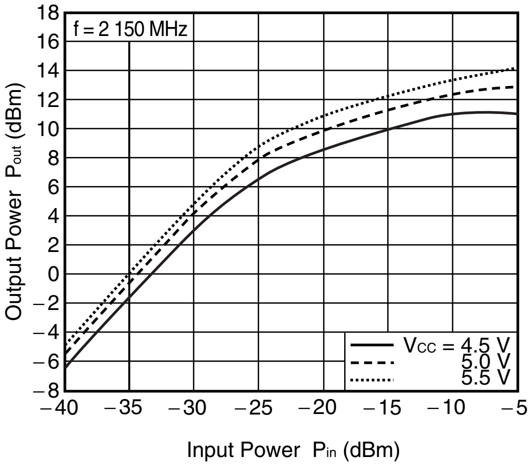




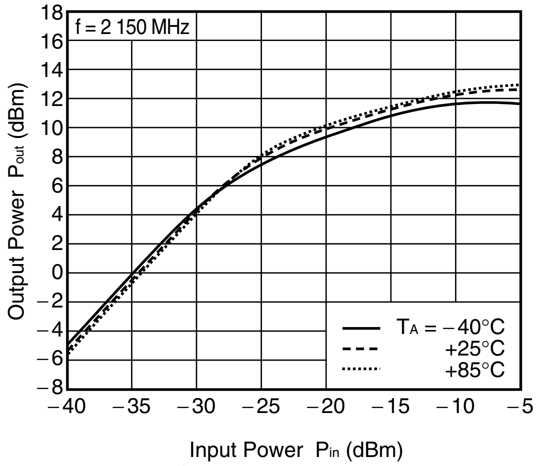


Remark The graphs indicate nominal characteristics.

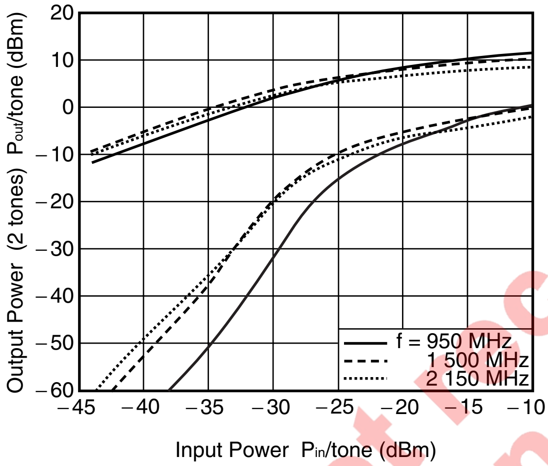
OUTPUT POWER vs. INPUT POWER



OUTPUT POWER vs. INPUT POWER

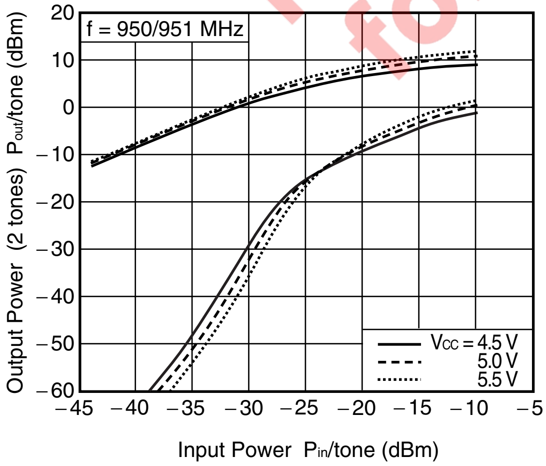


OUTPUT POWER (2 TONES) vs. INPUT POWER

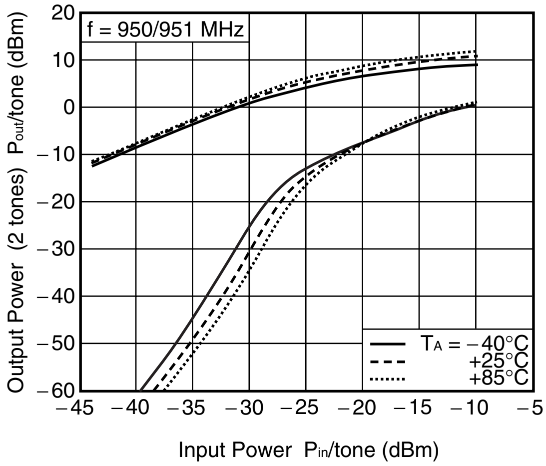


V_{CC} = 5.0 V, Δf = 1 MHz
f = 950/951 MHz : OIP_3 = 21.0 dBm
f = 1 500/1 501 MHz : OIP_3 = 18.2 dBm
f = 2 150/2 151 MHz : OIP_3 = 16.0 dBm

OUTPUT POWER (2 TONES) vs. INPUT POWER

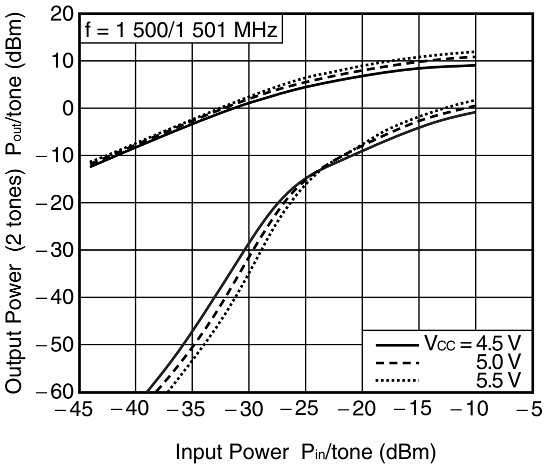


OUTPUT POWER (2 TONES) vs. INPUT POWER

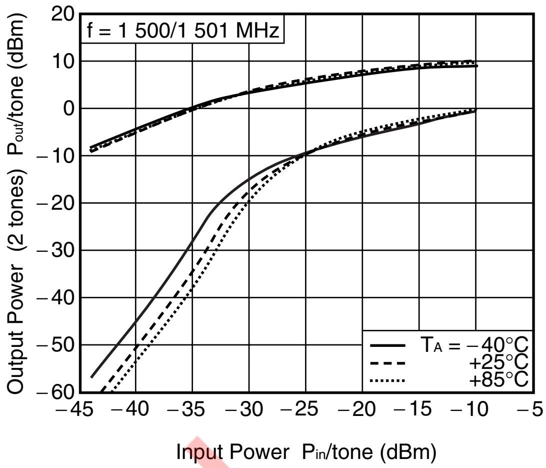


Remark The graphs indicate nominal characteristics.

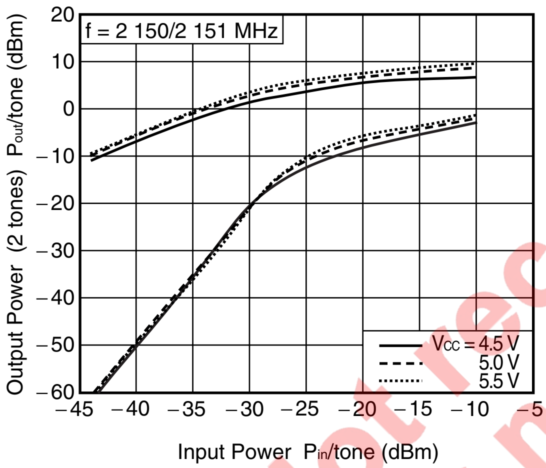
OUTPUT POWER (2 TONES)
vs. INPUT POWER



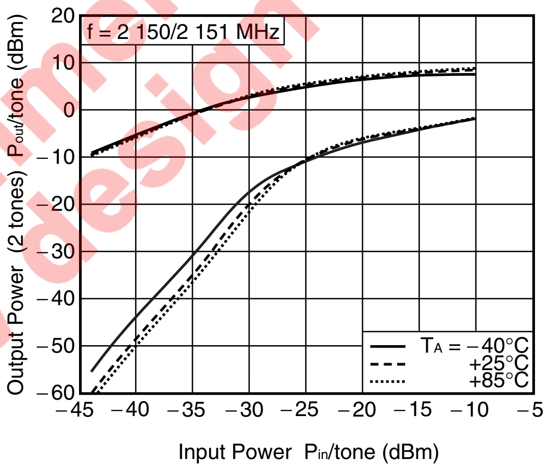
OUTPUT POWER (2 TONES)
vs. INPUT POWER



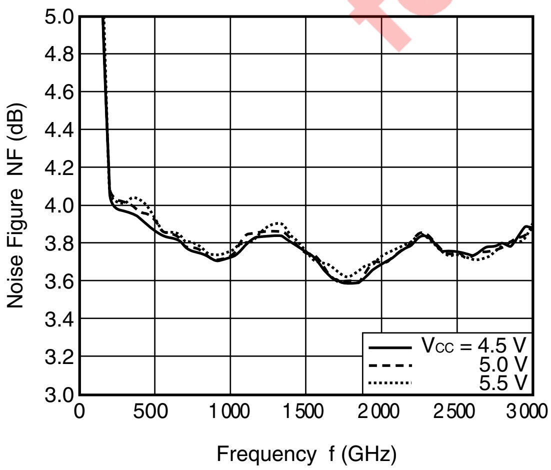
OUTPUT POWER (2 TONES)
vs. INPUT POWER



OUTPUT POWER (2 TONES)
vs. INPUT POWER



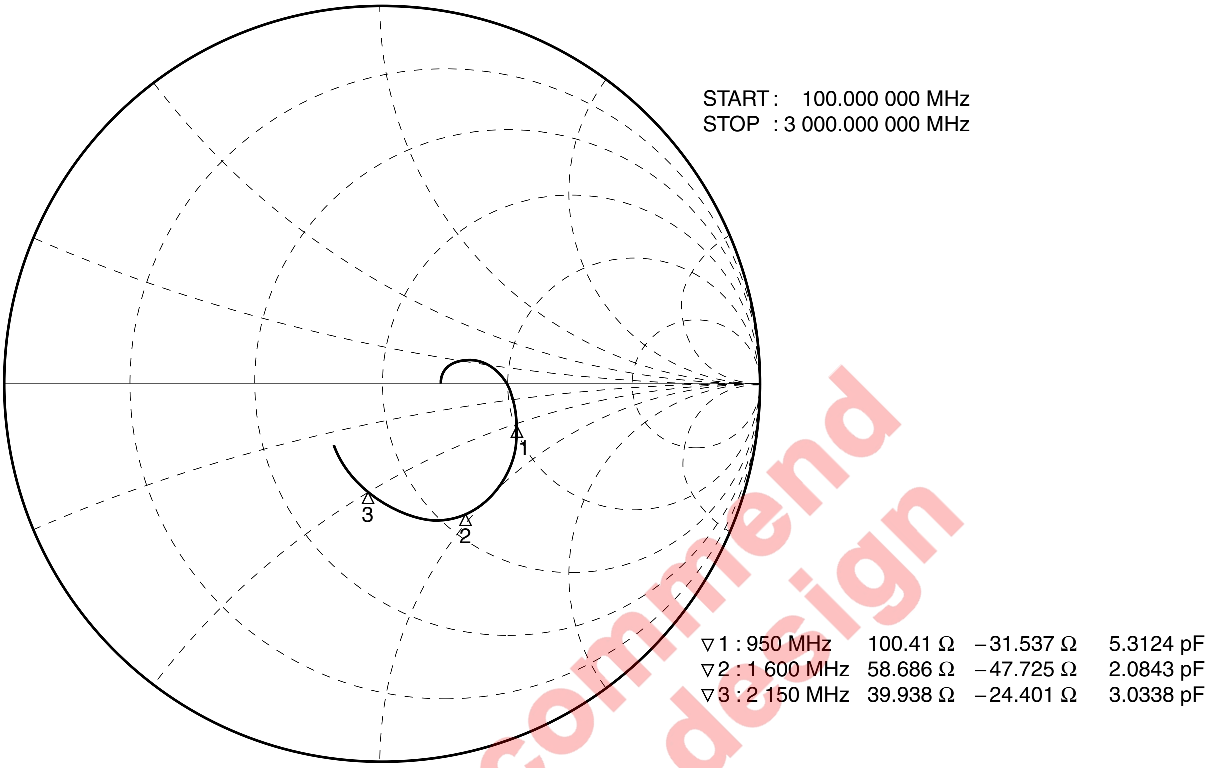
NOISE FIGURE vs. FREQUENCY



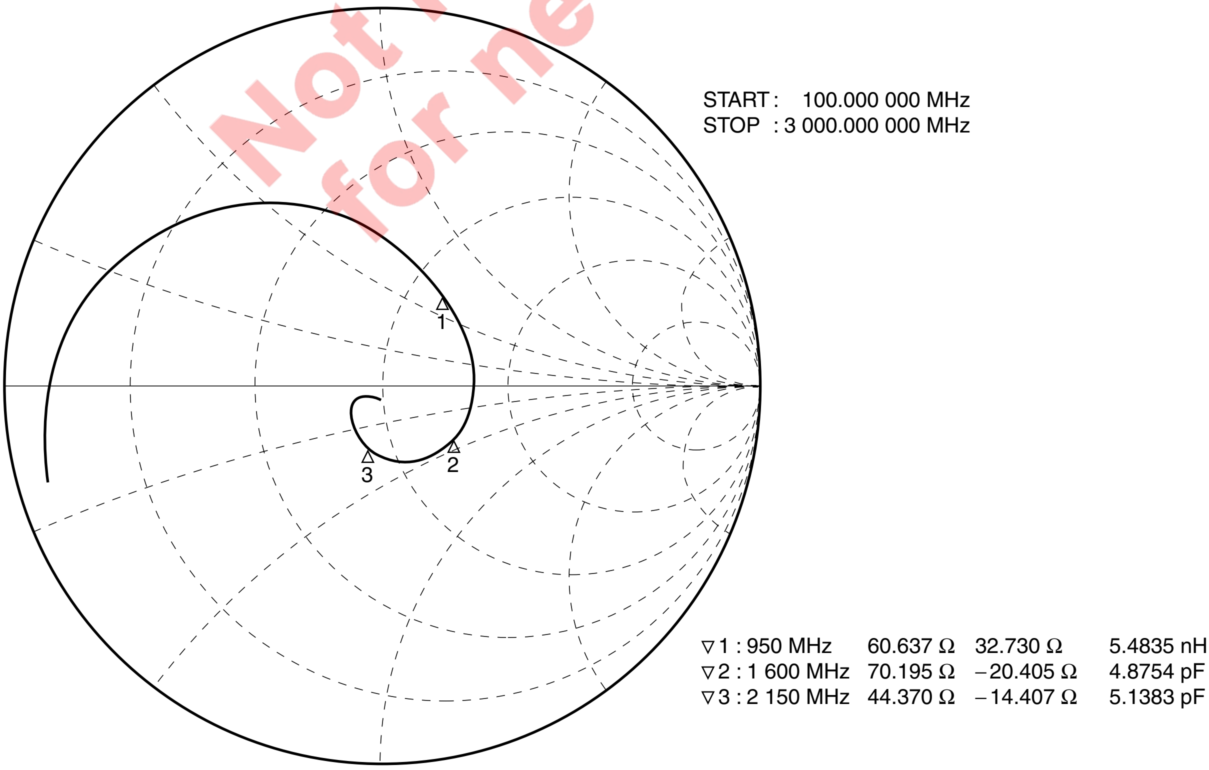
Remark The graphs indicate nominal characteristics.

S-PARAMETERS (T_A = +25°C, V_{CC} = V_{out} = 5.0 V)

S_{11}–FREQUENCY

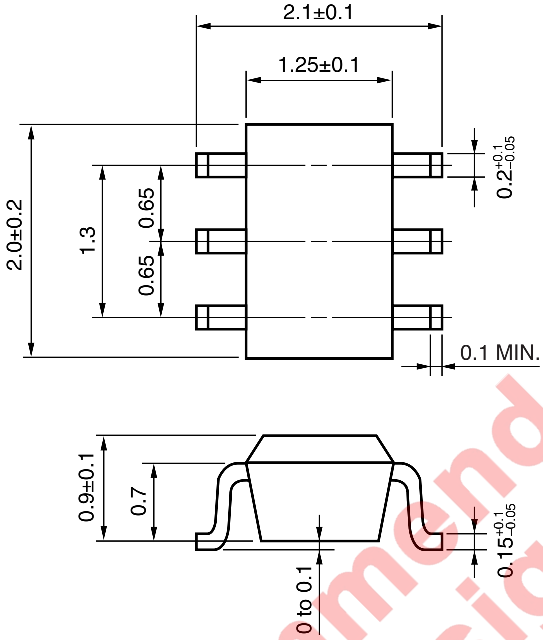


S_{22}–FREQUENCY



PACKAGE DIMENSIONS

6-PIN SUPER MINIMOLD (UNIT: mm)



NOTES ON CORRECT USE

(1) Observe precautions for handling because of electro-static sensitive devices.

(2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground pins must be connected together with wide ground pattern to decrease impedance difference.

(3) The bypass capacitor should be attached to the V_{CC} pin.

(4) The inductor (L) must be attached between V_{CC} and output pins. The inductance value should be determined in accordance with desired frequency.

(5) The DC cut capacitor must be attached to input and output pin.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

▶For further information, please contact

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