

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

**Phase-out/Discontinued**

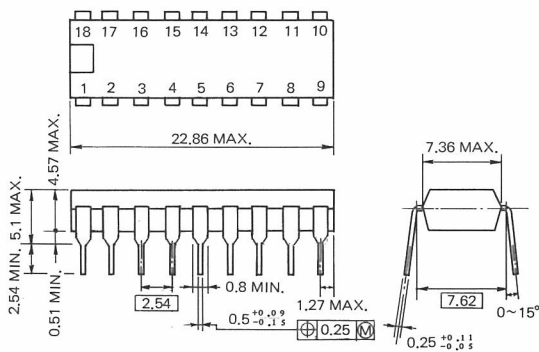
**HIGH-VOLTAGE FLUORESCENT INDICATOR PANEL DRIVER  
 PNP-NPN SILICON EPITAXIAL TRANSISTOR ARRAY**

**DESCRIPTION**

The  $\mu$ PA82C is a monolithic array of eight PNP-NPN structured transistors. This device is especially suited for driving High-Voltage FIP (Fluorescent Indicator Panel) called DOT TYPE FIP.

**PACKAGE DIMENSIONS**

in millimeters



**FEATURES**

- High voltage rating  $V_{SS}$ : -150 V
- Base current limiting resistors incorporated
- Package is 18 pin plastic DIP (Dual In-Line Package).

**ABSOLUTE MAXIMUM RATINGS**

Maximum Voltages and Currents ( $T_a=25^\circ\text{C}$ )

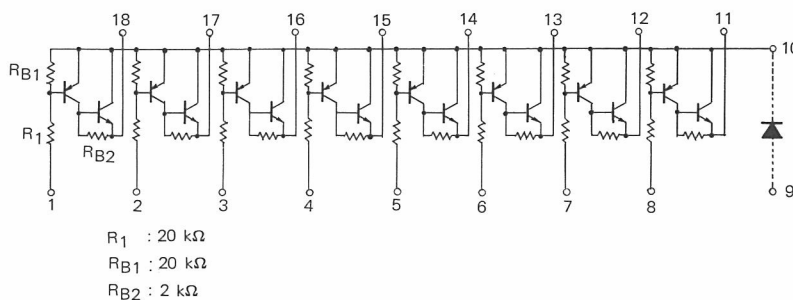
Supply Voltage	$V_{SS}$	-150	V
Input Voltage	$V_I$	-20	V
Output Current	$I_o$	50	mA/unit
Maximum Power Dissipation			
Package Dissipation	$P_D$	900	mW
Maximum Temperature			
Storage Temperature	$T_{stg}$	-40 to +125	$^\circ\text{C}$
Operating Junction Temperature	$T_{j(opt)}$	+125	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ\text{C}$ )**

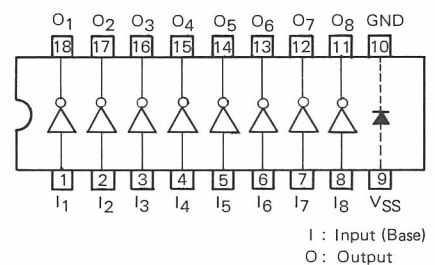
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Output Leakage Current	$I_L$			1.0	$\mu\text{A}$	$V_{CE}=120\text{ V}^*$
DC Current Gain	$h_{FE1}$	100				$V_{CE}=2.0\text{ V}, I_O=20\text{ mA}$
	$h_{FE2}$	250				$V_{CE}=2.0\text{ V}, I_O=40\text{ mA}$
Collector Saturation Voltage	$V_{CE(sat)}$			2.0	V	$I_O=40\text{ mA}, I_I=0.4\text{ mA}$
Input Current	$I_I$			320	$\mu\text{A}$	$V_I=-5.0\text{ V}$

\* Output-terminal be short-circuited to  $V_{SS}$ -terminal.

**EQUIVALENT CIRCUIT**



**CONNECTION DIAGRAM (Top View)**



Note:  $V_{SS}$ -terminal is to be connected to the lowest voltage level in the application.

**Phase-out/Discontinued**

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