

RKR0103BYPKQ

R07DS0564EJ0100

Rev.1.00

Silicon Schottky Barrier Diode for Rectifying

Jan 12, 2012

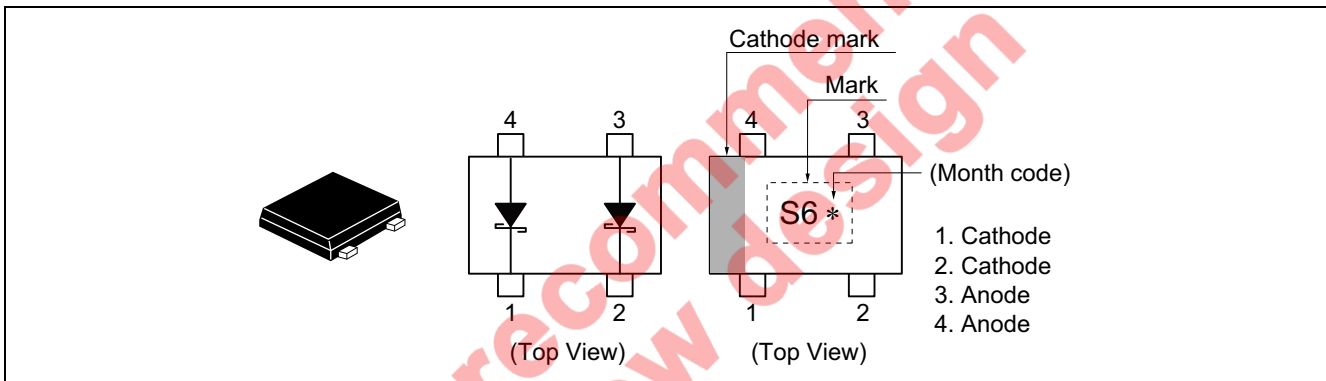
Features

- Low reverse voltage drop and suitable for high efficiency rectifying.
- VSON-4 Package is suitable for high density surface mounting.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKR0103BYPKQ # P1	S6	VSON-4	PUSN0004KA-A	P1 (3,000pcs / reel)

Pin Arrangement



Month Code

Month of Manufacture	Assemble
	MALAYSIA
January	1
February	2
March	3
April	4
May	5
June	6

Month of Manufacture	Assemble
	MALAYSIA
July	7
August	8
September	9
October	W
November	X
December	Y

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	30	V
Reverse voltage	V_R	30	V
Average rectified current	I_O^{*1}	100	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	1	A
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. Per one device. See Fig.4 – Fig.6.

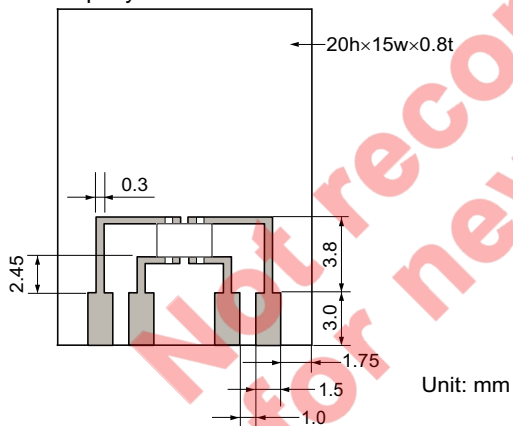
2. $t = 10$ ms sin wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.35	V	$I_F = 1$ mA
	V_{F2}	—	—	0.40		$I_F = 10$ mA
	V_{F3}	—	—	0.50		$I_F = 100$ mA
Reverse current	I_{R1}	—	—	0.5	μ A	$V_R = 10$ V
	I_{R2}	—	—	10		$V_R = 30$ V
Thermal resistance	$R_{th(j-a)}$	—	700	—	°C/W	Glass epoxy board ^{*1}

Note: 1. Glass epoxy board



Main Characteristics

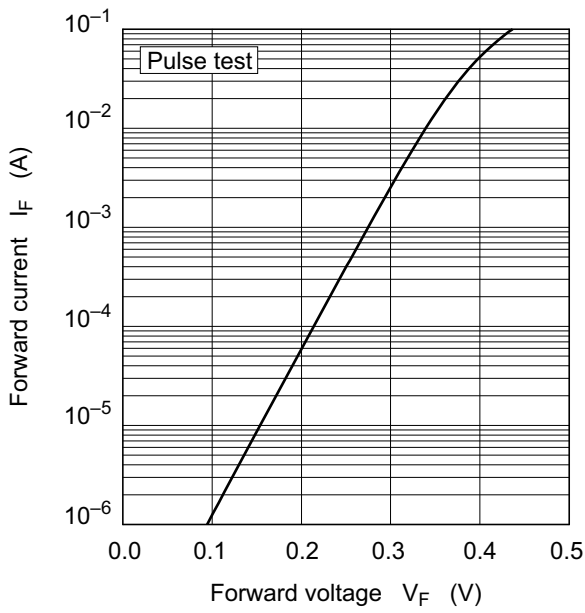


Fig.1 Forward current vs. Forward voltage

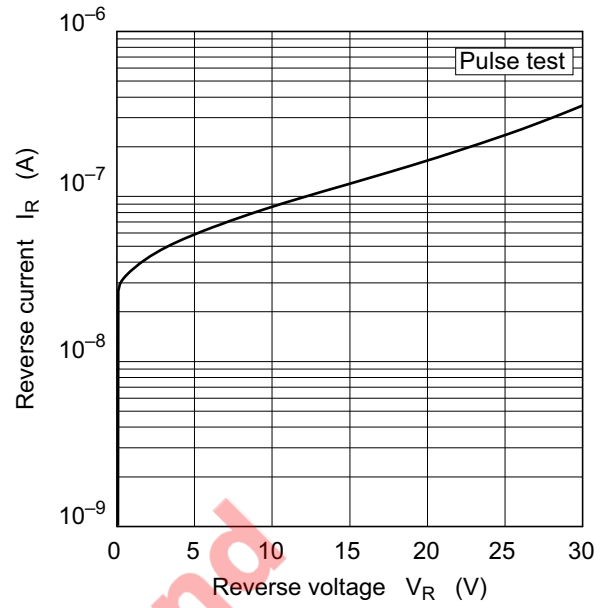


Fig.2 Reverse current vs. Reverse voltage

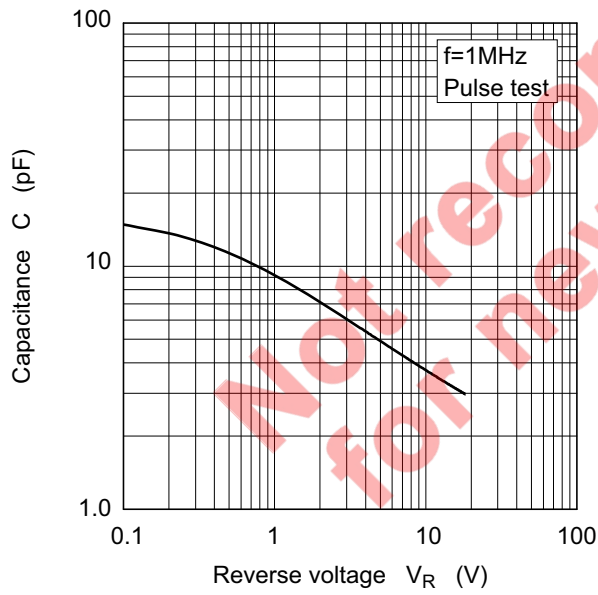


Fig.3 Capacitance vs. Reverse voltage

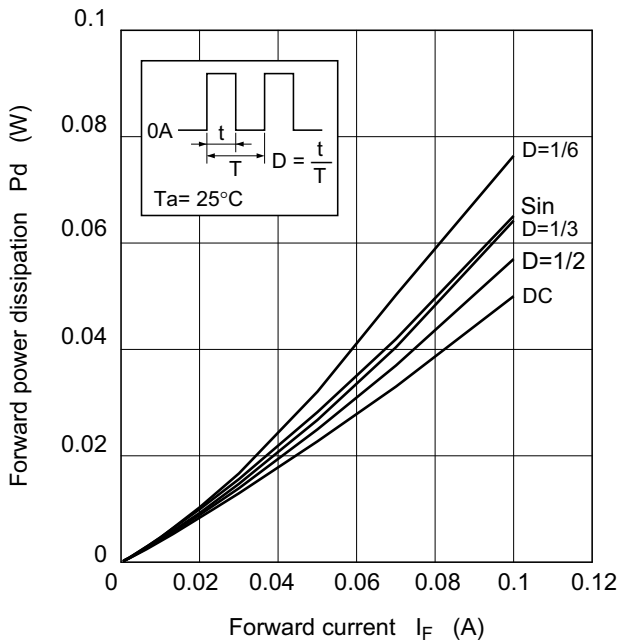


Fig.4 Forward power dissipation vs. Forward current

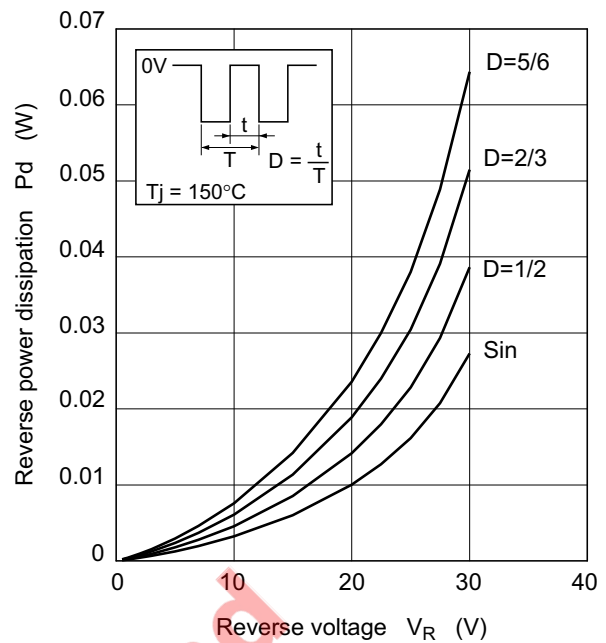


Fig.5 Reverse power dissipation vs. Reverse voltage

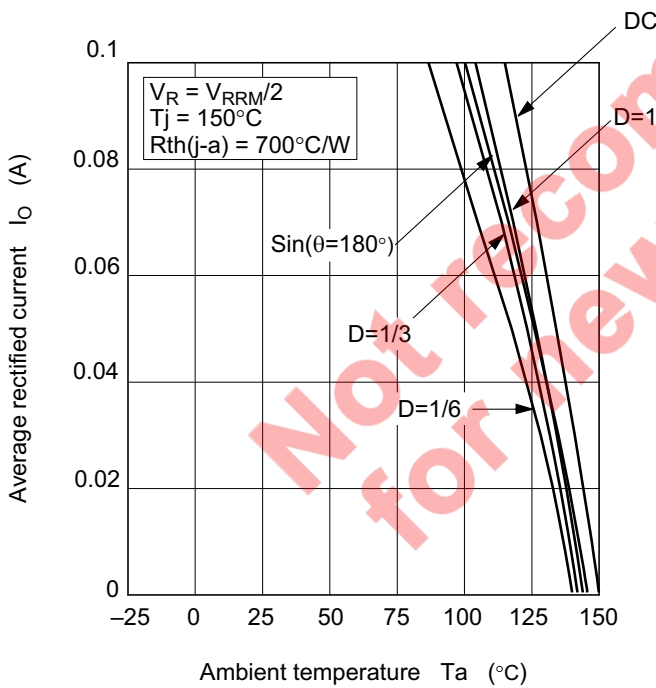
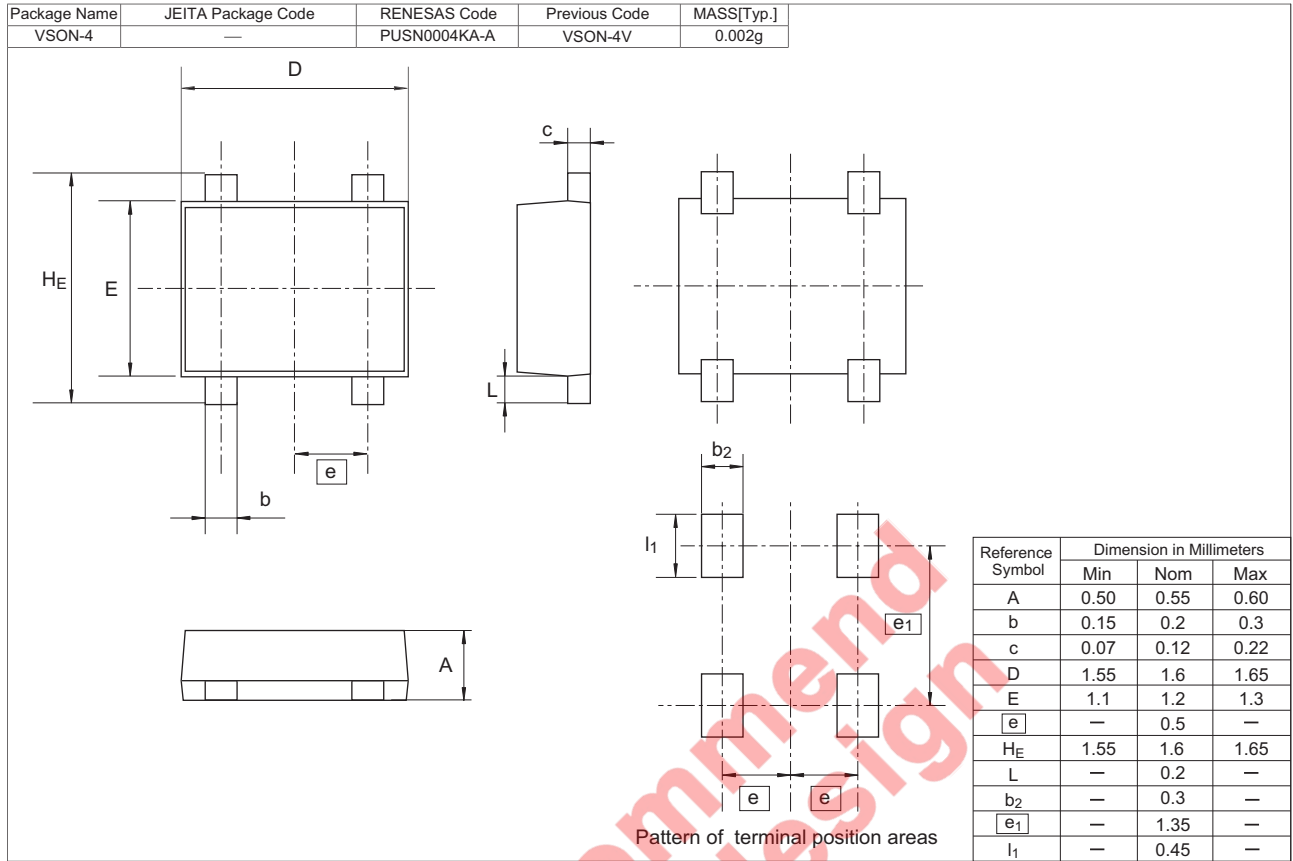


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions



Not recommended for new design

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