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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RD74LVC2G07

Dual Buffers / Drivers with Open Drain

REJ03D0751-0100

Rev.1.00

Oct 30, 2006

Description

The RD74LVC2G07 has Dual buffers / drives with open drain outputs in a 6-pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

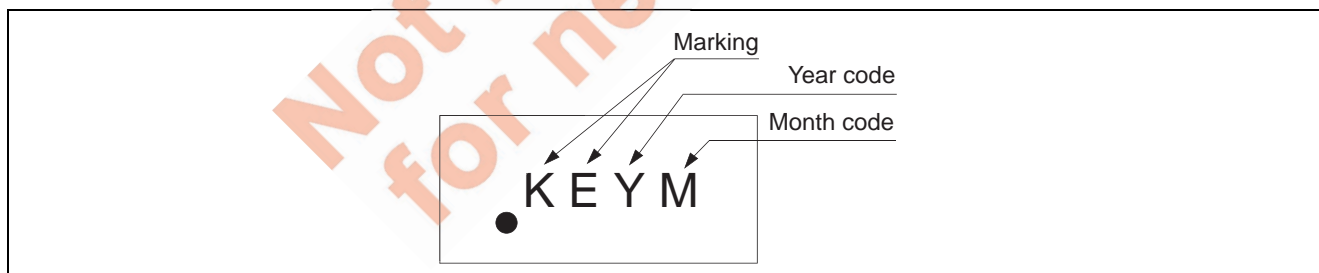
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supply voltage range : 1.65 to 5.5 V
- Operating temperature range: -40 to +85°C
- All inputs: $V_{IH} (\text{Max.}) = 5.5 \text{ V} (@V_{CC} = 0 \text{ V to } 5.5 \text{ V})$
- All outputs: $V_O (\text{Max.}) = 5.5 \text{ V} (@V_{CC} = 0 \text{ V})$
- Output current:
 - $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$
 - $\pm 8 \text{ mA} (@V_{CC} = 2.3 \text{ V})$
 - $\pm 24 \text{ mA} (@V_{CC} = 3.0 \text{ V})$
 - $\pm 32 \text{ mA} (@V_{CC} = 4.5 \text{ V})$

- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC2G07WPE	WCSP-6pin	SXBG0006LA-A (TBS-6BV)	WP	E (3,000 pcs/reel)

Article Indication



Function Table

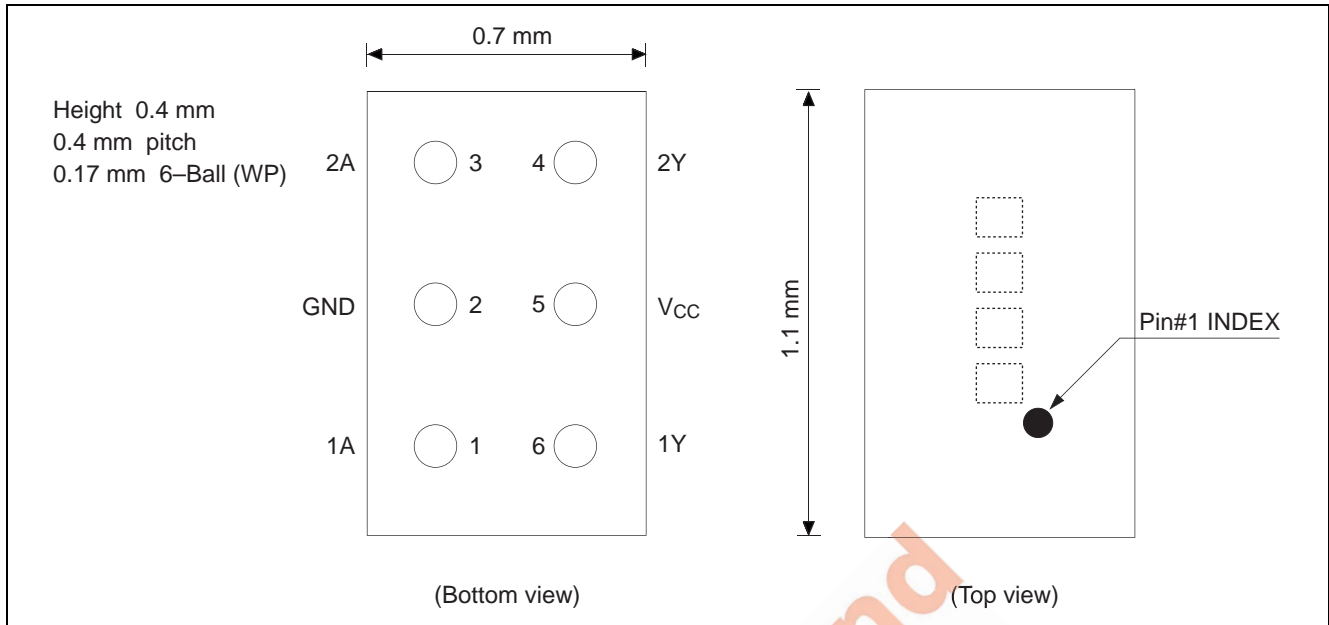
Input A	Output Y
L	L
H	Z

H: High level

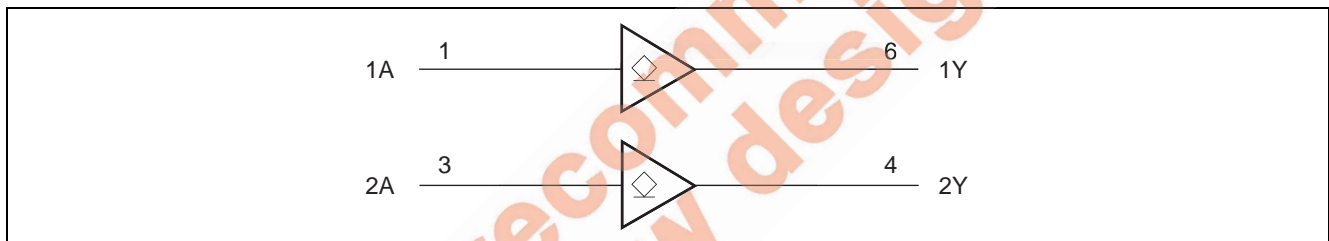
L: Low level

Z: High impedance

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	-0.5 to 6.5	V	
Input voltage range ^{*1}	V_I	-0.5 to 6.5	V	
Output voltage range ^{*1, 2}	V_O	-0.5 to $V_{CC} + 0.5$	V	Output : L
		-0.5 to 6.5		V_{CC} : OFF or Output : Z
Input clamp current	I_{IK}	-50	mA	$V_I < 0$
Output clamp current	I_{OK}	-50	mA	$V_O < 0$
Continuous output current	I_O	± 50	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 100	mA	
Package Thermal impedance	θ_{ja}	123	°C/W	WP
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
Output current	I_{OL}	—	4	mA	$V_{CC} = 1.65\text{ V}$
		—	8		$V_{CC} = 2.3\text{ V}$
		—	16		$V_{CC} = 3.0\text{ V}$
		—	24		$V_{CC} = 4.5\text{ V}$
		—	32		$V_{CC} = 4.5\text{ V}$
Input transition rise or fall rate	$\Delta t / \Delta v$	0	20	ns / V	$V_{CC} = 1.65\text{ to }1.95\text{ V,}$ 2.3 to 2.7 V
		0	10		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		0	5		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
Operating free-air temperature	T_a	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

$T_a = -40\text{ to }85^\circ\text{C}$

Item	Symbol	V_{CC} (V)	Min	Typ	Max	Unit	Test condition
Input voltage	V_{IH}	1.65 to 1.95	$V_{CC} \times 0.65$	—	—	V	
		2.3 to 2.7	1.7	—	—		
		3.0 to 3.6	2.0	—	—		
		4.5 to 5.5	$V_{CC} \times 0.7$	—	—		
	V_{IL}	1.65 to 1.95	—	—	$V_{CC} \times 0.35$		
		2.3 to 2.7	—	—	0.7		
		3.0 to 3.6	—	—	0.8		
		4.5 to 5.5	—	—	$V_{CC} \times 0.3$		
Output voltage	V_{OL}	Min to Max	—	—	0.1	V	$I_{OL} = 100\text{ }\mu\text{A}$
		1.65	—	—	0.45		$I_{OL} = 4\text{ mA}$
		2.3	—	—	0.3		$I_{OL} = 8\text{ mA}$
		3.0	—	—	0.4		$I_{OL} = 16\text{ mA}$
		—	—	—	0.55		$I_{OL} = 24\text{ mA}$
		4.5	—	—	0.55		$I_{OL} = 32\text{ mA}$
Input current	I_{IN}	0 to 5.5	—	—	± 5	μA	$V_{IN} = 5.5\text{ V or GND}$
Off state output current	I_{OZ}	5.5	—	—	10	μA	$V_O = 5.5\text{ V or GND}$
Quiescent supply current	I_{CC}	1.65 to 5.5	—	—	10	μA	$V_{IN} = V_{CC}\text{ or GND,}$ $I_O = 0$
	ΔI_{CC}	3 to 5.5	—	—	500		One input at $V_{CC}-0.6\text{ V,}$ Other input at $V_{CC}\text{ or GND}$
Output leakage current	I_{OFF}	0	—	—	± 10	μA	$V_{IN}\text{ or }V_O = 0\text{ to }5.5\text{ V}$
Input capacitance	C_{IN}	3.3	—	3.5	—	pF	$V_{IN} = V_{CC}\text{ or GND}$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

$V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{ZL} t_{LZ}	2.4	8.3	ns	$C_L = 30 \text{ pF}$, $R_L = 1.0 \text{ k}\Omega$	A	Y

$V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{ZL} t_{LZ}	1.0	5.5	ns	$C_L = 30 \text{ pF}$, $R_L = 500 \Omega$	A	Y

$V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{ZL} t_{LZ}	1.5	4.2	ns	$C_L = 50 \text{ pF}$, $R_L = 500 \Omega$	A	Y

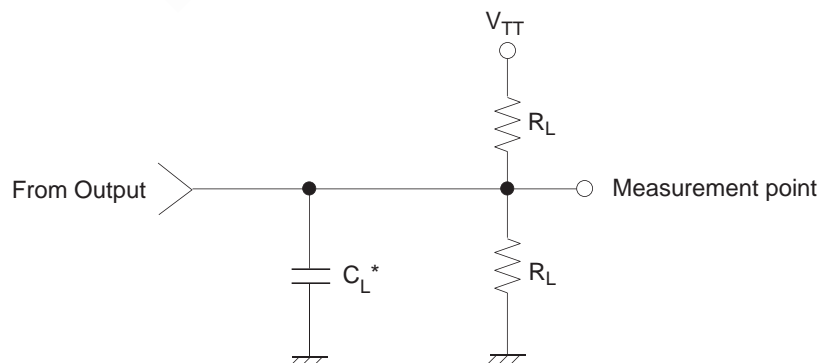
$V_{CC} = 5.0 \pm 0.5 \text{ V}$

Item	Symbol	Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{ZL} t_{LZ}	1.0	3.5	ns	$C_L = 50 \text{ pF}$, $R_L = 500 \Omega$	A	Y

Operating Characteristics

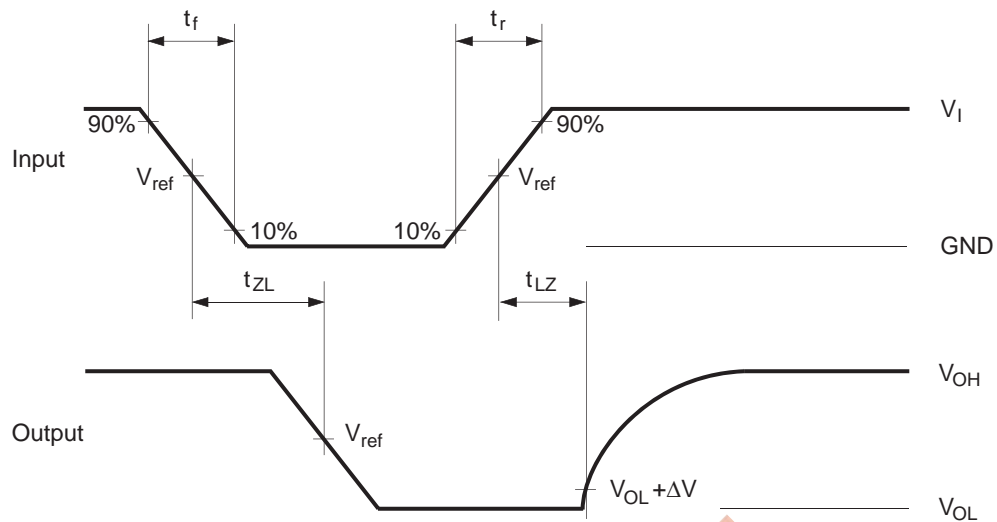
Item	Symbol	V _{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C _{PD}	1.8	—	16	—	pF	f = 10 MHz
		2.5	—	16	—		
		3.3	—	16	—		
		5.0	—	18	—		

Test Circuit



Note: C_L includes probe and jig capacitance.

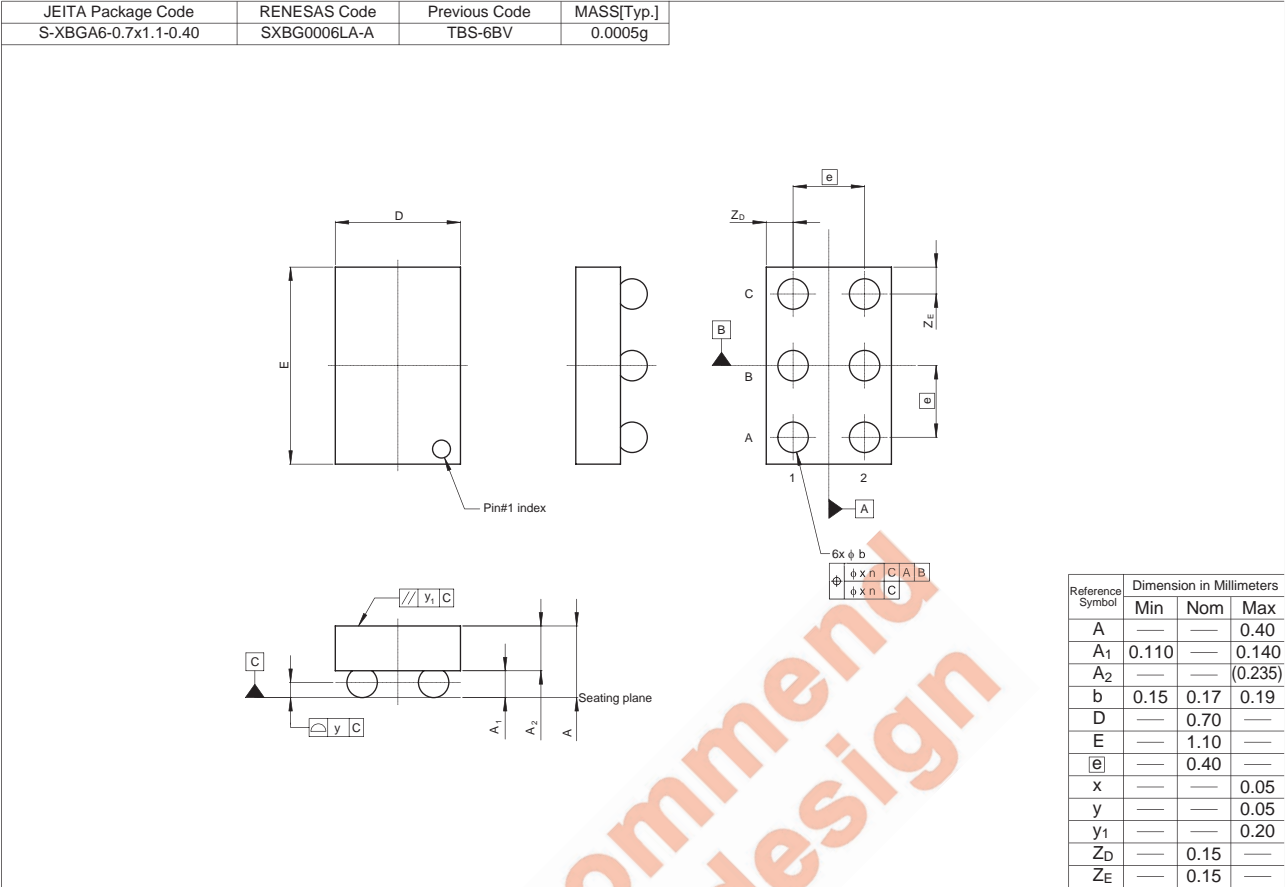
• Waveforms



V_{CC} (V)	Inputs		V_{ref}	C_L	R_L	ΔV	V_{TT}
	V_I	t_r / t_f					
1.8 ± 0.15	V_{CC}	≤ 2 ns	$V_{CC} / 2$	30 pF	1.0 k Ω	0.15 V	$V_{CC} \times 2$
2.5 ± 0.2	V_{CC}	≤ 2 ns	$V_{CC} / 2$	30 pF	500 Ω	0.15 V	$V_{CC} \times 2$
3.3 ± 0.3	3 V	≤ 2.5 ns	1.5 V	50 pF	500 Ω	0.3 V	6 V
5.0 ± 0.5	V_{CC}	≤ 2.5 ns	$V_{CC} / 2$	50 pF	500 Ω	0.3 V	$V_{CC} \times 2$

Notes: 1. Input waveform: PRR ≤ 10 MHz, $Z_o = 50 \Omega$.
 2. The output are measured one at a time with one transition per measurement.

Package Dimensions



Notes:

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