

RBN75H125S1FP4-A0

1250V - 75A - IGBT
Power Switching

R07DS1382EJ0141
Rev.1.41
Oct.14.2021

Features

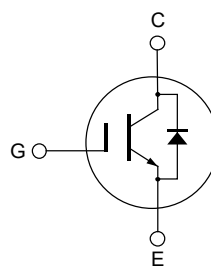
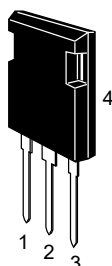
- Trench gate and thin wafer technology (G8H series)
- Built in fast recovery diode in one package
- Low collector to emitter saturation voltage
 $V_{CE(sat)} = 1.8 \text{ V typ. (at } I_C = 75 \text{ A, } V_{GE} = 15 \text{ V, } T_a = 25^\circ\text{C)}$
- Quality grade: Standard
- High speed switching
- Short circuit withstands time (10 $\mu\text{s min.}$)
- Applications: UPS, Welding, photovoltaic inverters, Power converter system

Key Performance

Type	V_{CES}	I_C	$V_{CE(sat)}, T_C = 25^\circ\text{C}$	I_F	t_{sc}	T_j
RBN75H125S1FP4-A0	1250 V	75 A	1.8 V	50 A	10 μs	175 $^\circ\text{C}$

Outline

RENESAS Package code: PRSS0003ZQ-A
(Package name: TO-247plus)



1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

(T_c = 25°C)

Item		Symbol	Ratings	Unit
Collector to emitter voltage		V _{CES}	1250	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	T _c = 25 °C	I _C	150	A
	T _c = 100 °C	I _C	75	A
Collector peak current		I _{C(peak)} Notes1	300	A
Diode forward current	T _c = 25 °C	I _F	100	A
	T _c = 100 °C	I _F	50	A
Diode forward peak current		I _{F(peak)} Notes1	300	A
Collector power dissipation		P _C Notes2	517	W
Junction temperature		T _J Notes2	175	°C
Storage temperature		T _{stg}	–55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C.
Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175 °C.

Thermal Resistance Characteristics

(T_c = 25°C)

Item	Symbol	Max. Value Notes3	Unit
Junction to case thermal resistance (IGBT)	R _{th(j-c)}	0.29	°C/W
Junction to case thermal resistance (Diode)	R _{th(j-c)}	0.65	°C/W

Notes: 3. Designed target value on Renesas measurement condition. (Not tested)

Electrical Characteristics

(Tc = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector to emitter leakage current	ICES	—	—	200	μA	VCE = 1250 V, VGE = 0 V
Gate to emitter leakage current	IGES	—	—	±1	μA	VGE = ±30 V, VCE = 0 V
Gate to emitter threshold voltage	VGE(th)	5.3	—	7.1	V	VCE = 10 V, IC = 2.5 mA
Collector to emitter saturation voltage	VCE(sat)	—	1.8	2.34	V	IC = 75 A, VGE = 15 V ^{Notes4}
Input capacitance	Cies	—	4200	—	pF	VCE = 25 V VGE = 0 V f = 1 MHz
Output capacitance	Coēs	—	240	—	pF	
Reverse transfer capacitance	Cres	—	30	—	pF	
Total gate charge	Qg	—	150	—	nC	VGE = 15 V VCE = 600 V IC = 75 A
Gate to emitter charge	Qge	—	47	—	nC	
Gate to collector charge	Qgc	—	78	—	nC	
Turn-on delay time	td(on)	—	35	—	ns	VCC = 600 V VGE = 15 V/–15 V IC = 75 A Rg = 10 Ω Tc = 25 °C Inductive load ^{Notes5}
Rise time	tr	—	23	—	ns	
Turn-off delay time	td(off)	—	148	—	ns	
Fall time	tf	—	96	—	ns	
Turn-on loss energy	Eon	—	6.6	—	mJ	
Turn-off loss energy	Eoff	—	3.7	—	mJ	
Total switching energy	Etotal	—	10.3	—	mJ	
Turn-on delay time	td(on)	—	35	—	ns	
Rise time	tr	—	25	—	ns	
Turn-off delay time	td(off)	—	183	—	ns	
Fall time	tf	—	203	—	ns	VCC = 600 V VGE = 15 V/–15 V IC = 75 A Rg = 10 Ω Tc = 150 °C Inductive load ^{Notes5}
Turn-on loss energy	Eon	—	9.9	—	mJ	
Turn-off loss energy	Eoff	—	5.9	—	mJ	
Total switching energy	Etotal	—	15.8	—	mJ	
Short circuit withstand time ^{Notes6}	tsc	10	—	—	μs	VCC ≤ 720 V, VGE = 15 V Tc ≤ 150 °C

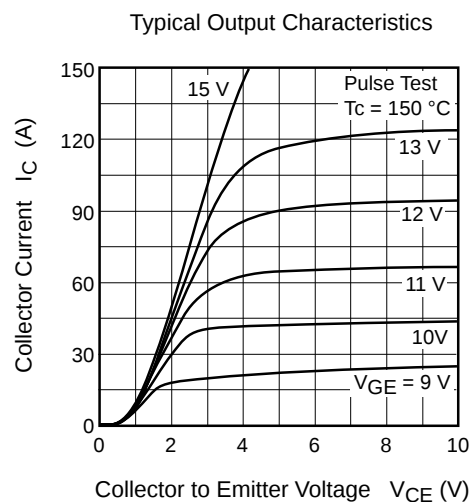
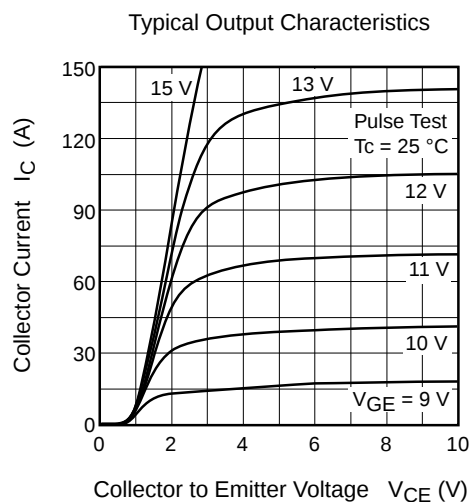
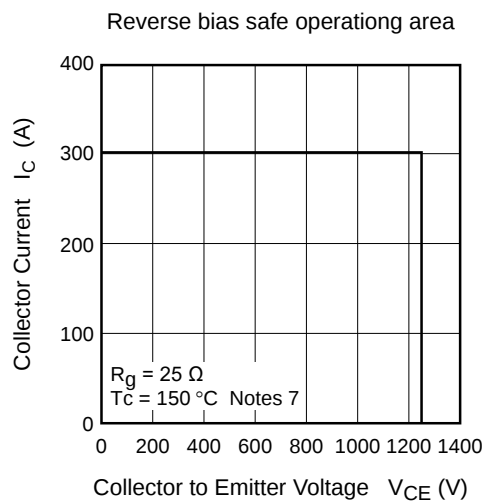
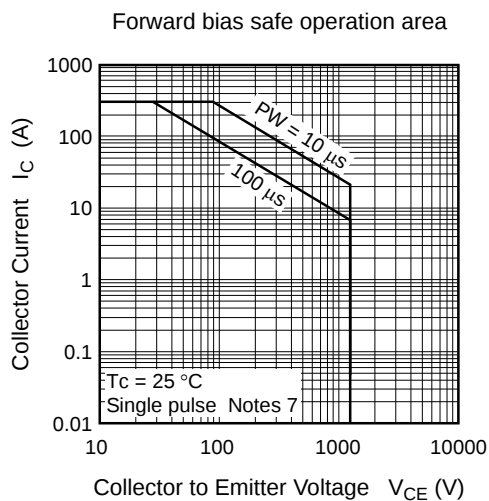
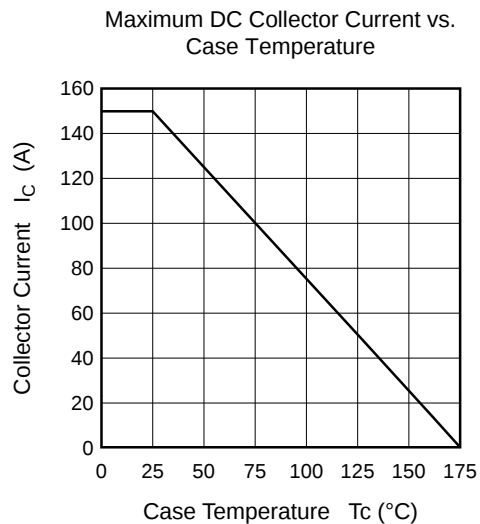
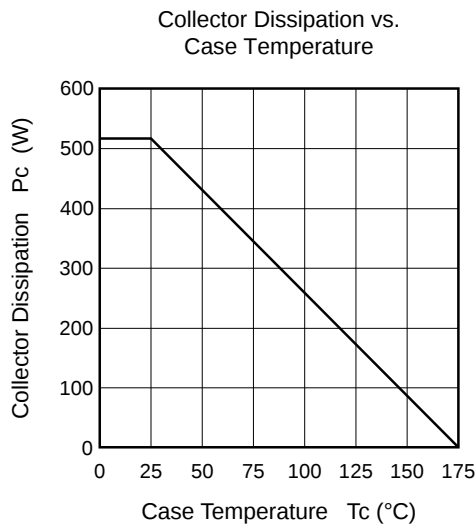
Diode forward voltage	VF	—	2.4	3.2	V	IF = 50 A ^{Notes4}
Diode reverse recovery time	trr	—	245	—	ns	IF = 50 A, diF/dt = 300 A/μs
Diode reverse recovery charge	Qrr	—	2.2	—	μC	
Diode peak reverse recovery current	Irr	—	16	—	A	

Notes: 4. Pulse test

5. Switching time test circuit and waveform are shown below.

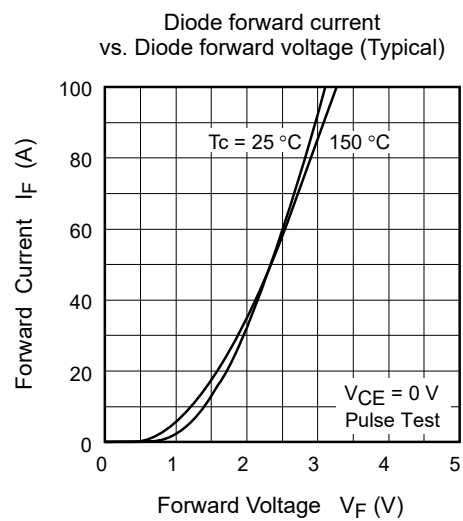
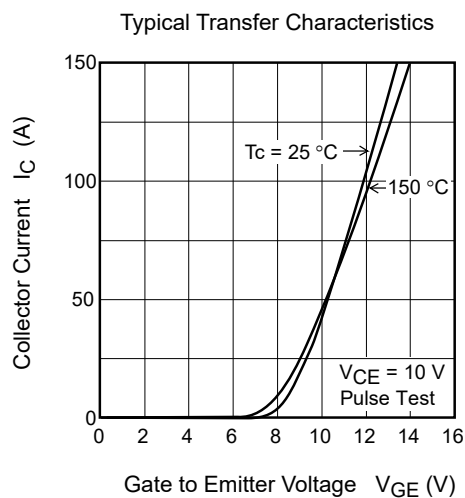
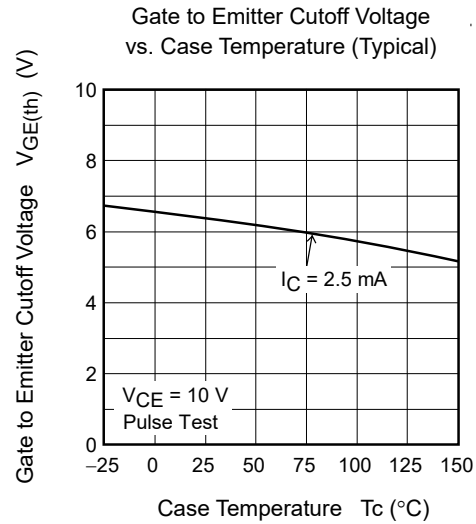
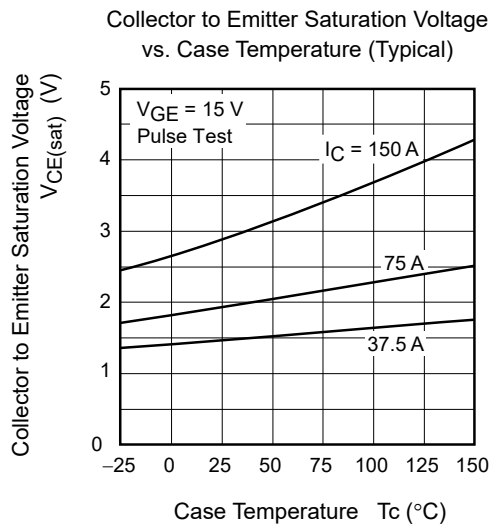
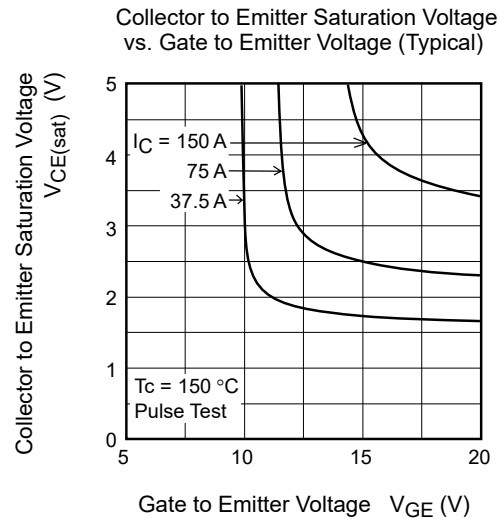
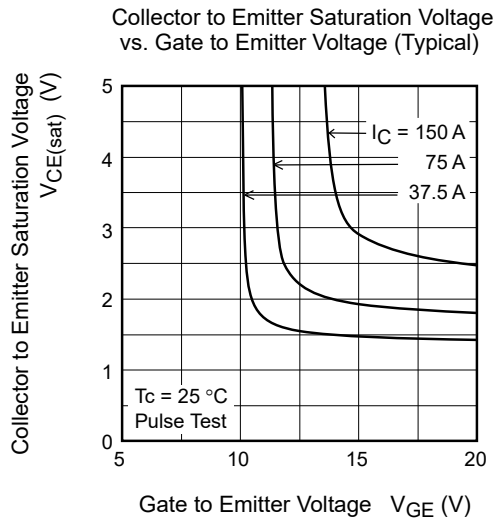
6. Designed target value on Renesas measurement condition. (Not tested)

Main Characteristics

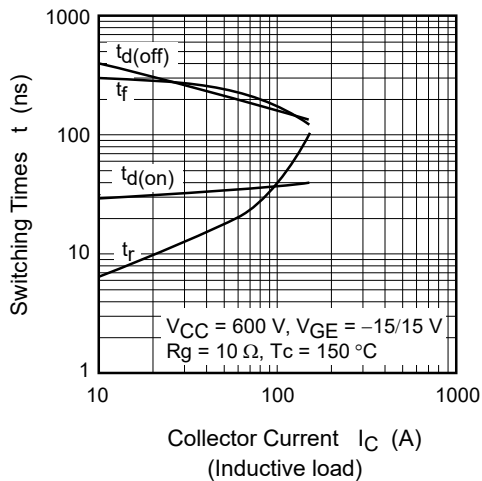


Notes: 7. Designed target value on Renesas measurement condition. (Not tested)

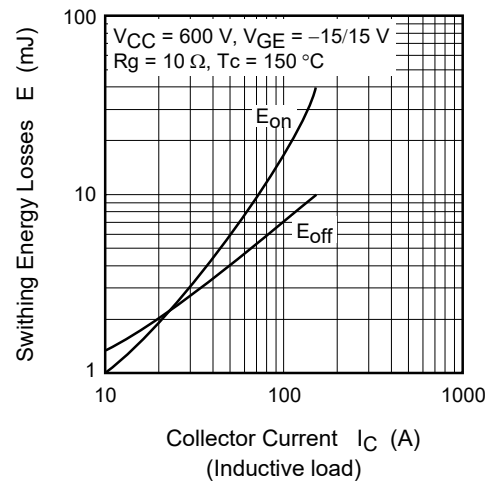
Renesas recommends that operating conditions are designed according to a document "Power MOS FET · IGBT Attention of Handling Semiconductor Devices".



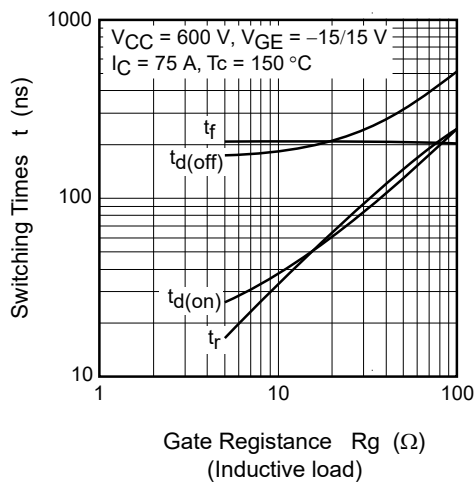
Switching Characteristics (Typical) (1)



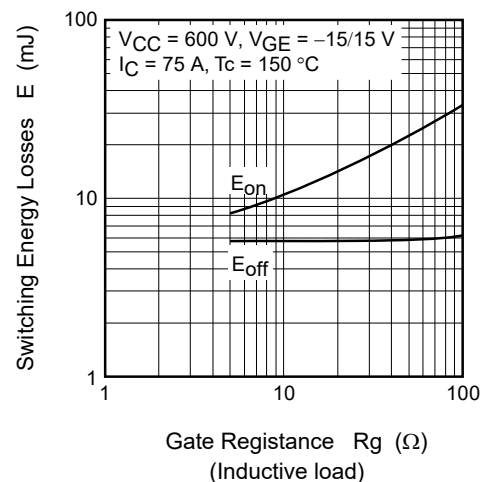
Switching Characteristics (Typical) (2)



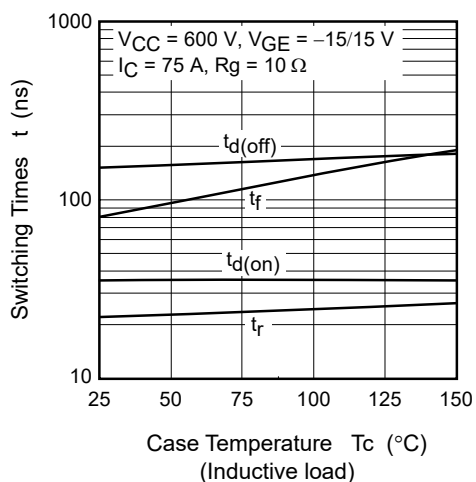
Switching Characteristics (Typical) (3)



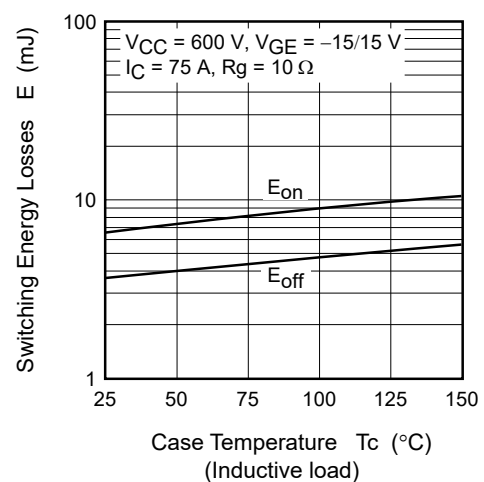
Switching Characteristics (Typical) (4)

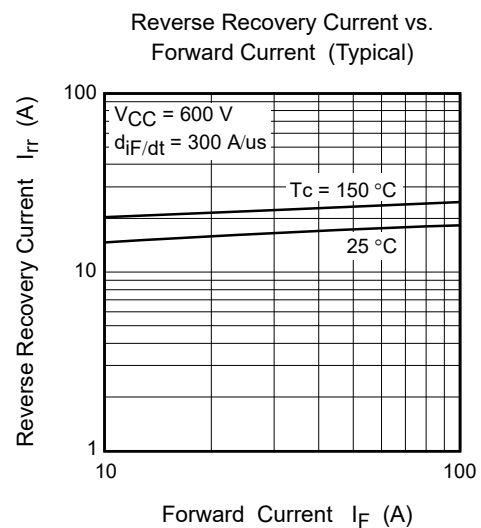
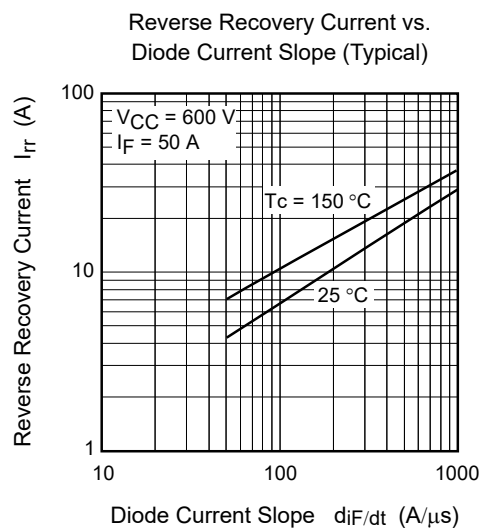
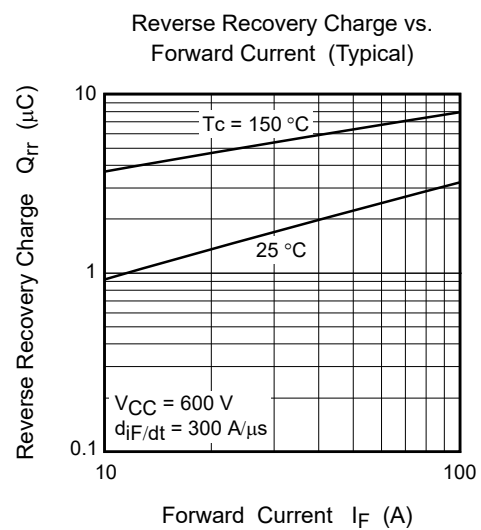
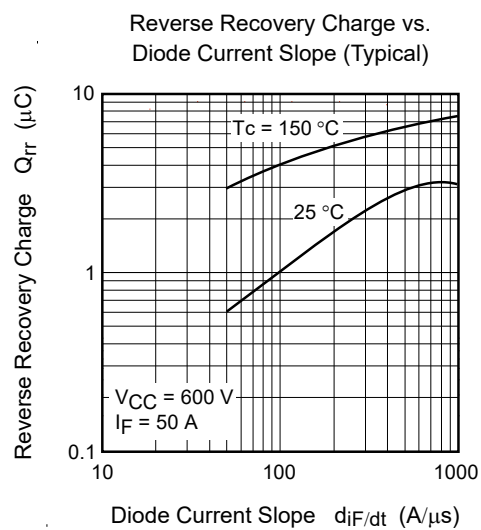
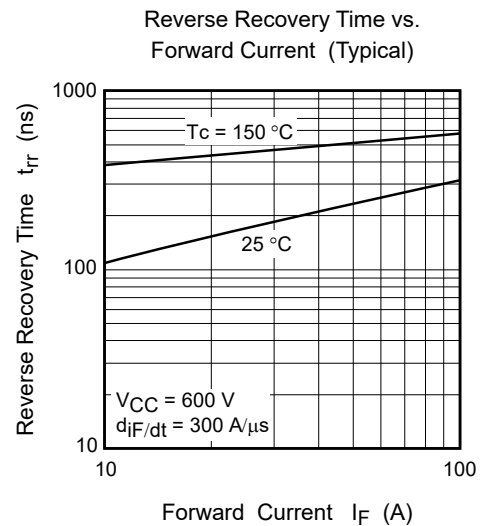
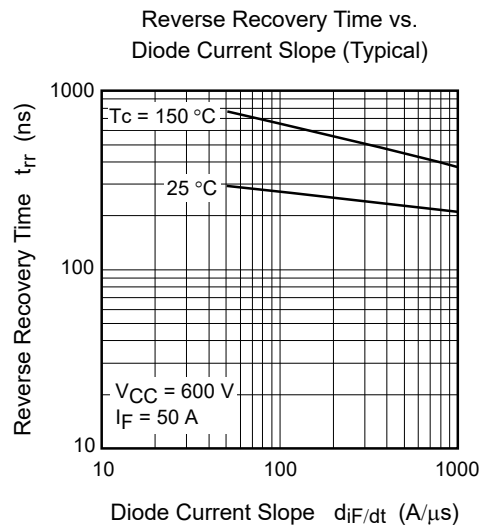


Switching Characteristics (Typical) (5)

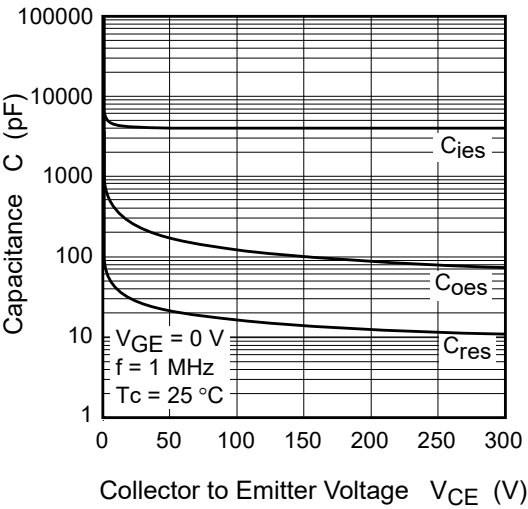


Switching Characteristics (Typical) (6)

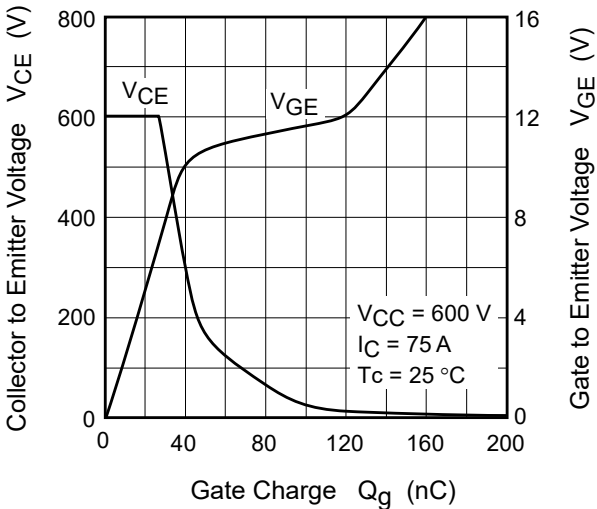


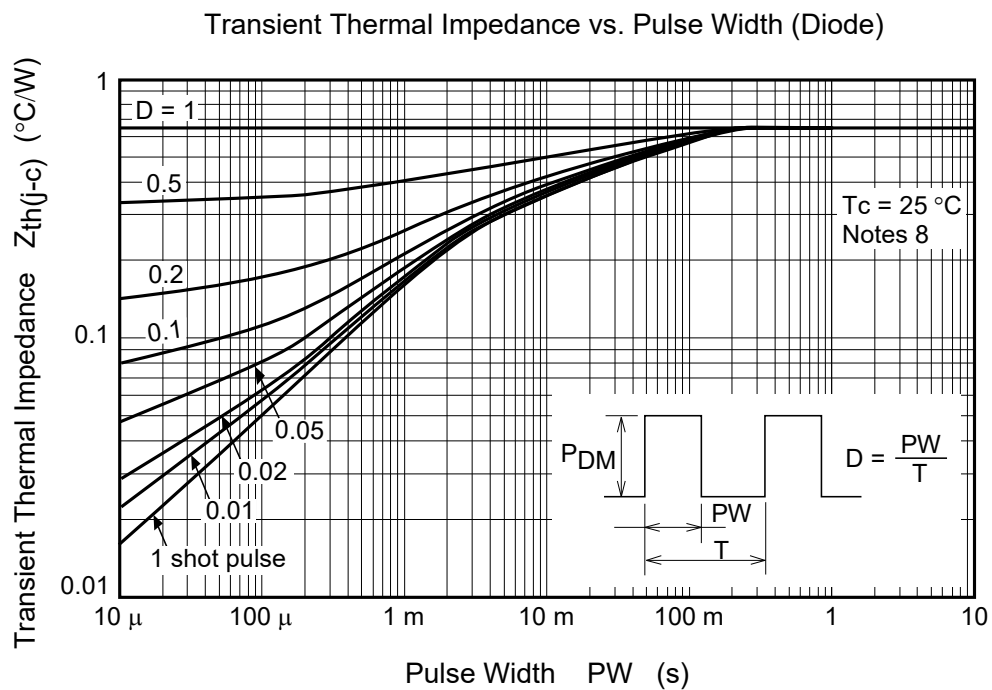
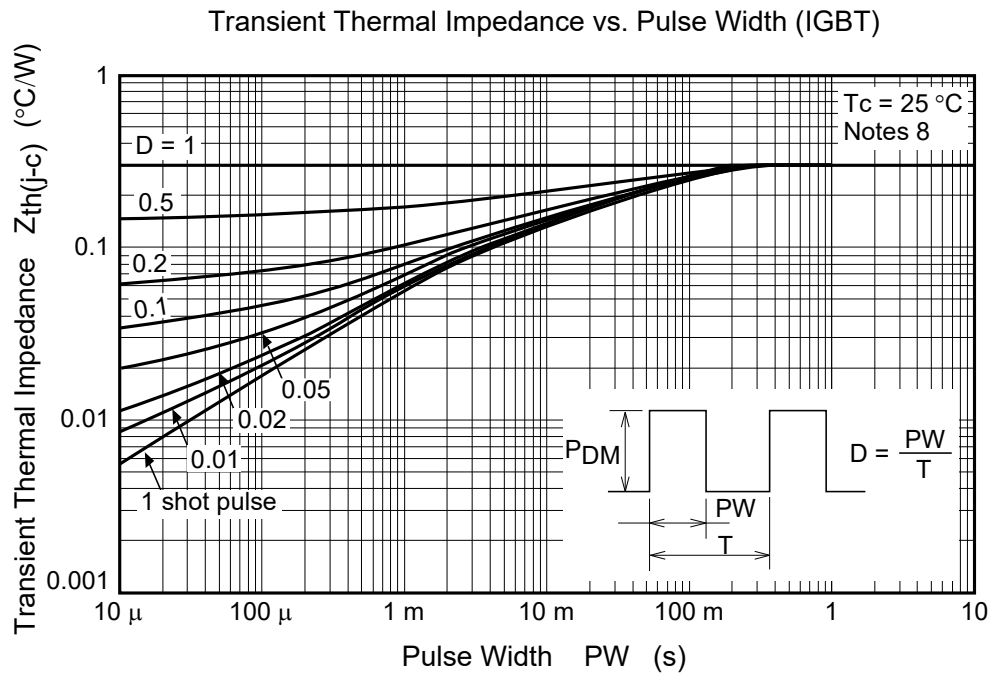


Typical Capacitance vs.
Collector to Emitter Voltage



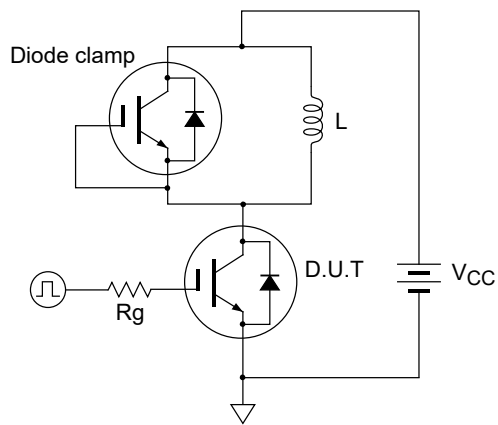
Dynamic Input Characteristics (Typical)



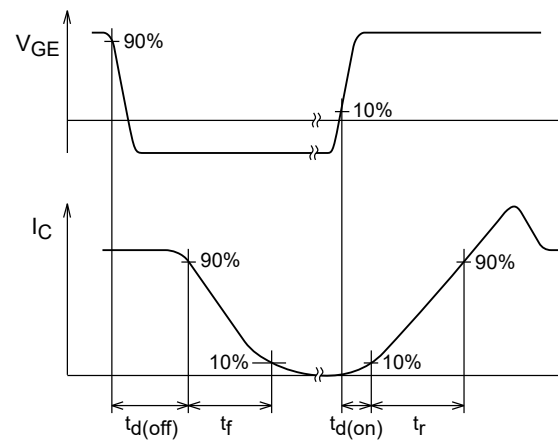


Notes: 8. Designed target value on Renesas measurement condition. (Not tested)

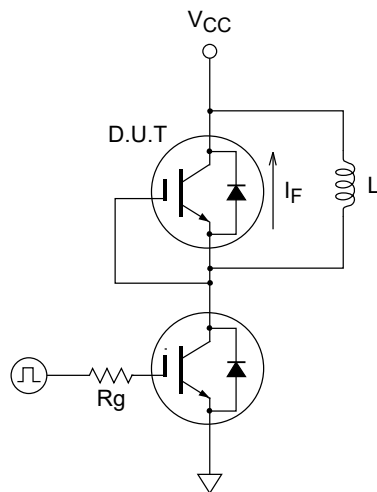
Switching Time Test Circuit



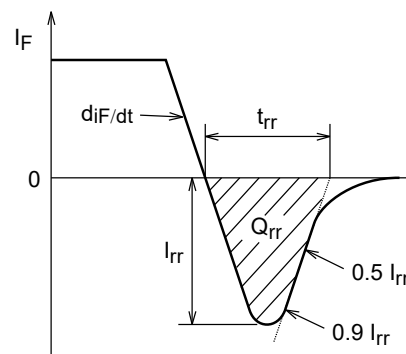
Waveform



Diode Reverse Recovery Time Test Circuit



Waveform



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