RBA100N04DANS-4UA02

N-Channel Power MOSFET

40V - 100A - 2.3mΩ

Description

The RBA100N04DANS-4UA02 is an AEC-Q101 qualified N-channel power MOSFET featuring low RDS(on) and low input capacitance for high-speed switching and low power loss. It uses a Renesas SO8-FL 5 x 6 mm² flat-lead, copper-clip package, supporting high current with excellent thermal performance, durability, and reliability, making it ideal for automotive applications such as power management, motor drives, and e-fuse.

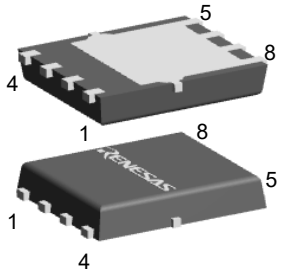
Features

- Standard-level gate drive voltage: $V_{GS(th)}$ = 2.0~4.0V
- Super low on-state resistance: $R_{DS(on)}$ = 2.3mΩ Max.
- Low input capacitance
- Low thermal resistance
- 100% Avalanche tested
- AEC-Q101 qualified
- Production Part Approval Process (PPAP) capable
- Pb-free lead plating: RoHS compliant
- MSL1 classified according to IPC/JEDEC J-STD-020

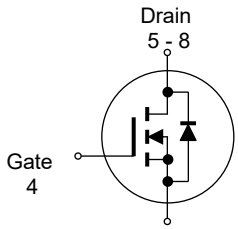
Application

- 12V/24V load EPS, ABS, BMS, e-fuse, etc.

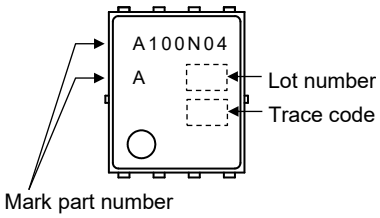
Outline



SO8-FL



Equivalent Circuit



Marking Specification

Absolute Maximum Ratings

(T_j = 25 °C unless otherwise notice.)

Item	Symbol	Ratings	Unit
Drain to Source Voltage	V_{DSS}	40	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	$I_{D(DC)}$ Note 1,2,5	±100	A
Drain Current (pulse)	$I_{D(pulse)}$ Note 1,3,5	±300	A
Power Dissipation	P_D Note 1,5	88	W
Junction Temperature	T_j	175	°C
Storage Temperature	T_{stg}	-55 to 175	°C
Single Avalanche Current	I_{AS} Note 4	38	A
Single Avalanche Energy	E_{AS} Note 4	144	mJ

Thermal Resistance

Item	Symbol	Max.	Unit
Junction to Case Thermal Resistance	$R_{th(j-c)}$ Note 5	1.7	°C/W
Junction to Ambient Thermal Resistance	$R_{th(j-a)}$ Note 5,6	50	°C/W

Electrical Characteristics

(T_j = 25 °C unless otherwise notice.)

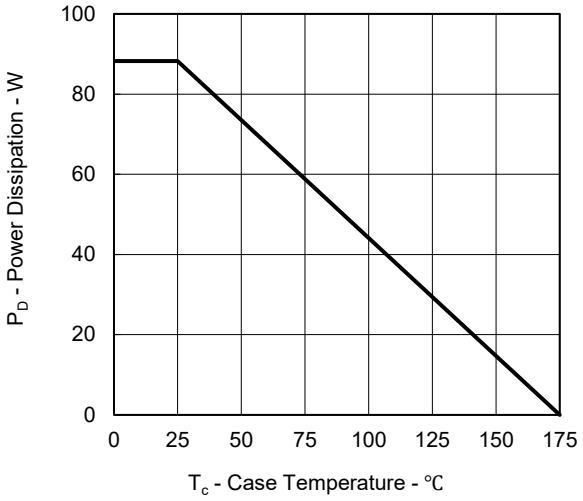
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	V_{DS} = 40 V, V_{GS} = 0 V
Gate Leakage Current	I_{GSS}	—	—	±100	nA	V_{GS} = ± 20 V, V_{DS} = 0 V
Gate to Source Threshold Voltage	$V_{GS(th)}$	2.0	—	4.0	V	V_{DS} = V_{GS}, I_D = 250 μA
Drain to Source On-state Resistance	$R_{DS(on)}$	—	1.86	2.3	mΩ	V_{GS} = 10 V, I_D = 50 A
Input Capacitance	C_{iss}	—	3200	—	pF	V_{DS} = 25 V
Output Capacitance	C_{oss}	—	370	—	pF	V_{GS} = 0 V
Reverse Transfer Capacitance	C_{rss}	—	240	—	pF	f = 1 MHz
Gate Resistance	R_g	—	1.7	—	Ω	
Turn-on Delay Time	$t_{d(on)}$	—	32	—	ns	V_{DD} = 20 V, I_D = 50 A
Rise Time	t_r	—	60	—	ns	V_{GS} = 10 V
Turn-off Delay Time	$t_{d(off)}$	—	70	—	ns	R_G = 5 Ω
Fall Time	t_f	—	17	—	ns	
Total Gate Charge	Q_g	—	60	—	nC	V_{DD} = 20 V
Gate to Source Charge	Q_{gs}	—	16	—	nC	V_{GS} = 10 V
Gate to Drain Charge	Q_{gd}	—	16	—	nC	I_D = 50 A
Gate Plateau Voltage	$V_{plateau}$	—	5.0	—	V	
Output Charge	Q_{oss}	—	20	—	nC	V_{DD} = 20 V, V_{GS} = 0 V
Body Diode Forward Voltage	$V_{F(S-D)}$	—	0.83	1.5	V	I_F = 50 A, V_{GS} = 0 V
Reverse Recovery Time	t_{rr}	—	46	—	ns	I_F = 50 A, V_{GS} = 0 V
Reverse Recovery Charge	Q_{rr}	—	48	—	nC	di/dt = 100 A/μs

Note 1. T_c = 25°C

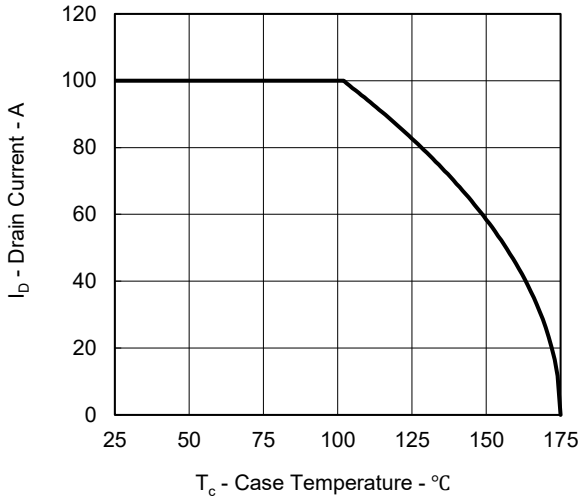
2. Value is limited by overall system design including PCB.
3. PW ≤ 10 μs, Duty Cycle ≤ 1%
4. L = 100 μH, V_{DD} = 20 V, V_{GS} = 20 → 0 V, R_G = 25 Ω
5. Defined by design. Not subject to production test.
6. Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4. (2 oz Cu pad.)

Typical Characteristics

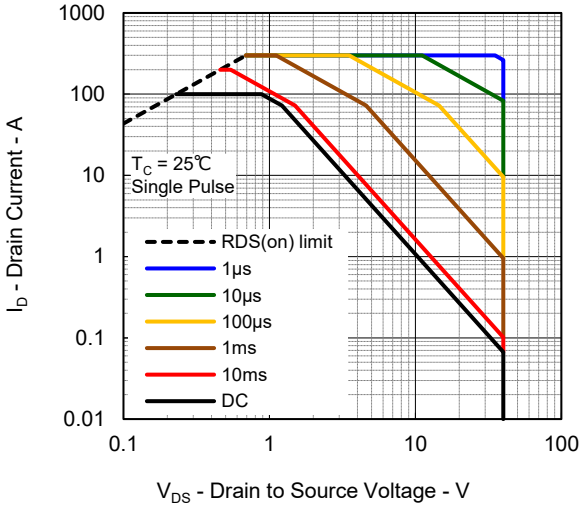
POWER DISSIPATION vs. CASE TEMPERATURE



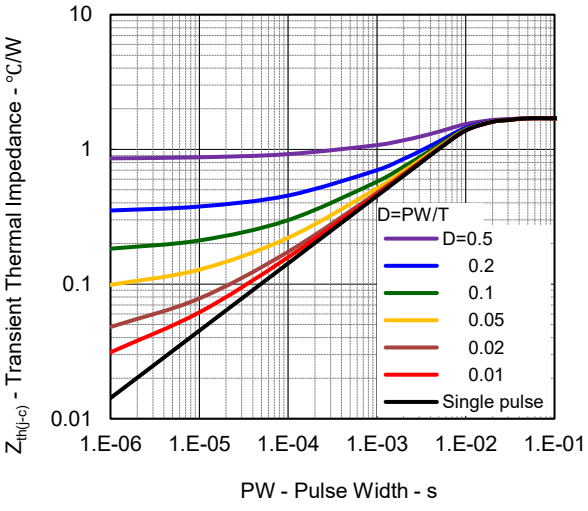
DRAIN CURRENT vs. CASE TEMPERATURE



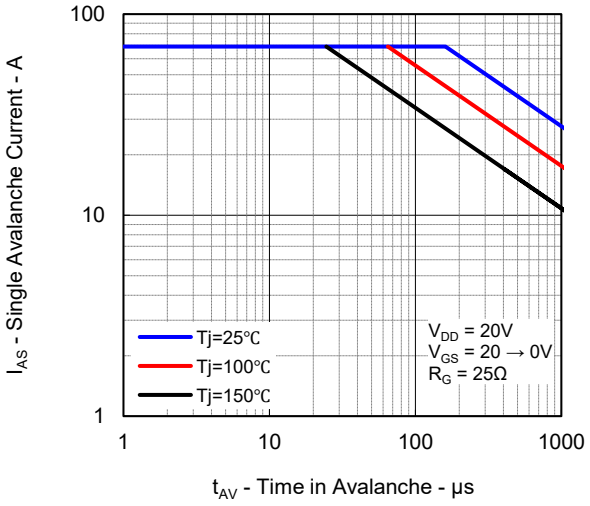
FORWARD BIAS SAFE OPERATING AREA



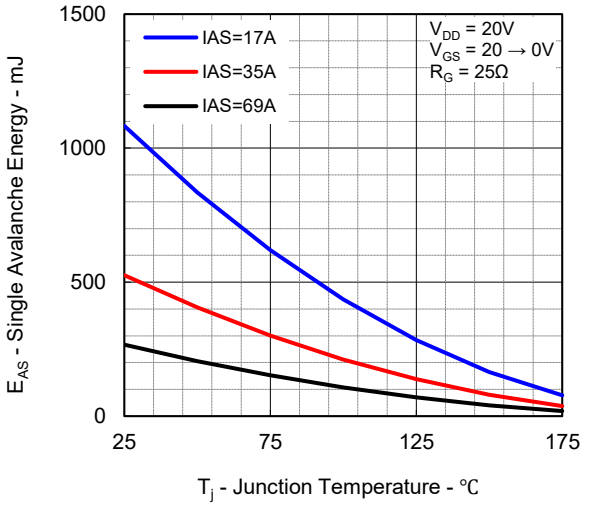
TRANSIENT THERMAL IMPEDANCE vs. PULSE WIDTH



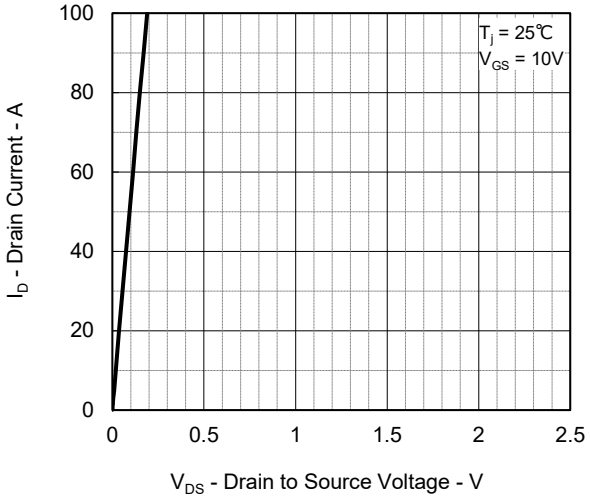
TYPICAL AVALANCHE CHARACTERISTICS



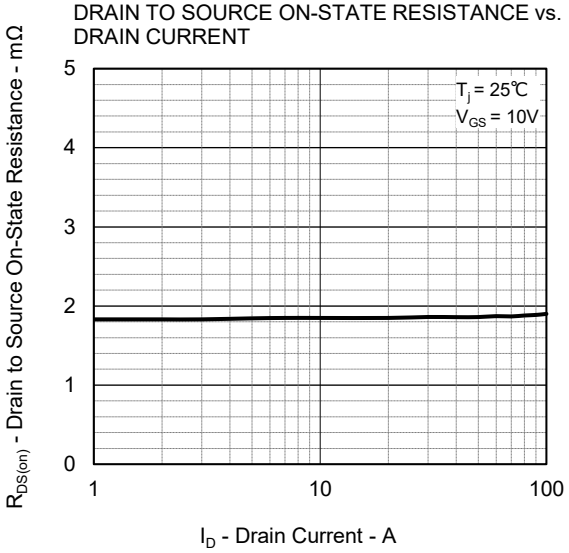
TYPICAL AVALANCHE ENERGY



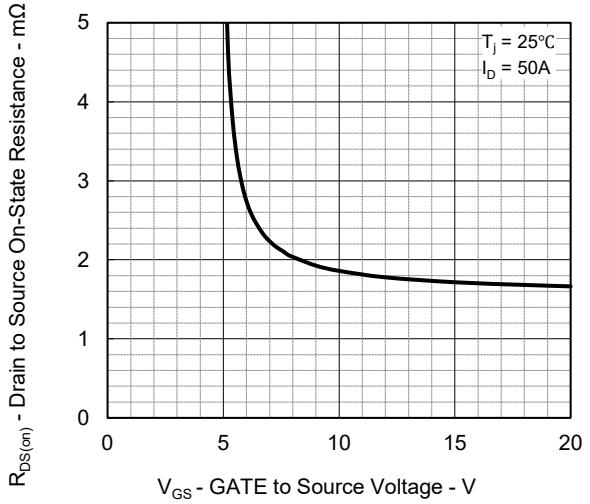
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



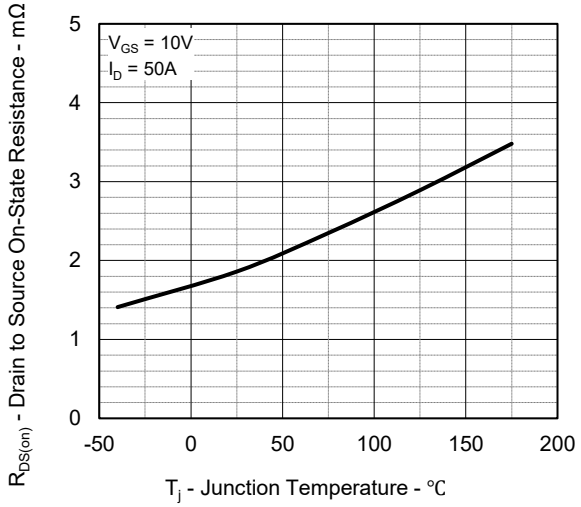
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



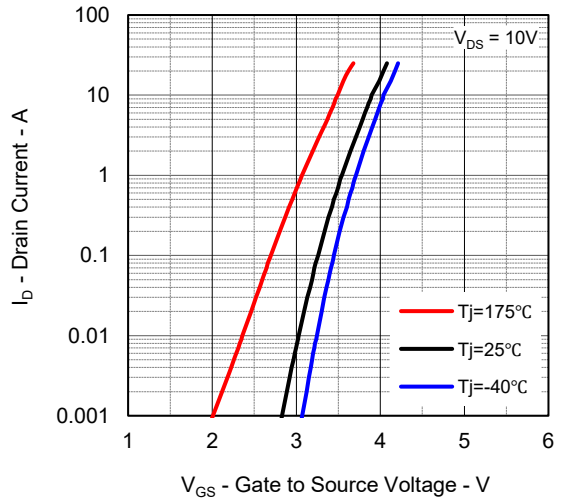
DRAIN TO SOURCE ON-STATERESISTANCE vs. GATE TO SOURCE VOLTAGE



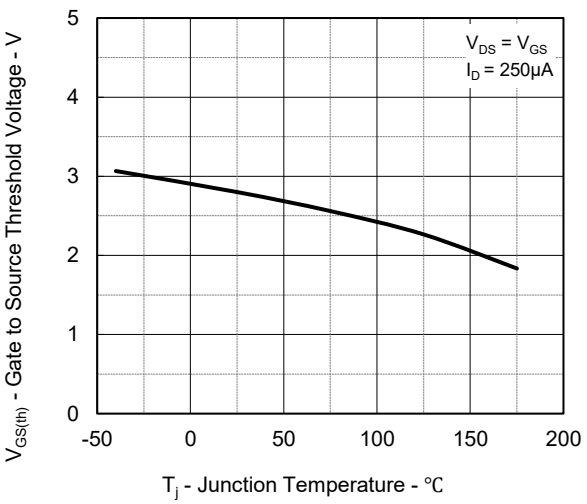
DRAIN TO SOURCE ON-STATE RESISTANCE vs JUNCTION TEMPERATURERE



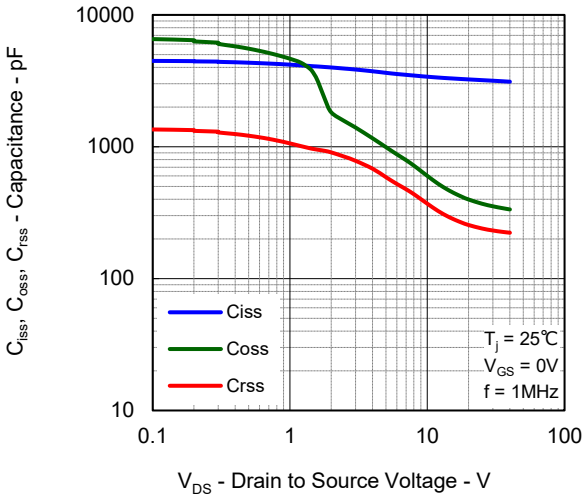
FORWARD TRANSFER CHARACTERISTICS



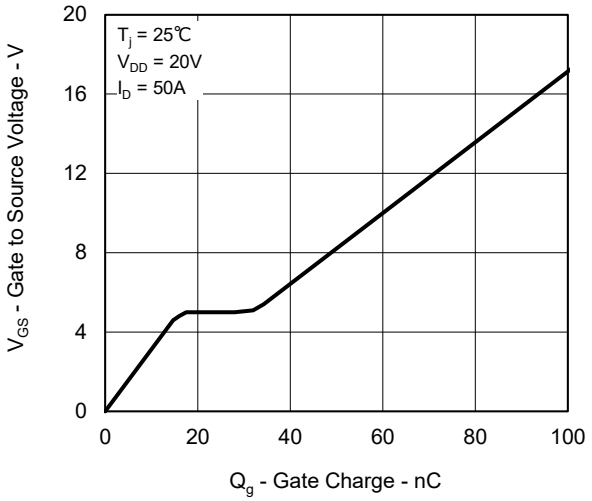
GATE TO SOURCE THRESHOLD VOLTAGE vs. JUNCTION TEMPERATURE



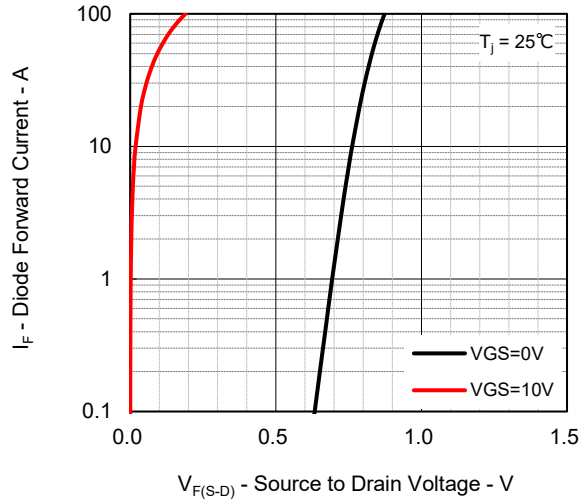
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



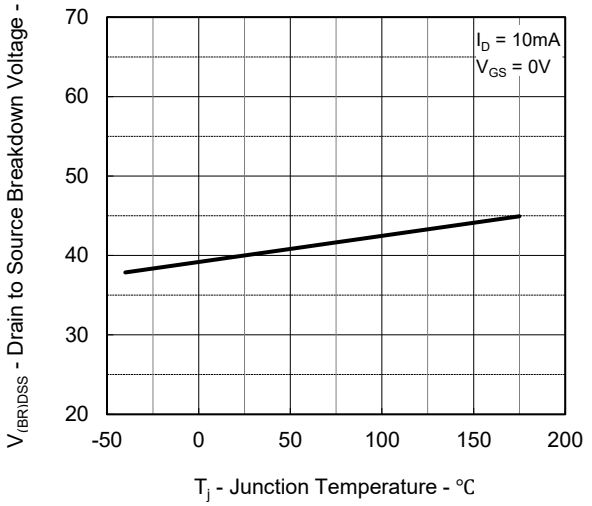
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



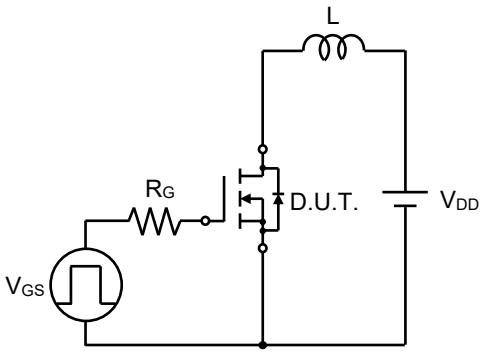
DRAIN TO SOURCE BREAKDOWN VOLTAGE vs. JUNCTION TEMPERATURE



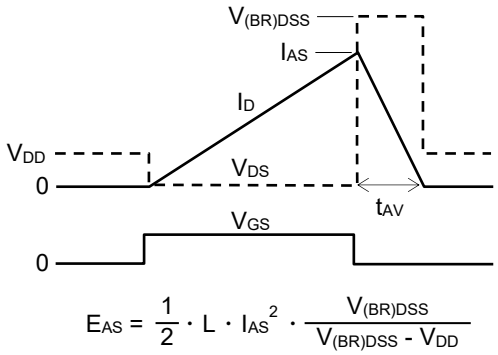
Test Circuit

Avalanche

Test Circuit



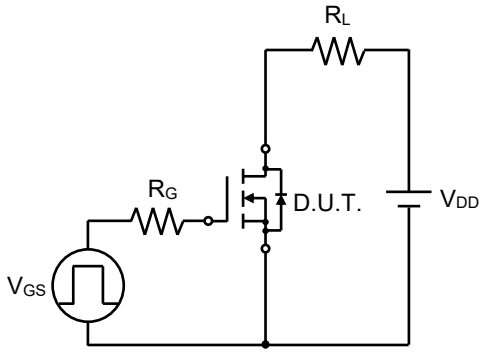
Waveform



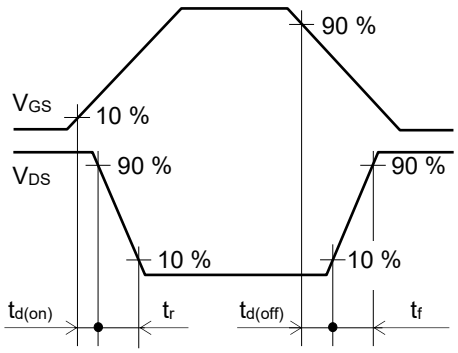
$$E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^{2} \cdot \frac{V_{(BR)DSS}}{V_{(BR)DSS} - V_{DD}}$$

Switching Time

Test Circuit

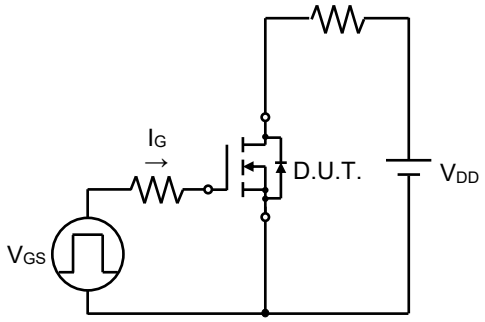


Waveform

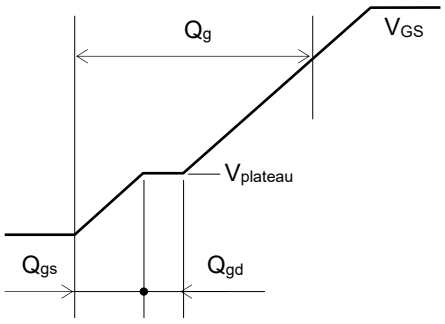


Gate Charge

Test Circuit

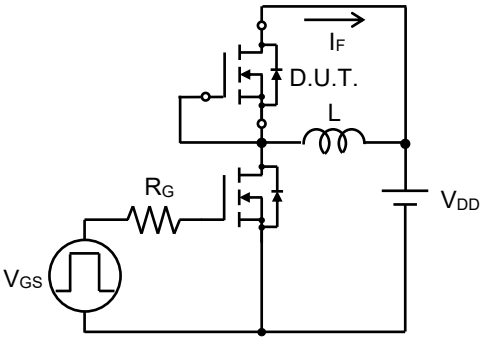


Waveform

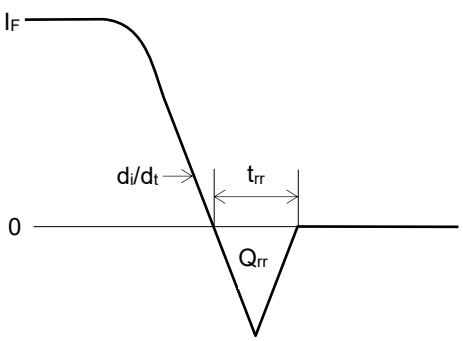


Reverse Recovery

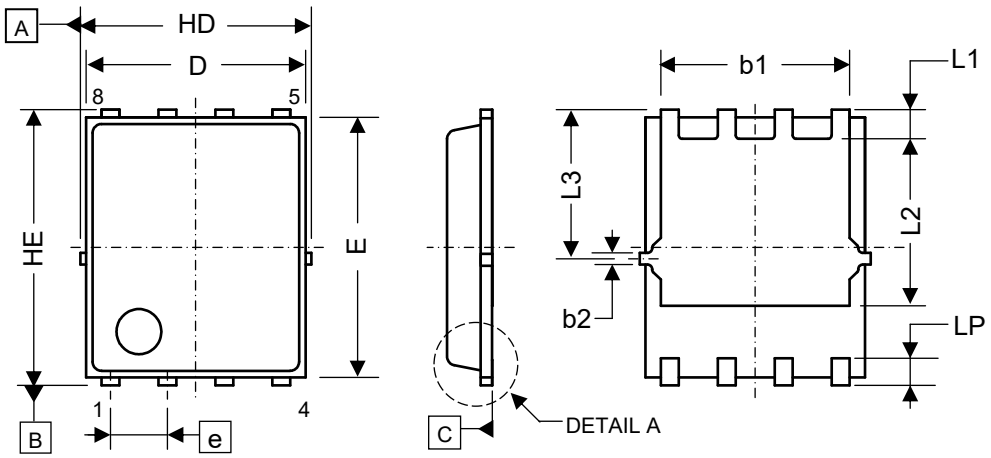
Test Circuit



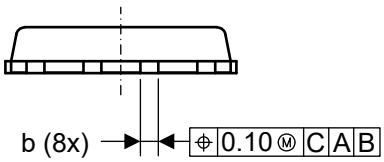
Waveform



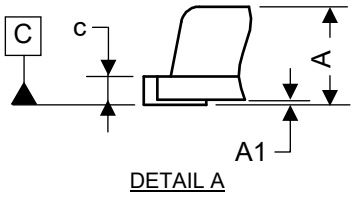
Package Dimensions



Mass : 0.1g

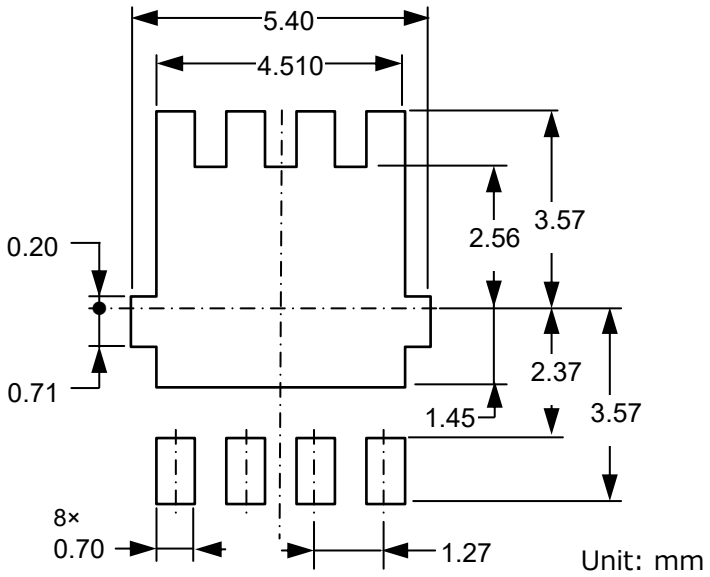


	DIMENSION (mm)		
SYMBOL	MIN	NOM	MAX
A	0.90	1.00	1.10
A1	0.00	-	0.05
b	0.30	0.40	0.50
b1	4.11	4.21	4.31
b2	-	0.26	-
c	0.15	0.25	0.35
D	4.75	4.90	5.05
E	5.60	5.75	5.90
e	1.27BSC		
HD	4.95	5.15	5.35
HE	5.90	6.10	6.30
L1	0.45	0.65	0.85
L2	3.49	3.69	3.89
L3	3.10	3.30	3.50
LP	0.40	0.60	0.80



DETAIL A

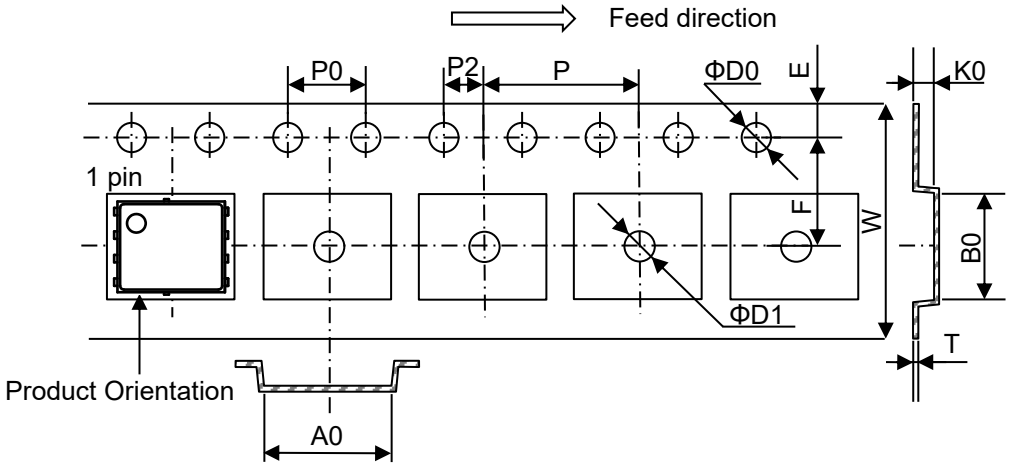
Mount Pad

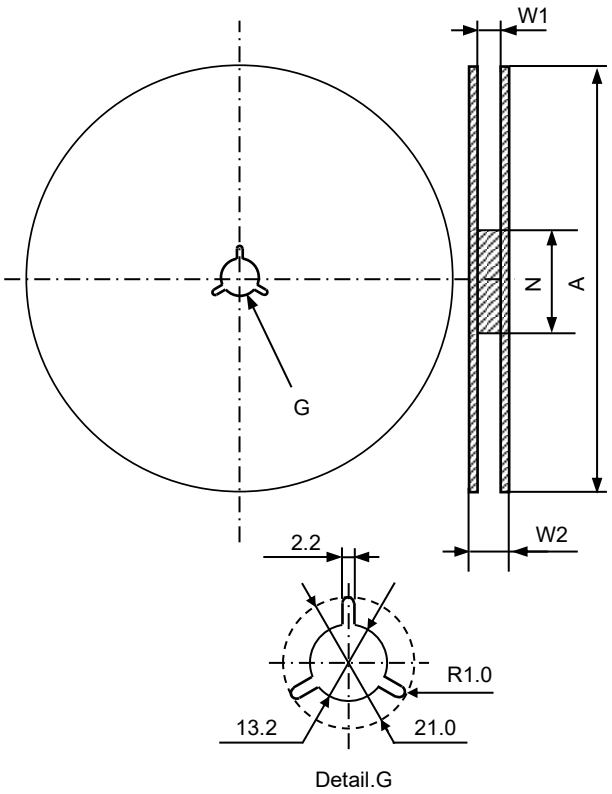


Ordering Information

Part No.	Packing	Quantity
RBA100N04DANS-4UA02#HB0	Taping	5000pcs/reel

Packing Specification





Unit : mm

	Symbol	Dimension
Tape Dimension	W	12
	P	8
	P0	4
	P2	2
	A0	6.35
	B0	5.35
	K0	1.2
	F	5.5
	E	1.75
	D0	1.5
	D1	1.5
	T	0.3
Reel Dimension	A	330
	N	100
	W1	13.5
	W2	17.4

Maximum storage	5000 pcs/Reel

Remark : Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.
Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook.

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