

# PS9351L, PS9351L2

HIGH NOISE REDUCTION, 15 Mbps CMOS OUTPUT TYPE 8mm CREEPAGE 6-PIN SDIP PHOTOCOUPLER

R08DS0127EJ0100 Rev.1.00 Apr 24, 2015

#### **DESCRIPTION**

The PS9351L and PS9351L2 are optically coupled isolator containing a GaAlAs LED on the input side and a CMOS output IC on the output side.

The PS9351L and PS9351L2 are in 6-pin plastic SDIP (Shrink Dual In-line Package). The PS9351L2 has 8 mm creepage distance. The mount area of 6-pin plastic SDIP is half size of 8-pin DIP.

This photocoupler is high common mode transient immunity (CMR), a high-speed CMOS output type device designed for high-speed logic interface circuits.

The PS9351L is lead bending type (Gull-wing) for surface mounting.

The PS9351L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

#### **FEATURES**

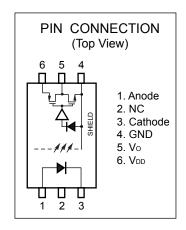
- High-speed response (15 Mbps)
- Long creepage distance (8mm MIN.: PS9351L2)
- Operable at high temperature ( $-40 \text{ to } +100^{\circ}\text{C}$ )
- High common mode transient immunity (CM<sub>H</sub>, CM<sub>L</sub> =  $\pm 20$  kV/ $\mu$ s TYP.)
- High isolation voltage (BV = 5000 Vr.m.s.)
- Pulse width distortion ( $|t_{PHL}-t_{PLH}| = 5$  ns TYP.)
- Ordering number of tape product : PS9351L-E3: 2,000 pcs/reel

: PS9351L2-E3: 2,000 pcs/reel

- Pb-Free product
- · Safety standards
  - UL approved: No. E72422
  - CSA approved: No. CA 101391 (CA5A, CAN/CSA-22.2 60065, 60950)
  - DIN EN 60747-5-5 (VDE0884-5) approved (Option)

## **APPLICATIONS**

- FA Network
- Measurement equipment
- PDP

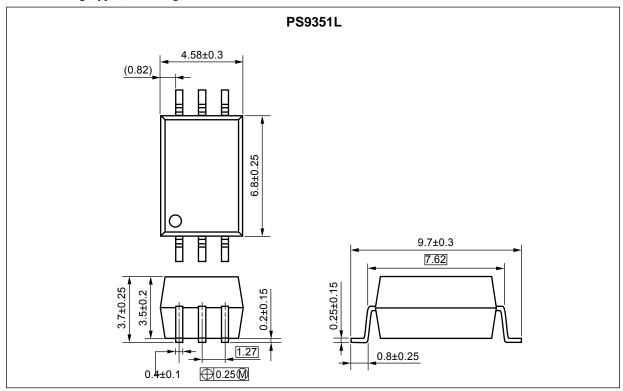


#### TRUTH TABLE

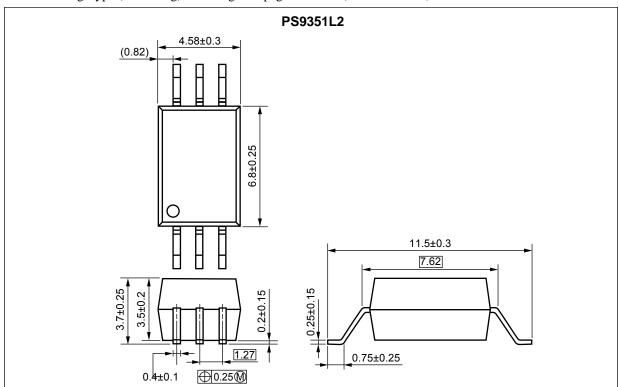
LED	Output		
ON	L		
OFF	Н		

## PACKAGE DIMENSIONS (UNIT: mm)

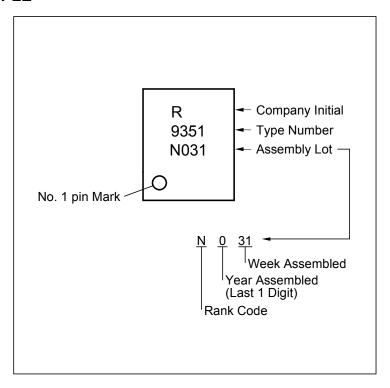
• Lead Bending Type (Gull-wing) For Surface Mount



• Lead Bending Type (Gull-wing) For Long Creepage Distance (Surface Mount)



## **MARKING EXAMPLE**



## PHOTOCOUPLER CONSTRUCTION

Parameter	PS9351L	PS9351L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

## **ORDERING INFORMATION**

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1	
PS9351L	PS9351L-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9351L	
PS9351L-E3	PS9351L-E3-AX	(Ni/Pd/Au)	Embossed Tape 2 000 pcs/reel	UL, CSA approved	F 3933 IL	
PS9351L2	PS9351L2-AX		20 pcs (Tape 20 pcs cut)	Standard products	PS9351L2	
PS9351L2-E3	PS9351L2-E3- AX		Embossed Tape 2 000 pcs/reel	Standard products UL, CSA approved	F39301L2	
PS9351L-V	PS9351L-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-5	PS9351L	
PS9351L-V-E3	PS9351L-V-E3- AX		Embossed Tape 2 000 pcs/reel	(VDE0884-5) approved (Option)	P39351L	
PS9351L2-V	PS9351L2-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-5	D00254L0	
PS9351L2-V-	PS9351L2-V-E3-		Embossed Tape 2 000	(VDE0884-5)	PS9351L2	
E3	AX		pcs/reel	approved (Option)		

Note: \*1. For the application of the Safety Standard, following part number should be used.

## **ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)**

Parameter		Symbol	Ratings	Unit
Diode	Diode Forward Current *1		25	mA
	Reverse Voltage	V <sub>R</sub>	5	V
Detector	Supply Voltage V <sub>DD</sub> 0 to 5.5		V	
	Output Voltage	Vo	−0.5 to V <sub>DD</sub> +0.5	V
	Output Current	Io	2	mA
Isolation Vo	oltage *2	BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +100	°C
Storage Temperature		T <sub>stg</sub>	-55 to +125	°C

Notes: \*1. Reduced to 0.3 mA/ $^{\circ}$ C at T<sub>A</sub> = 70 $^{\circ}$ C or more.

## RECOMMENDED OPERATING CONDITIONS ( $T_A = 25^{\circ}C$ )

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Forward Current	l <sub>F</sub>	10		16	mA
Supply Voltage	$V_{DD}$	4.5	5.0	5.5	V

<sup>\*2.</sup> AC voltage for 1 minute at  $T_A$  = 25°C, RH = 60% between input and output. Pins 1-3 shorted together, 4-6 shorted together.

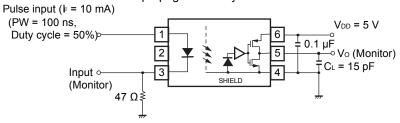
# ELECTRICAL CHARACTERISTICS ( $T_A = -40$ to +100°C, $V_{DD} = 4.5$ to 5.5 V, unless otherwise specified)

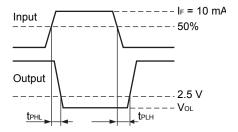
	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	$V_{F}$	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25°C	1.2	1.56	1.8	V
	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25°C			10	μΑ
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		30		pF
Detector	High Level Supply Current	I <sub>DDH</sub>	I <sub>F</sub> = 0 mA		2.5	5	mA
	Low Level Supply Current	I <sub>DDL</sub>	I <sub>F</sub> = 10 mA		2	5	
	High Level Output Voltage	Vон	$I_0 = -20 \mu A$ , $I_F = 0 mA$	4.0	5.0		V
	Low Level Output Voltage *2	Vol	I <sub>O</sub> = 20 μA, I <sub>F</sub> = 10 mA		0	0.1	
Coupled	Threshold Input Current	I <sub>FHL</sub>	V <sub>0</sub> < 1 V		1.9	5	mA
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1 kV <sub>DC</sub> , RH = 40 to 60%, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		0.7		pF
	Propagation Delay Time $(H \rightarrow L)^{*3}$	tрнL	$I_F$ = 10 mA, $V_{DD}$ = 5 V, $C_L$ = 15 pF, CMOS Levels		30	60	ns
	Propagation Delay Time $(L \rightarrow H)^{*3}$	t <sub>PLH</sub>			35	60	
	Pulse Width Distortion (PWD)*3	t <sub>PHL</sub> -t <sub>PLH</sub>			5	30	
	Propagation Delay Skew	<b>t</b> psk				40	
	Rise Time	tr			4		
	Fall Time	t <sub>f</sub>			4		
	Common Mode Transient Immunity at High Level Output*4	СМн	$V_{DD} = 5 \text{ V}, I_F = 0 \text{ mA},$ $V_{CM} = 1 \text{ kV}, V_O > 4 \text{ V},$ $T_A = 25^{\circ}\text{C}$	15	20		kV/μs
	Common Mode Transient Immunity at Low Level Output*4	CML	$V_{DD} = 5 \text{ V}, I_F = 10 \text{ mA},$ $V_{CM} = 1 \text{ kV}, V_O < 1 \text{ V},$ $T_A = 25^{\circ}\text{C}$	15	20		

Notes: \*1. Typical values at  $T_A = \overline{25^{\circ}C}$ 

<sup>\*2.</sup> Because VoL of 2 V or more may be output when LED current input and when output supply, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

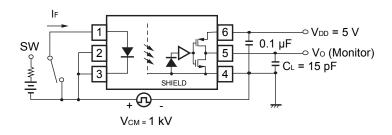
#### \*3 Test circuit for propagation delay time

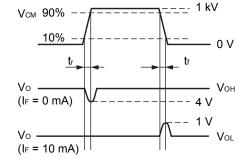




Remark CL includes probe and stray wiring capacitance.

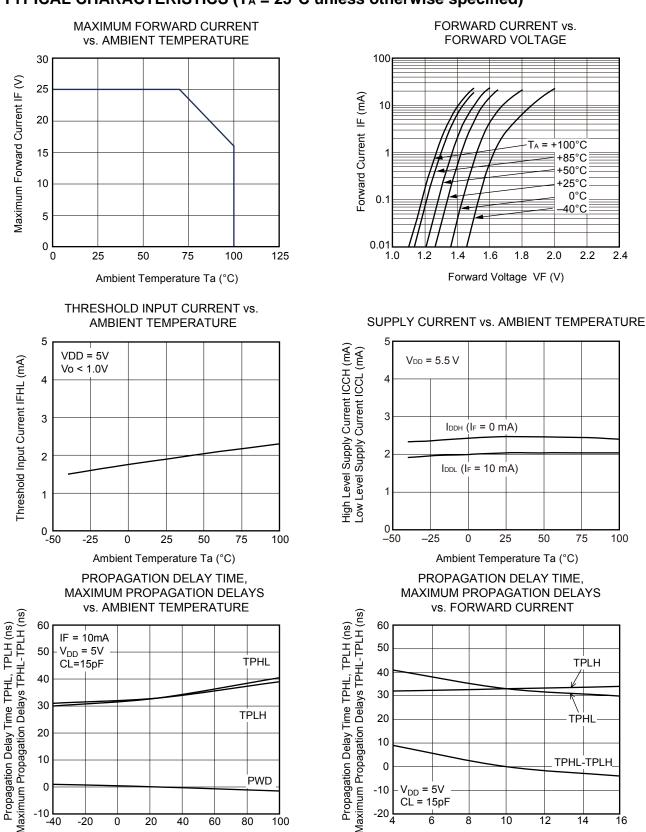
### 4 Test circuit for common mode transient immunity





 $\textbf{Remark} \ \ \textbf{C} \textbf{$\text{L}$ includes probe and stray wiring capacitance}.$ 

## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise specified)



-20

<u>-</u>40

40

Ambient Temperature Ta (°C)

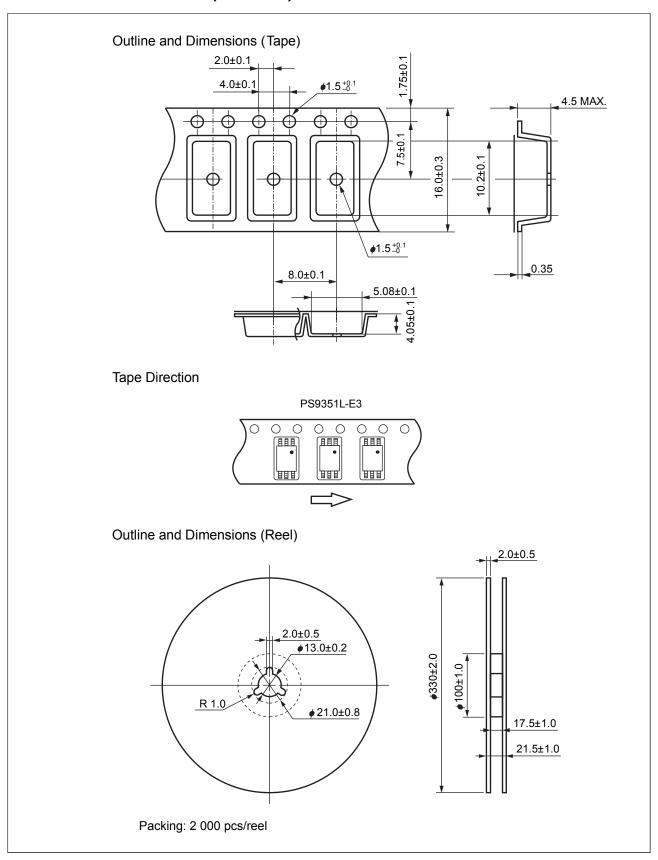
80

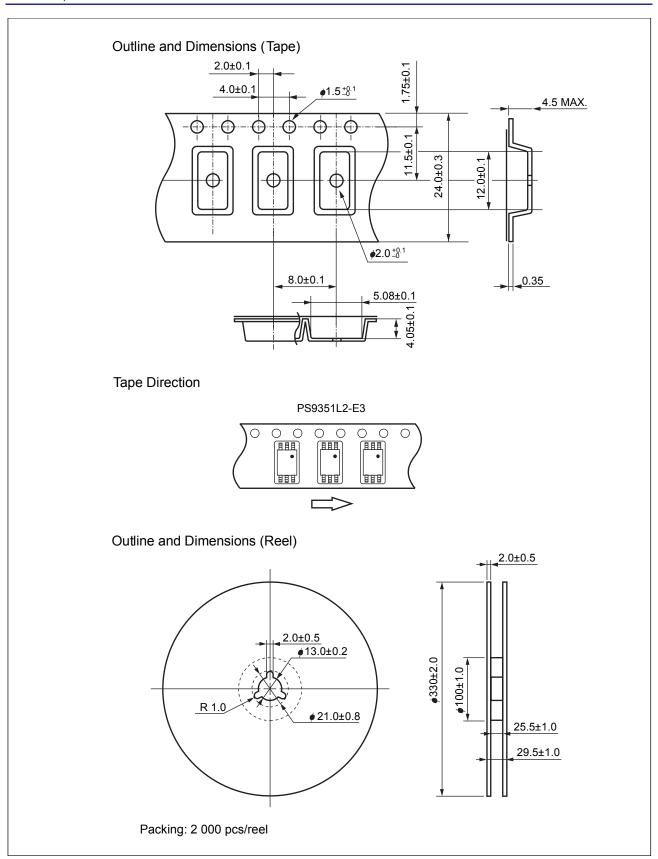
100

10

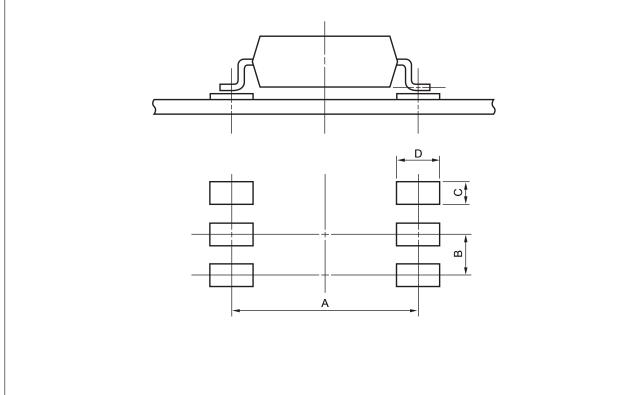
Forward Current IF (mA)

## **TAPING SPECIFICATIONS (UNIT:mm)**





## RECOMMENDED MOUNT PAD DIMENSIONS (UNIT:mm)



Part Number	Lead Bending	Α	В	С	D
PS9351L	lead bending type (Gull-wing) for surface mount	9.2	1.27	0.8	2.2
PS9351L2	lead bending type (Gull-wing) for long creepage distance (surface mount)	10.2	1.27	0.8	2.2

#### **NOTES ON HANDLING**

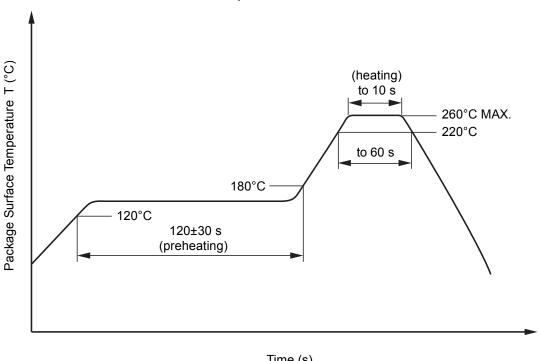
- 1. Recommended soldering conditions
  - (1) Infrared reflow soldering
    - Peak reflow temperature 260°C or below (package surface temperature)
    - Time of peak reflow temperature 10 seconds or less
    - Time of temperature higher than 220°C 60 seconds or less  $120 \pm 30 \text{ s}$
    - Time to preheat temperature from 120 to 180°C
    - Number of reflows
    - Flux

Three Rosin flux containing small amount of chlorine (The flux

with a maximum chlorine content of 0.2 Wt% is

recommended.)

## Recommended Temperature Profile of Infrared Reflow



Time (s)

#### (2) Wave soldering

260°C or below (molten solder temperature) Temperature

Time 10 seconds or less

Preheating conditions 120°C or below (package surface temperature)

Number of times One (Allowed to be dipped in solder including plastic mold portion.)

Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

## (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below

Time (each pins) 3 seconds or less

Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

- (4) Cautions
  - Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.
- 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

### **USAGE CAUTIONS**

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1  $\mu$ F is used between  $V_{DD}$  and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pin 2 (which is an NC\*1 pin) can either be connected directly to the GND pin on the LED side or left open. Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
  - \*1 NC: Not connected (No connection)
- 4. Avoid storage at a high temperature and high humidity.

## SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/100/21	
Dielectric strength			
maximum operating isolation voltage	$U_IORM$	1 130	$V_{peak}$
Test voltage (partial discharge test, procedure a for type test and random test)	$U_pr$	1 808	$V_{peak}$
$U_{pr} = 1.6 \times U_{IORM.}, P_d < 5 pC$			
Test voltage (partial discharge test, procedure b for all devices)	$U_pr$	2 119	$V_{peak}$
$U_{pr} = 1.875 \times U_{IORM.}, P_d < 5 pC$			
Highest permissible overvoltage	$U_TR$	8 000	$V_{peak}$
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	$T_{stg}$	-55 to +125	°C
Operating temperature range	T <sub>A</sub>	-40 to +100	°C
Isolation resistance, minimum value			
$V_{IO}$ = 500 V dc at $T_A$ = 25°C	Ris MIN.	10 <sup>12</sup>	Ω
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> MAX. at least 100°C	Ris MIN.	10 <sup>11</sup>	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I <sub>F</sub> , Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
$V_{IO}$ = 500 V dc at $T_A$ = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

#### Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or i any way allow it to enter the mouth.

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California Eastern Laboratories, Inc. 4590 Patrick Henry Drive, Santa Clara, California 95054-1817, U.S.A. Tel: +1-408-919-2500, Fax: +1-408-988-0279

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China
Tel: +86-10-2335-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd. Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333 Tel: +86-21-2226-088, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16IF., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2265-6688, Fax: 4852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Tai aipei 10543, Taiwan

Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyllux Innovation Centre, Singapore 339949 Tel: +55-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Tel: +60-3-7955-9390, Fax: +60-3-7955-9510 pr Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HALII Stage, Indiranagar, Bangalore, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

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