

# PS2561D-1, PS2561DL-1, PS2561DL2-1

DIP PHOTOCOUPLER OPERATING AMBIENT TEMPERATURE 110 °C

R08DS0181EJ0101 Rev.1.01 Feb 21, 2022

### DESCRIPTION

The PS2561D-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon phototransistor.

The PS2561D-1 is in a plastic DIP (Dual In-line Package) and the PS2561DL-1 is lead bending type (Gullwing) for surface mount.

The PS2561DL1-1 is lead bending type for long creepage distance.

The PS2561DL2-1 is lead bending type for long creepage distance (Gull-wing) for surface mount.

### **FEATURES**

- Operating ambient temperature: 110 °C
- High isolation voltage (BV = 5 000 Vr.m.s.)
- High collector to emitter voltage (V<sub>CEO</sub> = 80 V)
- High current transfer ratio (CTR = 160 % TYP.)
- High-speed switching (t<sub>f</sub> = 3 µs TYP., t<sub>f</sub> = 5 µs TYP.)
- Ordering number of taping product: PS2561DL-1-F3 : 2 000 pcs/reel

: PS2561DL2-1-F3 : 2 000 pcs/reel

- Pb-Free product
- Safety standards
  - UL approved: UL1577, Double protection
  - CSA approved: CAN/CSA-C22.2 No. 62368-1, Reinforced insulation
  - BSI approved: BS EN 62368-1, Reinforced insulation
  - SEMKO approved: EN 62368-1, IEC 62368-1, Reinforced insulation
  - NEMKO approved: EN 62368-1, Reinforced insulation
  - FIMKO approved: EN 62368-1, Reinforced insulation
  - DEMKO approved: EN 62368-1, Reinforced insulation
  - CQC approved: GB8898, GB4943.1, Reinforced insulation
  - VDE approved: DIN EN 60747-5-5 (Option)

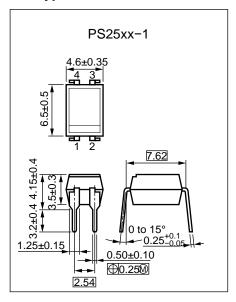
# PIN CONNECTION (Top View) 4 3 1. Anode 2. Cathode 3. Emitter 4. Collector

### **APPLICATIONS**

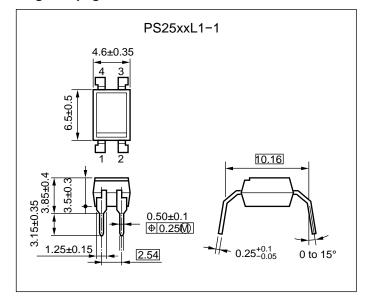
- · Power supply
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controllers

# PACKAGE DIMENSIONS (UNIT: mm)

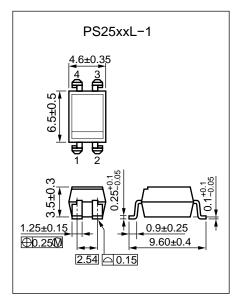
# **DIP Type**



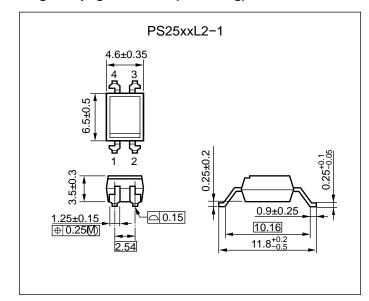
# **Long Creepage Distance**



# **Lead Bending Type**



# **Long Creepage Distance (Gull-Wing)**

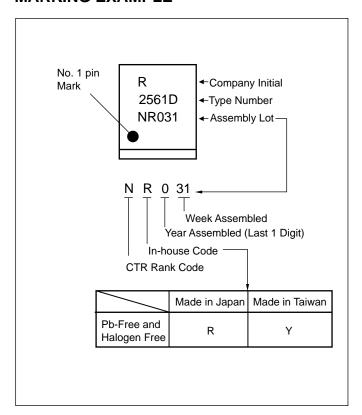


Weight (4-pin DIP) : 0.26 g (typ.)

# PHOTOCOUPLER CONSTRUCTION

Parameter	PS2561D-1, PS2561DL-1	PS2561DL1-1, PS2561DL2-1
Air Distance (MIN.)	7 mm	8 mm
Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

# **MARKING EXAMPLE**



# **ORDERING INFORMATION**

Part Number	Order Number *1	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number *2
PS2561D-1	PS2561D-1Y-A	Pb-Free and	Magazine case 100 pcs	Standard products	PS2561D-1
PS2561DL-1	PS2561DL-1Y-A	Halogen Free		(UL, CSA, BSI,	PS2561DL-1
PS2561DL1-1	PS2561DL1-1Y-A			FIMKO, DEMKO,	PS2561DL1-1
PS2561DL2-1	PS2561DL2-1Y-A				PS2561DL2-1
PS2561DL-1-F3	PS2561DL-1Y-F3-A		Embossed Tape 2 000 pcs/reel	ogo appioroa)	PS2561DL-1
PS2561DL2-1-F3	PS2561DL2-1Y-F3-A		Embossed Tape 2 000 pcs/reel		PS2561DL2-1
PS2561D-1-V	PS2561D-1Y-V-A		Magazine case 100 pcs	UL, CSA, BSI,	PS2561D-1
PS2561DL-1-V	PS2561DL-1Y-V-A			SEMKO, NEMKO,	PS2561DL-1
PS2561DL1-1-V	PS2561DL1-1Y-V-A			FIMKO, DEMKO, CQC,	PS2561DL1-1
PS2561DL2-1-V	PS2561DL2-1Y-V-A			DIN EN 60747-5-5 approved	PS2561DL2-1
PS2561DL-1-V-F3	PS2561DL-1Y-V-F3-A		Embossed Tape 2 000 pcs/reel		PS2561DL-1
PS2561DL2-1-V-F3	PS2561DL2-1Y-V-F3-A		Embossed Tape 2 000 pcs/reel		PS2561DL2-1

Notes: \*1. If requested by Order Number in the table, all CTR rank (CTR = 50-400 % (@  $I_F$  = 5 mA,  $V_{CE}$  = 5 V) & CTR = 10 % and larger (@  $I_F$  = 1 mA,  $V_{CE}$  = 5 V)) products will be shipped.

When specifying CTR rank, please add "/CTR rank" after Order Number.

ex. L rank: PS2561D-1Y-A/L

Notes: \*2. For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise specified)

	Parameter		Ratings	Unit
Diode	Reverse Voltage	$V_R$	6	V
	Forward Current (DC)	l <sub>F</sub>	40	mA
	Power Dissipation Derating	ΔP <sub>D</sub> /°C	1.5	mW/°C
	Power Dissipation	P <sub>D</sub>	150	mW
	Peak Forward Current <sup>*1</sup>	I <sub>FP</sub>	1	Α
Transistor	Collector to Emitter Voltage	Vceo	80	V
	Emitter to Collector Voltage	V <sub>ECO</sub>	7	V
Collector Current		lc	50	mA
	Power Dissipation Derating		1.5	mW/°C
	Power Dissipation	Pc	150	mW
Isolation Voltage*2		BV	5 000	Vr.m.s.
Operating	Operating Ambient Temperature		-55 to +110	°C
Storage Temperature		T <sub>stg</sub>	-55 to +150	°C

Note: \*1. PW = 100  $\mu$ s, Duty Cycle = 1 %

<sup>\*2.</sup> AC voltage for 1 minute at  $T_A$  = 25 °C, RH = 60 % between input and output. Pins 1-2 shorted together, 3-4 shorted together.

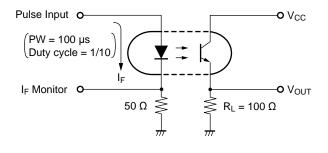
# **ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)**

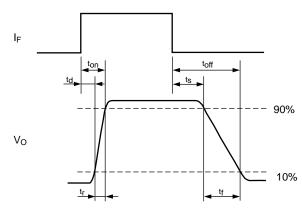
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA		1.2	1.4	V
	Reverse Current	lR	V <sub>R</sub> = 5 V			5	μΑ
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		10		pF
Transistor	Collector to Emitter Dark Current	Iceo	Vce = 48 V, IF = 0 mA			100	nA
Coupled	Current Transfer Ratio	CTR	IF = 5 mA, VcE = 5 V	50	160	400	%
	(Ic/I <sub>F</sub> )*1		IF = 1 mA, VcE = 5 V	10	80		
	Collector Saturation Voltage	VCE (sat)	IF = 10 mA, Ic = 2 mA			0.3	V
	Isolation Resistance	Rı-o	VI-O = 1.0 kVDC	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.5		pF
	Rise Time*2	tr	Vcc = 10 V, Ic = 2 mA,		3		μs
	Fall Time*2	tf	RL = 100 Ω		5		

Note: \*1. CTR rank

CTR Rank	CTR (%)	Conditions	
Н	80 to 160	IF = 5 mA, VCE = 5 V	
П	16 and larger	IF = 1 mA, VcE = 5 V	
O	100 to 200	IF = 5 mA, VcE = 5 V	
Q	20 and larger	IF = 1 mA, VcE = 5 V	
١٨/	130 to 260	IF = 5 mA, VcE = 5 V	
W	26 and larger	IF = 1 mA, VcE = 5 V	
	200 to 400	IF = 5 mA, VCE = 5 V	
L	40 and larger	IF = 1 mA, VCE = 5 V	
N	50 to 400	IF = 5 mA, VcE = 5 V	
	10 and larger	IF = 1 mA, VcE = 5 V	

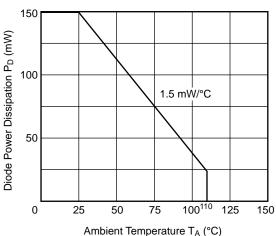
# \*2. Test Circuit for Switching Time



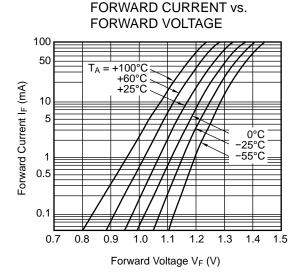


# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise specified)

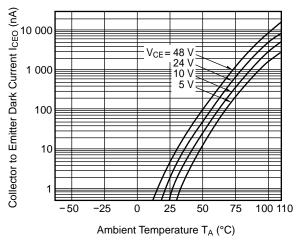
# DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



# Ambient Temperature T<sub>A</sub> (°C)

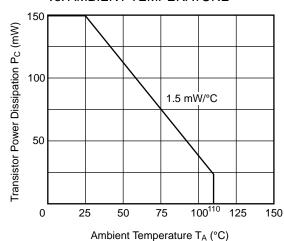


# COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE

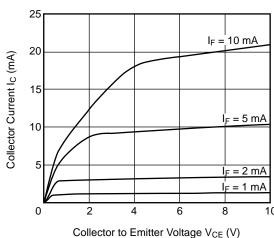


### Remark The graphs indicate nominal characteristics.

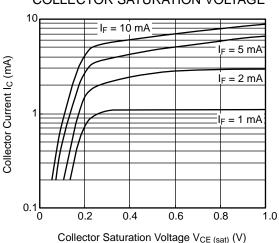
# TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



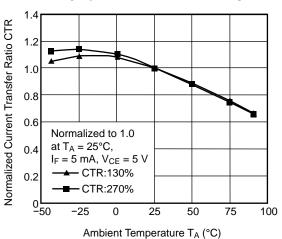
# COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



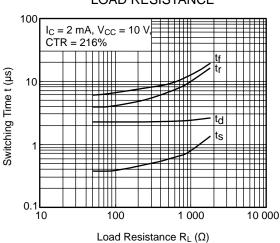
# COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



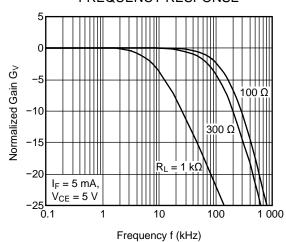
# NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



# SWITCHING TIME vs. LOAD RESISTANCE

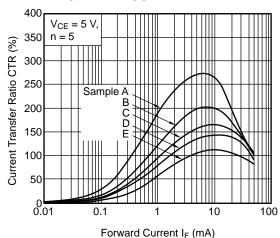


# FREQUENCY RESPONSE

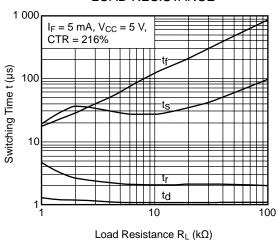


**Remark** The graphs indicate nominal characteristics.

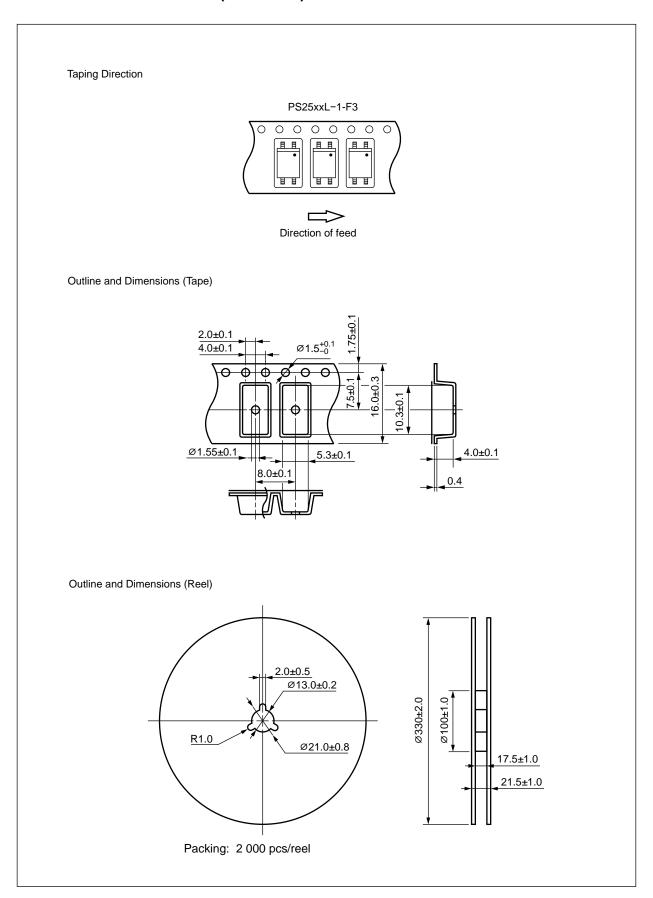
# CURRENT TRANSFER RATIO vs. FORWARD CURRENT



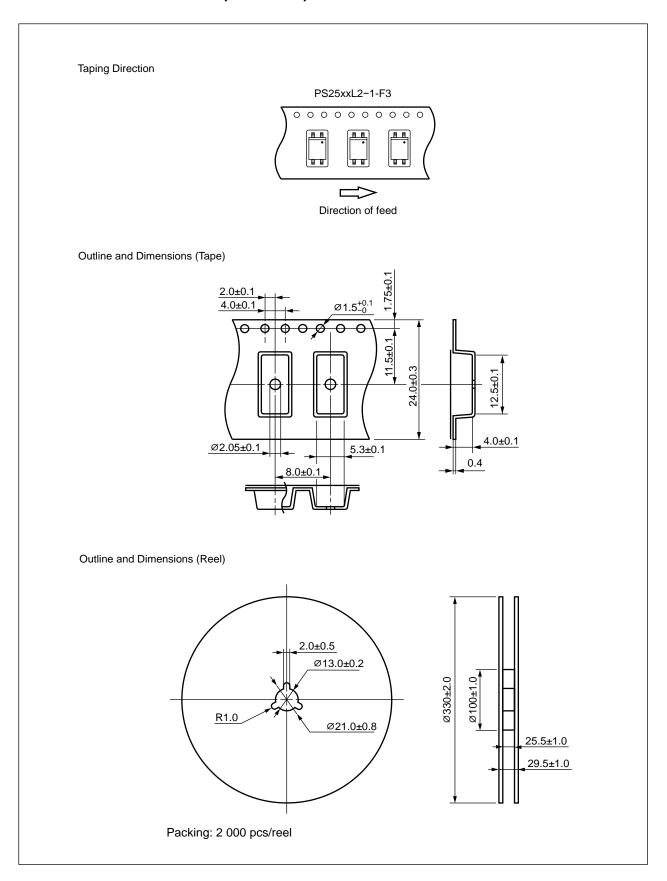
### SWITCHING TIME vs. LOAD RESISTANCE



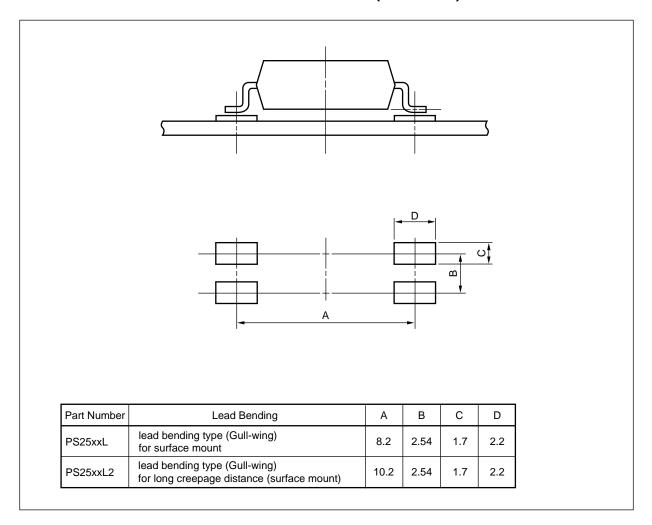
# **TAPING SPECIFICATIONS (UNIT: mm)**



# **TAPING SPECIFICATIONS (UNIT: mm)**



# RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



**Remark** All dimensions in this figure must be evaluated before use.

### **NOTES ON HANDLING**

- 1. Recommended soldering conditions
  - (1) Infrared reflow soldering

 Peak reflow temperature 260 °C or below (package surface temperature)

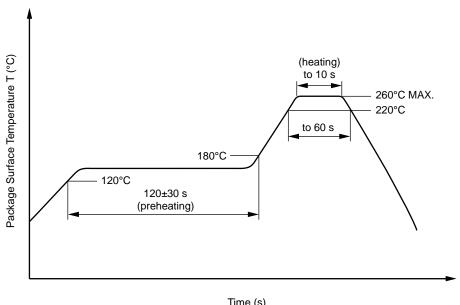
• Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220 °C 60 seconds or less

• Time to preheat temperature from 120 to 180 °C  $\,$  120  $\pm$  30 s Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of

0.2 Wt% is recommended.)

# Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

 Temperature 260 °C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120 °C or below (package surface temperature)

 Number of times One (Allowed to be dipped in solder including plastic mold portion.) Rosin flux containing small amount of chlorine (The flux with a maximum • Flux

chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

 Peak Temperature (lead part temperature) 350 °C or below 3 seconds or less Time (each pins)

• Flux Rosin flux containing small amount of chlorine

(The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 100 °C
- (4) Cautions
  - Flux Cleaning

Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.

• Do not use fixing agents or coatings containing halogen-based substances.

- 2. Cautions regarding noise
  - Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.
- Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

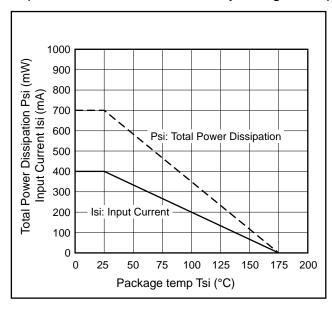
# **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.
- 3. Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
- 4. Do not use fixing agents or coatings containing halogen-based substances.

# SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (1/2) (PS2561D-1, PS2561DL-1)

Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/110/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.6 \times U_{\text{IORM}},  P_{\text{d}} < 5  \text{pC}$	U <sub>IORM</sub> U <sub>pr</sub>	890 1 424	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}$ , $P_d < 5 \ pC$	$U_pr$	1 669	$V_{peak}$
Highest permissible overvoltage	U <sub>ІОТМ</sub>	8 000	$V_{\text{peak}}$
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	T <sub>stg</sub>	-55 to +150	°C
Operating temperature range	T <sub>A</sub>	-55 to +110	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V}$ dc at $T_A = 25 ^{\circ}\text{C}$ $V_{IO} = 500 \text{ V}$ dc at $T_A$ MAX. at least 100 $^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature	Tsi	175	°C
Current (input current I <sub>F</sub> , Psi = 0) Power (output or total power dissipation) Isolation resistance	Isi Psi	400 700	mA mW
$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 <sup>9</sup>	Ω

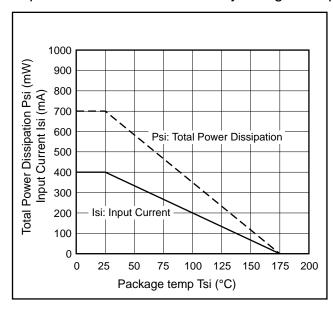
# Dependence of maximum safety ratings with package temperature



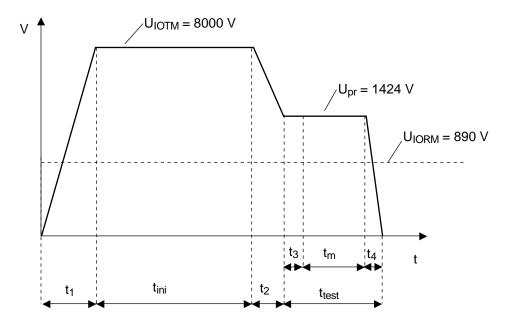
# SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (2/2) (PS2561DL1-1, PS2561DL2-1)

Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/110/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.6 \times U_{IORM},  P_d < 5 \; pC$	U <sub>IORM</sub> U <sub>pr</sub>	1 130 1 808	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}, \ P_d < 5 \ pC$	U <sub>pr</sub>	2 119	$V_{peak}$
Highest permissible overvoltage	U <sub>ІОТМ</sub>	8 000	V <sub>peak</sub>
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	T <sub>stg</sub>	-55 to +150	°C
Operating temperature range	T <sub>A</sub>	-55 to +110	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V}$ dc at $T_A = 25 ^{\circ}\text{C}$ $V_{IO} = 500 \text{ V}$ dc at $T_A$ MAX. at least 100 $^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I <sub>F</sub> , Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 <sup>9</sup>	Ω

# Dependence of maximum safety ratings with package temperature



# Method a) Destructive Test, Type and Sample Test (PS2561D-1, PS2561DL-1)

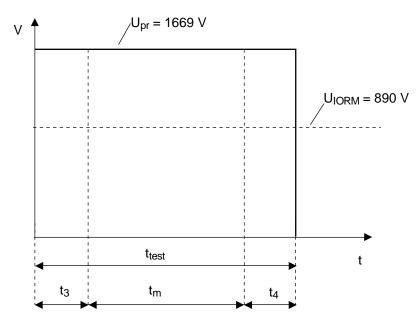


 $t_1$ ,  $t_2 = 1$  to 10 sec

 $t_3, t_4 = 1 sec$ 

 $t_{m(PARTIAL\ DISCHARGE)} = 10\ sec$   $t_{test} = 12\ sec$   $t_{ini} = 60\ sec$ 

# Method b) Non-destructive Test, 100 % Production Test (PS2561D-1, PS2561DL-1)

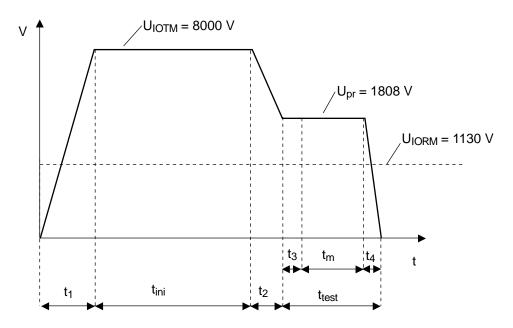


 $t_3$ ,  $t_4 = 0.1 \text{ sec}$ 

 $t_{m(PARTIAL\ DISCHARGE)} = 1.0\ sec$ 

 $t_{test} = 1.2 \text{ sec}$ 

Method a) Destructive Test, Type and Sample Test (PS2561DL1-1, PS2561DL2-1)



 $t_1$ ,  $t_2 = 1$  to 10 sec

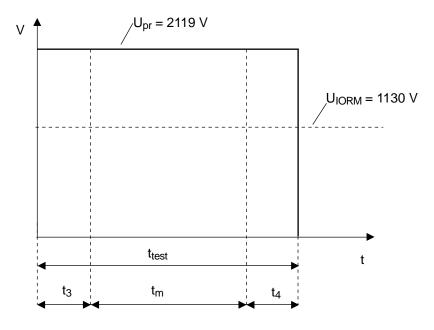
 $t_3$ ,  $t_4 = 1$  sec

 $t_{\text{m(PARTIAL DISCHARGE)}} = 10 \text{ sec}$ 

 $t_{test} = 12 \text{ sec}$ 

 $t_{ini} = 60 \text{ sec}$ 

Method b) Non-destructive Test, 100% Production Test (PS2561DL1-1, PS2561DL2-1)



 $t_3$ ,  $t_4 = 0.1 \text{ sec}$ 

 $t_{m(PARTIAL\ DISCHARGE)} = 1.0\ sec$ 

 $t_{test} = 1.2 \text{ sec}$ 

### Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or i any way allow it to enter the mouth.

All trademarks and registered trademarks are the property of their respective owners.

### **Notice**

- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products
  and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your
  product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use
  of these circuits, software, or information.
- 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others
- 4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
- 5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- 6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
  - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
  - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

- 7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
- 8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
- 9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- 10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
- 12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
- 13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
- 14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
- (Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.5.0-1 October 2020)

# Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

www.renesas.com

### **Trademarks**

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

### **Contact information**

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit: <a href="https://www.renesas.com/contact/">www.renesas.com/contact/</a>.