

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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SILICON POWER TRANSISTOR NTA1010

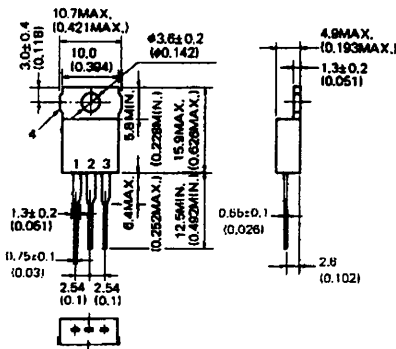
HIGH SPEED HIGH CURRENT SWITCHING PNP SILICON EPITAXIAL TRANSISTOR

Industrial Use

DESCRIPTION

Suitable for switching regulator, DC-DC converter and ultrasonic appliance applications.

PACKAGE DIMENSIONS in millimeters (inches)



1. Base (B)
2. Collector (C)
3. Emitter (E)
4. Pin (Collector)
JEDEC: TO-220AB

FEATURES

- High speed switching.
- Low collector saturation voltage.
- Specified of reverse biased SOA with inductive loads.

ABSOLUTE MAXIMUM RATINGS

Maximum Voltages and Currents (Ta=25°C)

Collector to Emitter Voltage	V _{CEX}	-100	V
Collector to Emitter Sustaining Voltage	V _{CEO(SUS)}	-100	V
Collector to Emitter Sustaining Voltage	V _{CEX(SUS)}	-100	V
Emitter to Base Voltage	V _{EBO}	-7.0	V
Continuous Collector Current	I _{C(DC)}	-7.0	A
Peak Collector Current	I _{C(pulse)*}	-15	A
Continuous Base Current	I _{B(DC)}	-3.0	A

Maximum Power Dissipations

Total Power Dissipation	P _{T(Tc=25°C)}	65	W
Total Power Dissipation	P _{T(Tc=100°C)}	26	W
Total Power Dissipation	P _{T(Ta=25°C)}	2.0	W

Maximum Temperatures

Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Lead Temperature	T _L	260	°C
1/8 inch from case for 10 seconds			

Thermal Resistances

Junction to Case	R _{th(j-c)}	1.92	°C/W
Junction to Ambient	R _{th(j-a)}	62.5	°C/W

* Pulsed PW ≤ 300 μs, duty cycle ≤ 10%

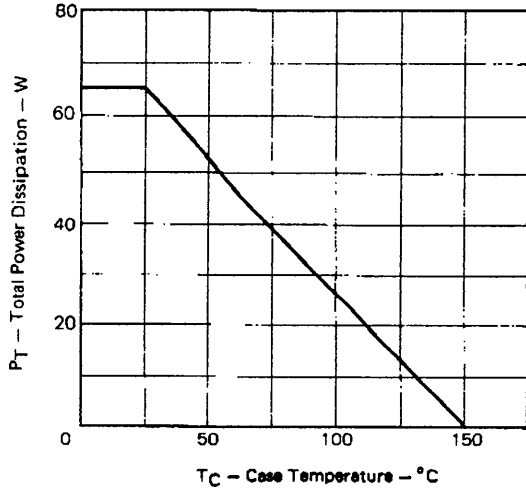
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise noted.)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector to Emitter Sustaining Voltage	VCEO(SUS)	-100			V	IC=-5.0A, IB=-0.5A, L=100μH
	VCEX(SUS)1	-100			V	IC=-5.0A, IB1=-IB2=-0.5A, VBE(OFF)=5V, L=180μH, Ta=125°C *1
	VCEX(SUS)2	-100			V	IC=-10A, IB1=-1.0A, IB2=0.5A, VBE(OFF)=5V, L=180μH, *2 Ta=125°C
Collector Cutoff Current	ICEX1			-10	μA	VCE=-100V, VBE(OFF)=1.5V
	ICEX2			-1.0	mA	VCE=-100V, VBE(OFF)=1.5V, Ta=125°C
	ICER			-1.0	mA	VCE=-100V, RBE=100Ω, Ta=125°C
Emitter Cutoff Current	IEBO			-10	μA	VEB=-5.0V, IC=0
Second Breakdown Collector Current	IS/B	-2.0			A	VCE=-20V, t=1sec Tc=25°C
Second Breakdown Energy	ES/B	125			μJ	IC=-5A, IB1=-0.5A, VBE(OFF)=5V, RBB=50Ω, L=10μH
DC Current Gain	hFE1	40				VCE=-5.0V, IC=-3.0A, *3
	hFE2	20				VCE=-5.0V, IC=-5.0A *3
Collector Saturation Voltage	VCE(sat)			-0.6	V	IC=-5.0A, IB=-0.5A *3
Base Saturation Voltage	VBE(sat)			-1.5	V	
Gain Bandwidth Product	fT	20			MHz	VCE=-10V, IC=-0.2A, f=3MHz
Output Capacitance	Cob			250	pF	VCB=-10V, IE=0, f=1MHz
Turn On Time	ton			0.5	μs	IC=-5.0A, IB1=-IB2=-0.5A VBE(OFF)=5V, RL=10Ω, Vcc≈-50V
Storage Time	tstg			1.5	μs	
Fall Time	tf			0.3	μs	

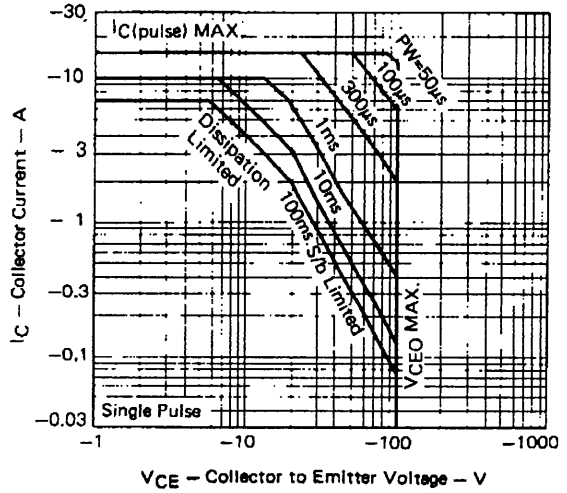
- *1 VCE clamped Vclamp = -100V
- *2 VCE clamped Vclamp = -100V
- *3 Pulsed PW ≤ 350μs, duty cycle ≤ 2%

TYPICAL CHARACTERISTICS (Ta=25°C)

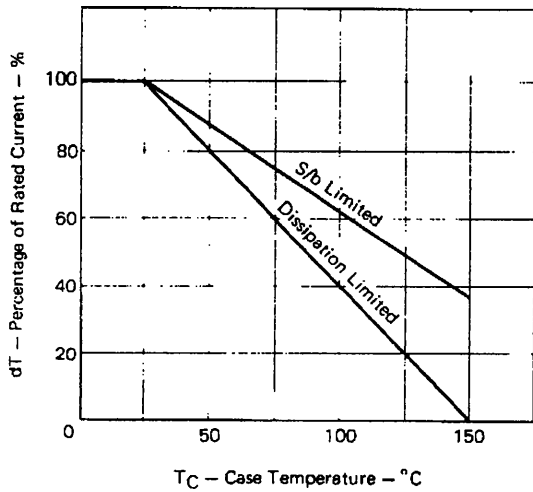
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



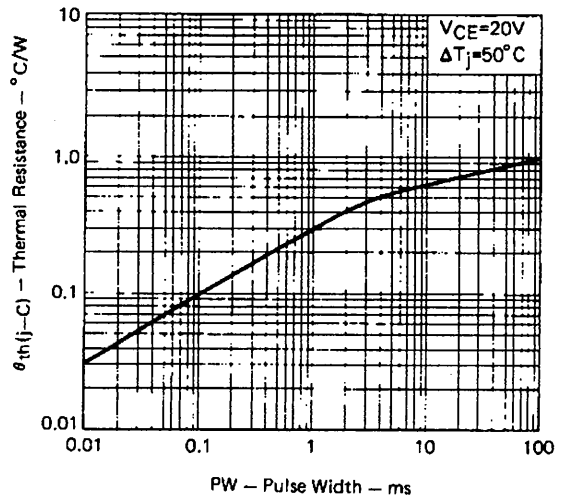
SAFE OPERATING AREA



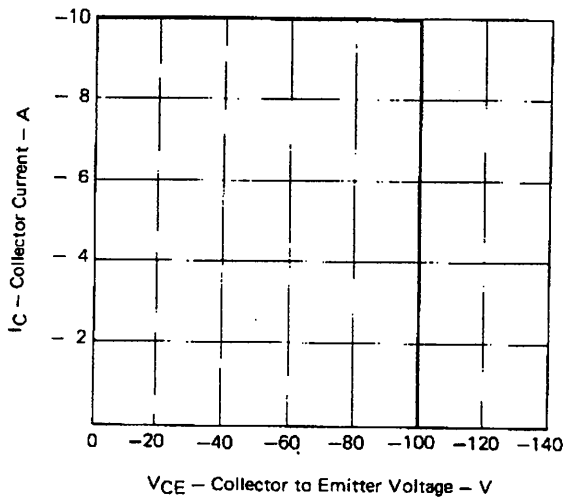
DERATING CURVE OF SAFE OPERATING AREAS



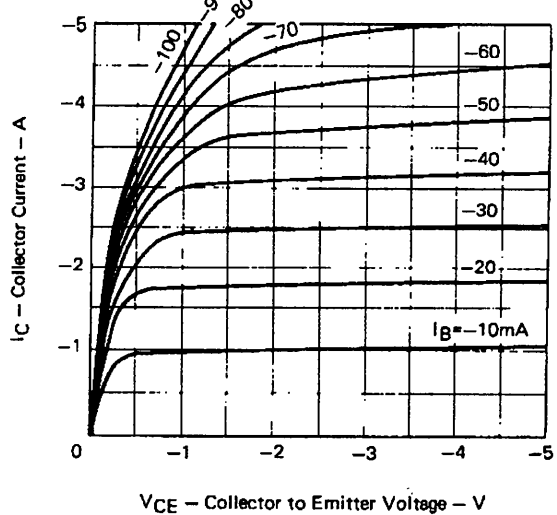
TRANSIENT THERMAL RESISTANCE



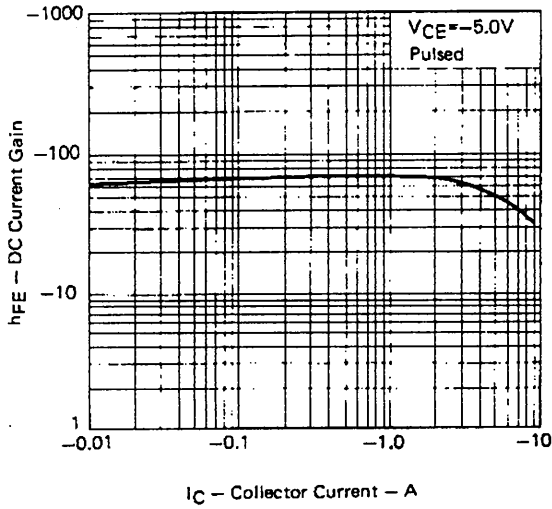
REVERSE BIAS SAFE OPERATING AREAS



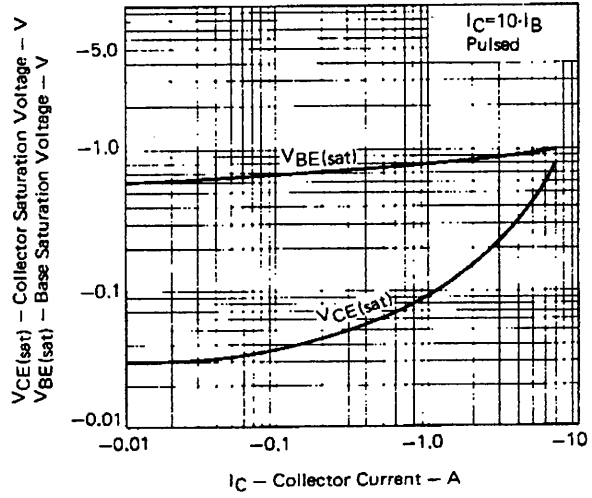
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



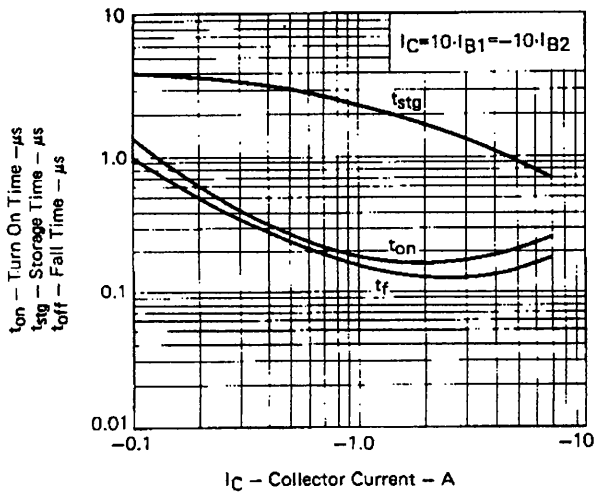
DC CURRENT GAIN vs. COLLECTOR CURRENT



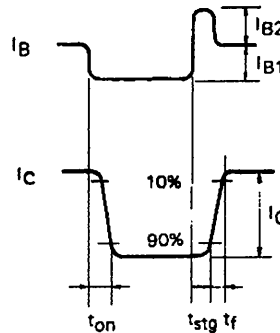
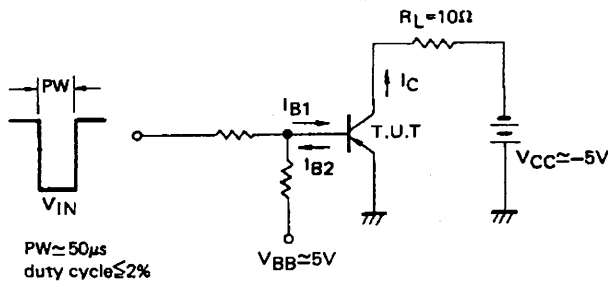
BASE AND COLLECTOR SATURATION VOLTAGE vs. COLLECTOR CURRENT



TURN ON TIME, STORAGE AND FALL TIME vs. COLLECTOR CURRENT



SWITCHING TIME (t_{on} , t_{stg} , t_f) TEST CIRCUIT



Nippon Electric Co., Ltd.

NEC Building, 33-1, Shiba Gochome, Minato-ku, Tokyo 108, Japan
Tel.: Tokyo 454-1111
Telex Address: NECTOK A J22686
Cable Address: MICROPHONE TOKYO

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