

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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GaAs MULTI-CHIP MODULE  
**MC-7833**

**870 MHz CATV 25 dB PUSH-PULL AMPLIFIER**

**DESCRIPTION**

The MC-7833 is a GaAs Multi-chip Module designed for use in CATV applications up to 870 MHz. This unit has low distortion, low noise figure and return loss across the entire frequency band.

Reliability and performance uniformity are assured by our stringent quality and control procedures.

**FEATURES**

- Low distortion
- High linear gain  $G_L = 25.0 \text{ dB MIN. @ } f = 870 \text{ MHz}$
- Low return loss

**ORDERING INFORMATION**

Part Number	Order Number	Package	Supplying Form
MC-7833	MC-7833-AZ	7-pin special with heatsink (Pb-Free)	25 pcs MAX./Tray

**Remark** To order evaluation samples, contact your nearby sales office.

Part number for sample order: MC-7833

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C)**

Parameter	Symbol	Ratings	Unit
Supply Voltage	V <sub>DD</sub>	30	V
Input Voltage <sup>Note</sup>	V <sub>i</sub>	65.0	dBmV
Operating Case Temperature	T <sub>c</sub>	-30 to +100	°C
Storage Temperature	T <sub>stg</sub>	-40 to +100	°C

**Note** In case of single tone

**Caution** Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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**RECOMMENDED OPERATING CONDITIONS ( $Z_s = Z_L = 75 \Omega$ )**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	$V_{DD}$		23.5	24.0	24.5	V
Input Voltage	$V_i$	110 channel, Flat	–	19.0	25.5	dBmV
Operating Case Temperature	$T_C$		–30	+25	+85	°C

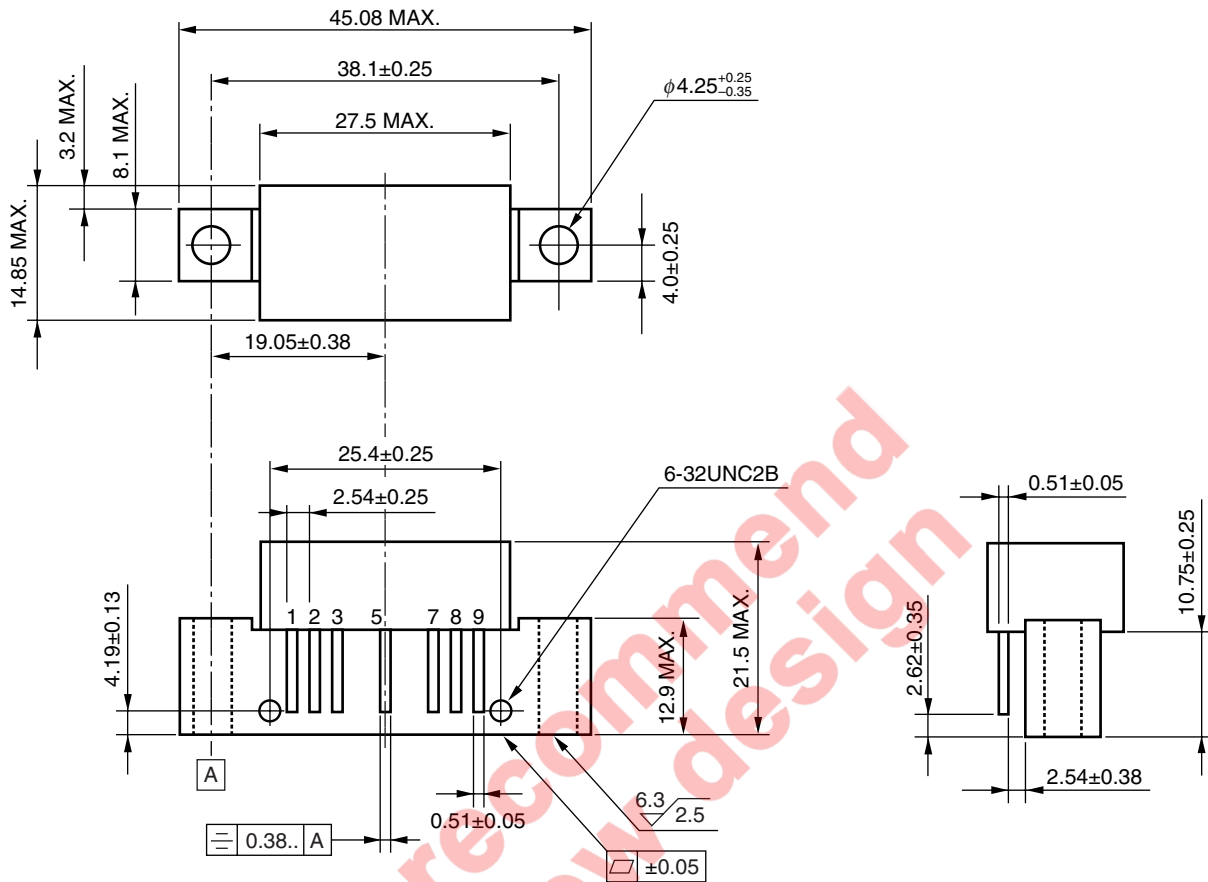
**ELECTRICAL CHARACTERISTICS ( $T_C = 30 \pm 5^\circ\text{C}$ ,  $V_{DD} = 24 \text{ V}$ ,  $Z_s = Z_L = 75 \Omega$ )**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Linear Gain	$G_L$	$f = 870 \text{ MHz}$	25.0	–	26.0	dB
Gain Slope	$G_{\text{slope}}$	$f = 40 \text{ to } 870 \text{ MHz}$	1.0	–	1.8	dB
Gain Flatness	$G_{\text{flatness}}$	$f = 40 \text{ to } 870 \text{ MHz}$ , Peak to valley	–	–	0.7	dB
Noise Figure 1	NF1	$f = 50 \text{ MHz}$	–	–	5.5	dB
Noise Figure 2	NF2	$f = 870 \text{ MHz}$	–	–	6.0	dB
Operating Current	$I_{DD}$	$P_{in} = \text{None}$	180	–	240	mA
Composite Triple Beat	CTB	110 channel,	–	–	–57	dBc
Cross Modulation	XM	$V_o = 44 \text{ dBmV}$ , Flat	–	–	–50	dBc
Composite 2nd Order Beat	CSO		–	–	–57	dBc
Input / Output Return Loss 1	RL1	$f = 40 \text{ to } 160 \text{ MHz}$	20	–	–	dB
Input / Output Return Loss 2	RL2	$f = 160 \text{ to } 320 \text{ MHz}$	19	–	–	dB
Input / Output Return Loss 3	RL3	$f = 320 \text{ to } 640 \text{ MHz}$	17.5	–	–	dB
Input / Output Return Loss 4	RL4	$f = 640 \text{ to } 870 \text{ MHz}$	16	–	–	dB

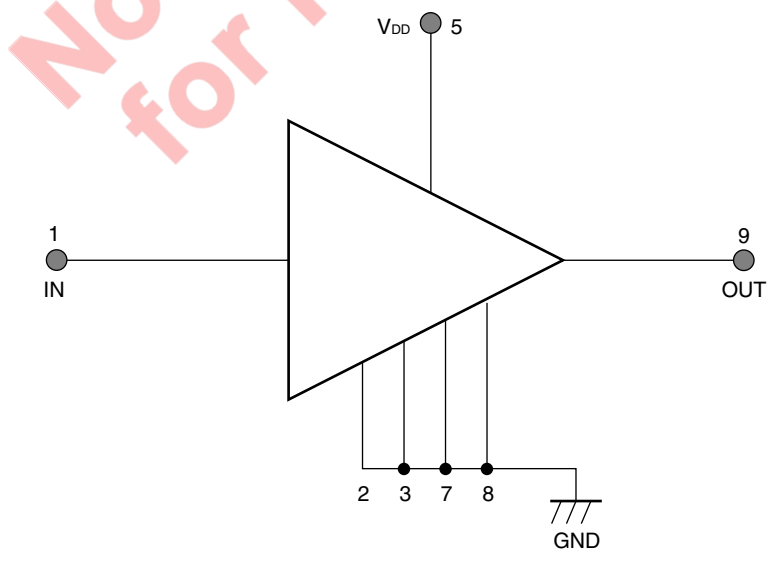
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PACKAGE DIMENSIONS

7-PIN SPECIAL WITH HEATSINK (UNIT: mm)



PIN CONNECTION



**NOTES ON CORRECT USE**

- (1) The space between PC board and root of the lead should be kept more than 1 mm to prevent undesired stress to the lead and also should be kept less than 4 mm to prevent undesired parasitic inductance.  
Recommended that space is 2.0 to 3.0 mm typical.
- (2) Recommended torque strength of the screw is 59 to 78 Ncm.
- (3) Form the ground pattern as wide as possible to minimize ground impedance.  
(to prevent undesired oscillation)  
All the ground pins must be connected together with wide ground pattern to decrease impedance difference.

**RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Partial Heating	Peak temperature (pin temperature) : 350°C or below <sup>Note</sup> Soldering time (per pin of device) : 3 seconds or less	—

**Note** The point of pin part heating must be kept more than 1.2 mm distance from the root of lead.

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