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# M62782GP

# Voltage Detecting, System Resetting IC Series

REJ03D0524-0100 Rev.1.00 May 27, 2005

#### **Description**

The M62782GP is a voltage threshold detector designed for detection of a supply voltage and generation of a system reset pulse for almost all logic circuits such as microprocessor.

It also has extensive applications including battery checking, level detecting, and waveform shaping circuits.

#### **Features**

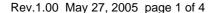
- Few external parts
- Low threshold operating voltage (Supply voltage to keep low-state at low supply voltage) 0.65V (Typ.) at  $R_L=22k\Omega$
- Wide supply voltage range 1.5V to 7.0V
- Wide application range
- Extra small 5-pin package (5-pin SOP) SOT-25
- Built-in long delay time 100ms

#### **Application**

- Reset pulse generation for almost all logic circuits
- Battery checking, level detecting, waveform shaping circuits
- Delayed waveform generator
- · Switching circuit to a back-up power supply
- DC/DC converter
- Over voltage protection circuit

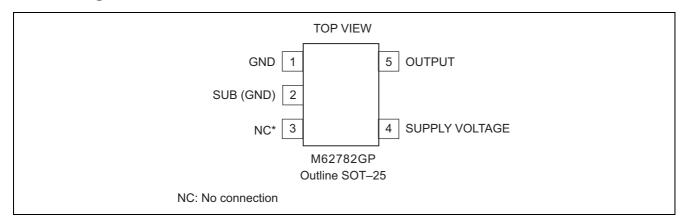
#### **Recommended Operating Condition**

• Supply voltage range 1.5V to 7.0V

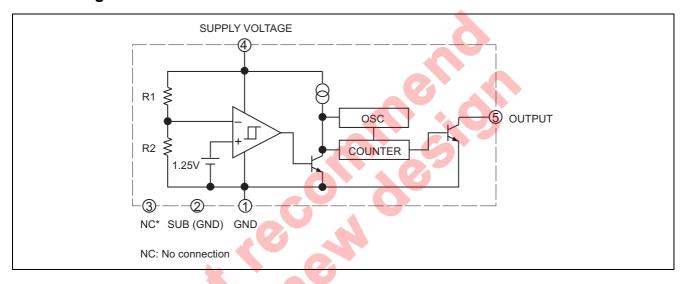




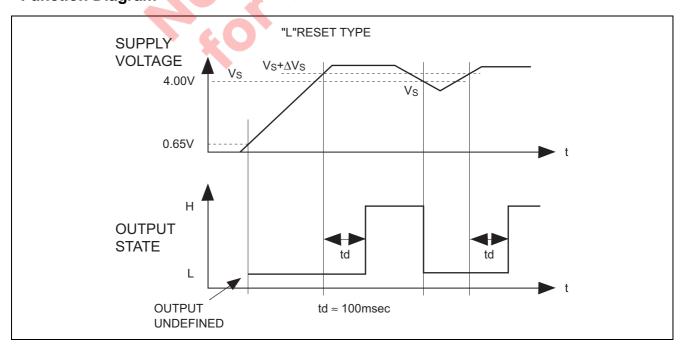
#### **Pin Arrangement**



## **Block Diagram**



#### **Function Diagram**



#### **Absolute Maximum Ratings**

(Ta = 25°C, unless otherwise noted)

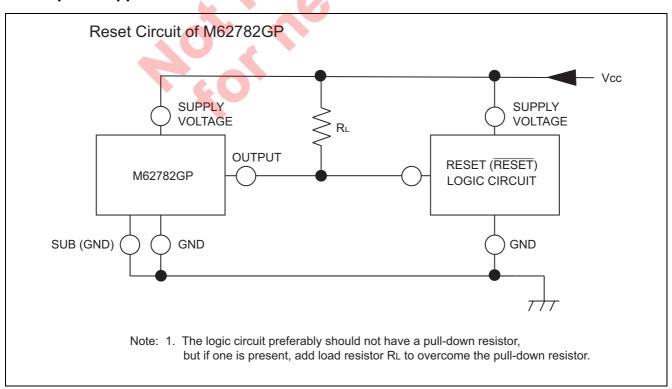
Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage	V <sub>CC</sub>	7	V	
Output sink current	I <sub>sink</sub>	6	mA	
Output voltage	Vo	7	V	Output with open collector
Power dissipation	Pd	200	mW	5pin SOP (SOT-25)
Thermal derating	$K_{\scriptscriptstyle{\theta}}$	2	mW/°C	Ta ≥ 25°C
Operating temperature	T <sub>opr</sub>	-30 to +85	°C	
Storage temperature	T <sub>stg</sub>	-40 to +125	°C	

#### **Electrical Characteristics**

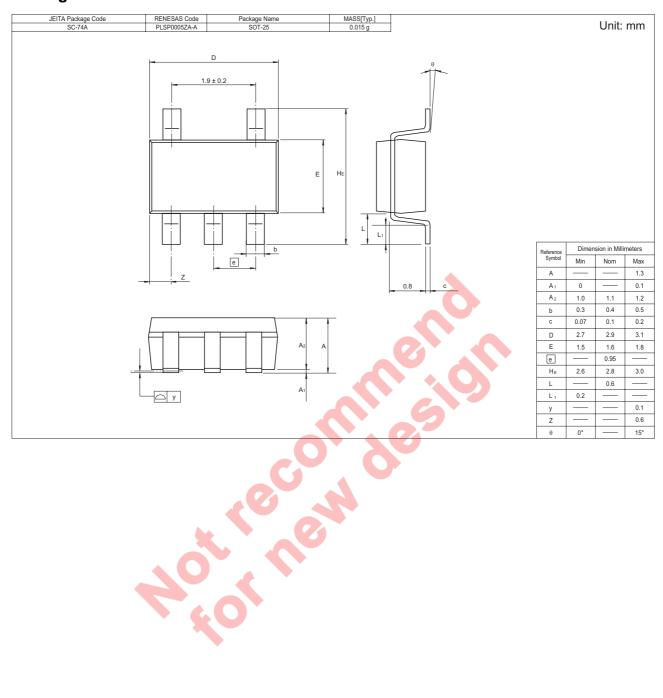
(Ta = 25°C, unless otherwise noted)

Item	Symbol	Min	Тур	Max	Unit	Test condition	
Detecting voltage	Vs	3.84	4.00	4.16	V		
Hysteresis voltage	$\Delta V_S$	50	80	110	mV		
Detecting voltage temperature coefficient	V <sub>S</sub> / $\Delta$ T	1	0.01	_	%/°C	<b>S</b>	
Circuit current	I <sub>CC</sub>	_	400	600	μΑ	$V_{CC} = 5.0V$	
Output saturation voltage	Vsat	_	0.2	0.4	V	$V_{CC}=3.5V$ , $I_{sink}=4mA$ ,	
Threshold	$V_{OPL}$	_	0.7	8.0	V	Minimum	R <sub>L</sub> =2.2kΩ, Vsat≤0.4V
operating voltage		_	0.6	0.7	5	supply voltage for operation	R <sub>L</sub> =100kΩ, Vsat≤0.4V
Output leak current	I <sub>OH</sub>	_		30	nA		
		_		1	μΑ	Ta = −30 to +85°C	
delay time	tpd	60	100	140	ms		

## **Example of Application Circuit**



#### **Package Dimensions**



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- (ii) use of nontrammaple material of (iii) prevention against any maintention or misnap.

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Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

**Renesas Technology Taiwan Co., Ltd.** 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001