To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HZN6.8Z4MFA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0205-0100Z
Rev.1.00
Mar.29.2004

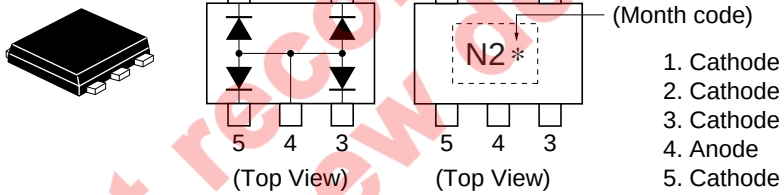
Features

- HZN6.8Z4MFA has four devices in a monolithic, and can absorb surge.
- Low capacitance (C = 4.0 pF Typ / 4.5 pF max) and can protect ESD of signal line.
- VSON-5T Package is suitable for high density surface mounting.

Ordering Information

Type No.	Laser Mark	Package Code
HZN6.8Z4MFA	N2	VSON-5T

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics *[1]

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V_Z	6.47	—	7.00	V	I_Z = 5 mA, 40 ms pulse
Reverse current	I_R	—	—	2	μA	V_R = 3.5 V
Capacitance	C	—	4.0	4.5	pF	V_R = 0 V, f = 1 MHz
Dynamic resistance	r_d	—	—	30	Ω	I_Z = 5 mA
ESD-Capability *[2]	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.
2. Failure criterion ; I_R > 2 μA at V_R = 3.5 V.
3. Between cathode and anode.

Month Code

Month of Manufacture	Assemble JAPAN	Assemble MALAYSIA	Month of Manufacture	Assemble JAPAN	Assemble MALAYSIA
January	A	1	July	G	7
February	B	2	August	H	8
March	C	3	September	J	9
April	D	4	October	K	W
May	E	5	November	L	X
June	F	6	December	M	Y

Main Characteristic

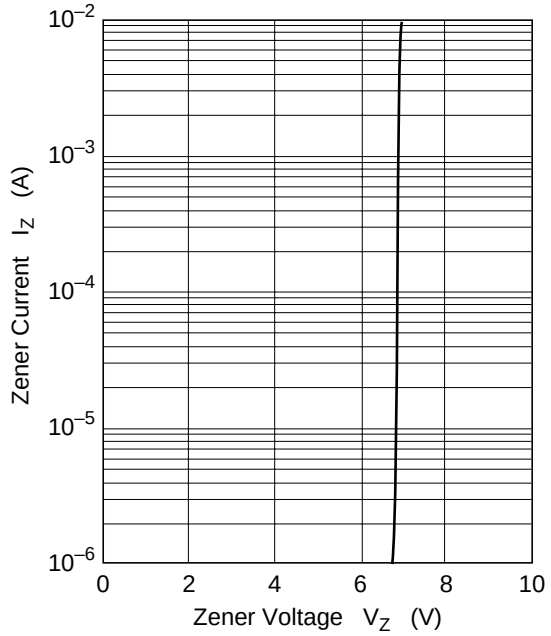


Fig.1 Zener current vs. Zener voltage

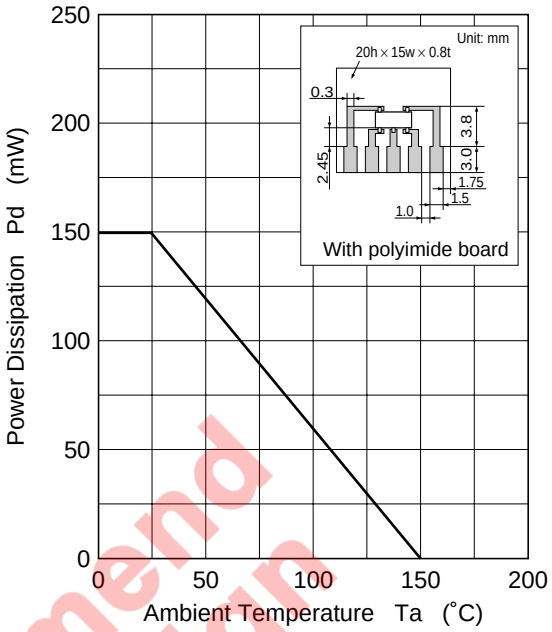


Fig.2 Power Dissipation vs. Ambient Temperature

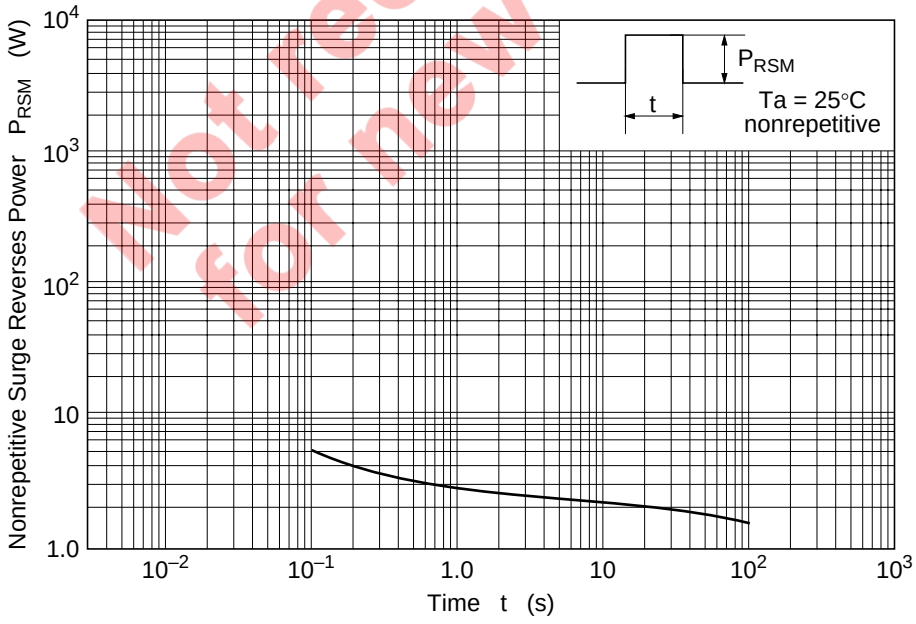
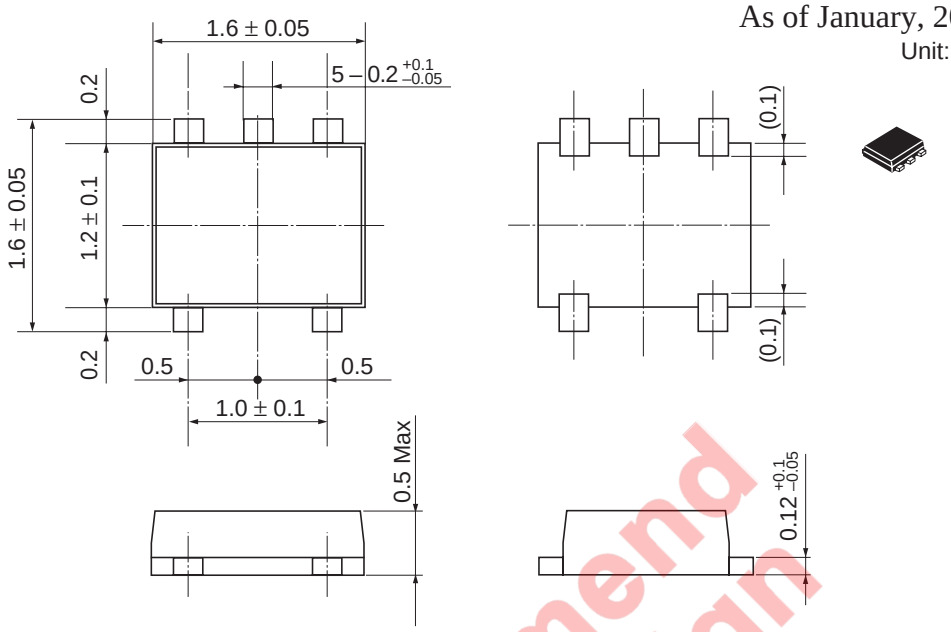


Fig.3 Surge Reverse Power Ratings

Package Dimensions

As of January, 2003

Unit: mm



Package Code	VSON-5T
JEDEC	—
JEITA	—
Mass (reference value)	0.002 g

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