

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

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HZM4.3FA

Silicon Epitaxial Planar Zener Diode for Surge Absorb

RENESAS

ADE-208-468B (Z)

Rev.2
Nov. 2002

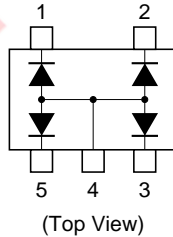
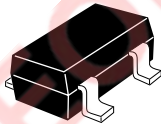
Features

- HZM4.3FA has four devices, and can absorb surge.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM4.3FA	43A	MPAK-5

Pin Arrangement



1. Cathode
2. Cathode
3. Cathode
4. Anode
5. Cathode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics*¹

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _z	4.01	—	4.48	V	I _z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	10	μA	V _R = 1 V
Capacitance	C	—	—	150	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	130	Ω	I _z = 5 mA
ESD-Capability * ²	—	30	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; I_R > 10 μA at V_R = 1 V.

Main Characteristic

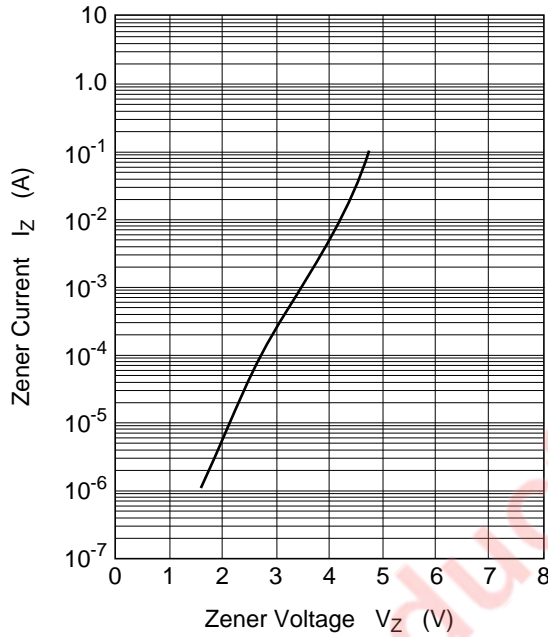


Fig.1 Zener current vs. Zener voltage

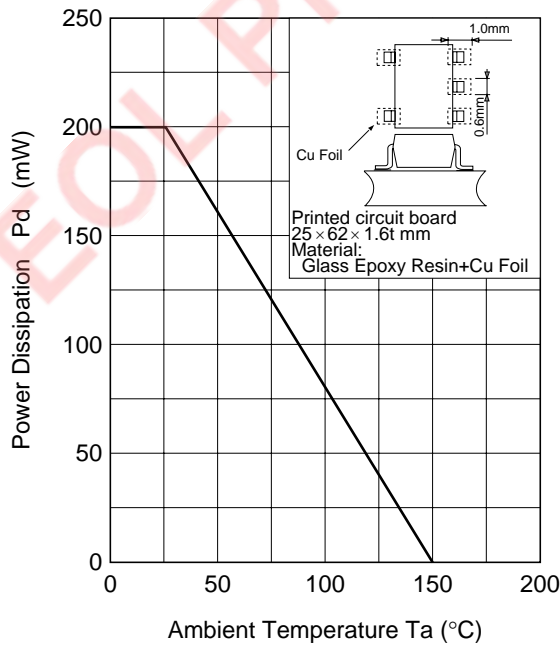


Fig.2 Power Dissipation vs. Ambient Temperature

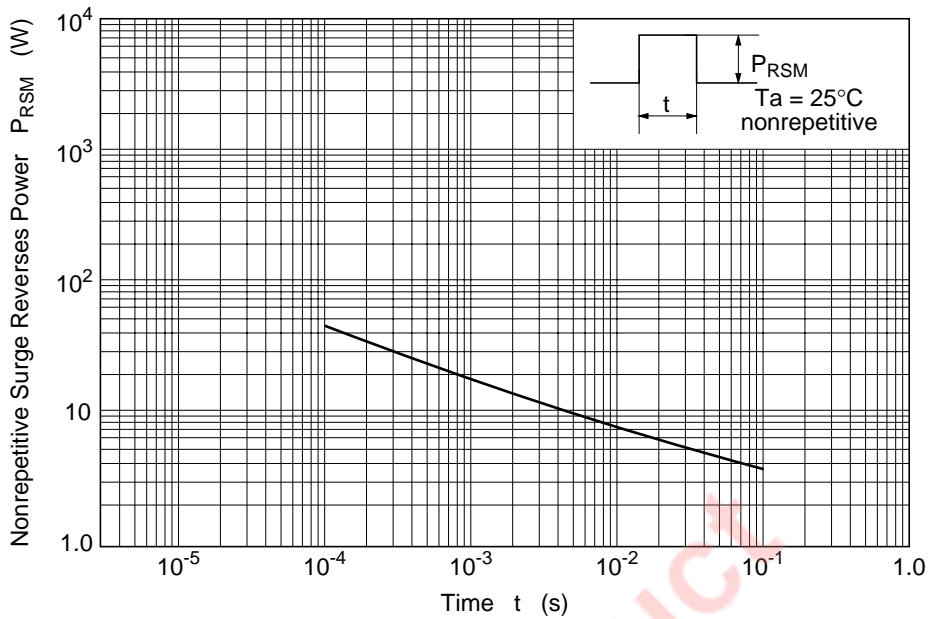


Fig.3 Surge Reverse Power Ratings

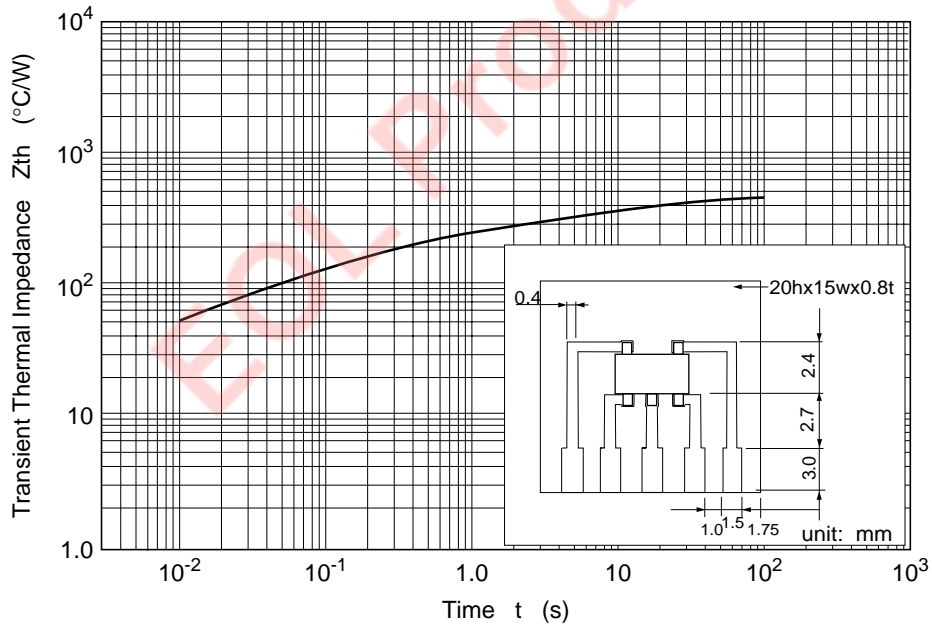
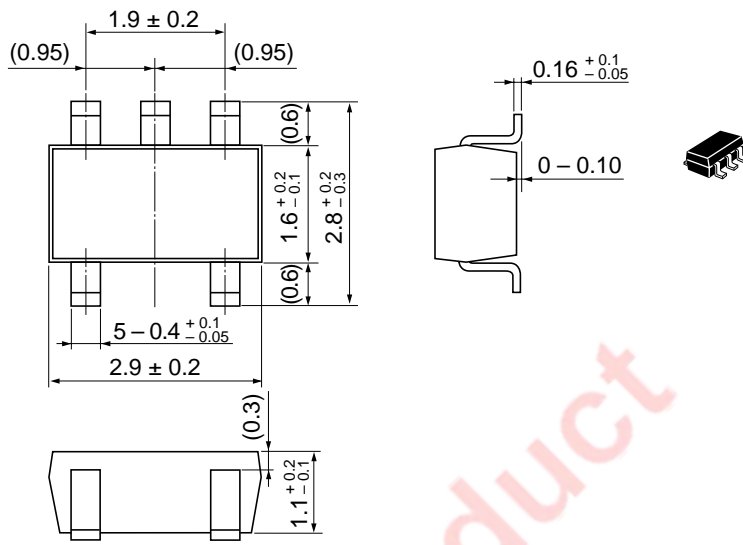


Fig.4 Transient Thermal Impedance

Package Dimensions

As of July, 2001
Unit: mm



Hitachi Code	MPAK-5
JEDEC	—
JEITA	—
Mass (reference value)	0.013 g

EOL Product

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