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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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HVR100

Variable Capacitance Diode for AM tuner

REJ03G0102-0700
 Rev.7.00
 Feb 21, 2005

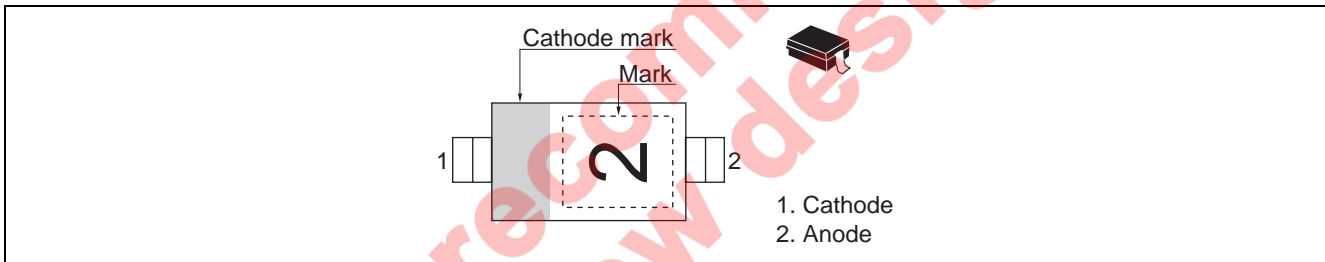
Features

- High capacitance ratio. (n = 16.0 min)
- High figure of merit. (Q = 200 min)
- To be usable at low voltage.
- Small Resin Package (SRP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Renesas Code	Previous Code
HVR100	2	PLSP0002ZA-A	SRP

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	-55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V _R	15	—	—	V	I _R = 10 μA
Reverse current	I _R	—	—	100	nA	V _R = 9 V
Capacitance	C ₁	421.5	—	524.6	pF	V _R = 1 V, f = 1 MHz
	C ₃	182.0	—	275.7		V _R = 3 V, f = 1 MHz
	C ₅	73.2	—	121.4		V _R = 5 V, f = 1 MHz
	C ₆	42.2	—	72.2		V _R = 6 V, f = 1 MHz
	C ₇	26.2	—	41.6		V _R = 7 V, f = 1 MHz
	C ₈	20.4	—	28.2		V _R = 8 V, f = 1 MHz
Capacitance ratio	n	16	—	—	—	C ₁ / C ₈
Figure of merit	Q	200	—	—	—	C = 450 pF, f = 1 MHz
Matching error	ΔC/C * ¹	—	—	3.0	%	V _R = 1 to 8 V, f = 1 MHz
ESD-Capability * ²	—	80	—	—	V	C = 200 pF, Both forward and reverse direction 1 pulse.

Note: 1. Each group shall uniform a multiple of 3 diodes.
 A set of HVR100 is of uniform C-V characteristics.
 Measure max. value and min. value of capacitance at each bias point of V_R = 1V through 8 V.
 Calculate Matching Error,

$$\Delta C/C = \frac{(C_{\max} - C_{\min})}{C_{\min}} \times 100 (\%)$$

2. Failure criterion ; I_R < 100 nA at V_R = 9 V

Main Characteristic

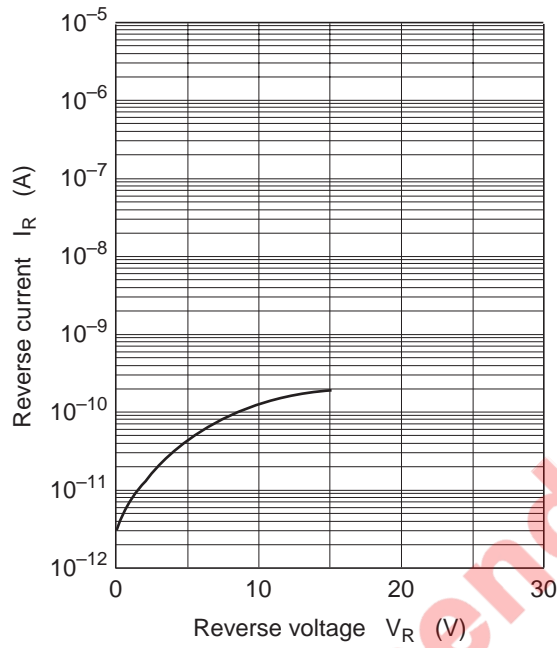


Fig.1 Reverse current vs. Reverse voltage

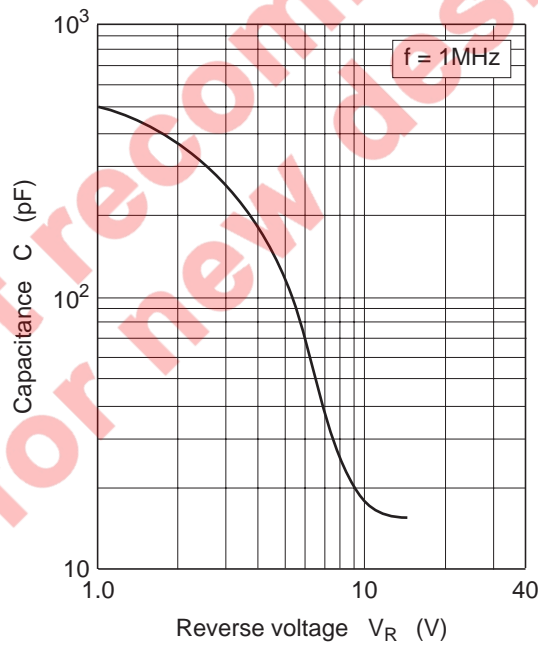
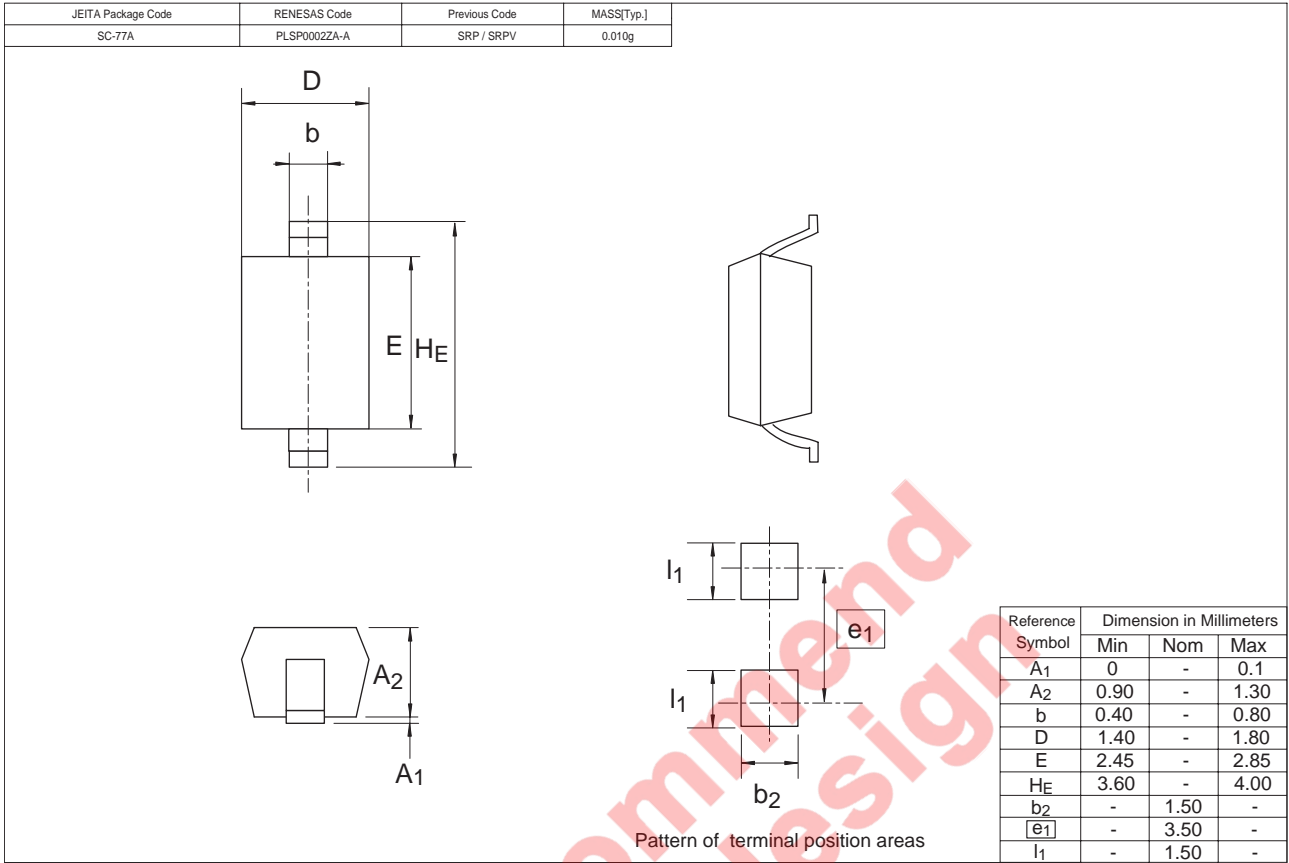


Fig.2 Capacitance vs. Reverse voltage

Package Dimensions



Not recommended for new design

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