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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

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HVL388CM

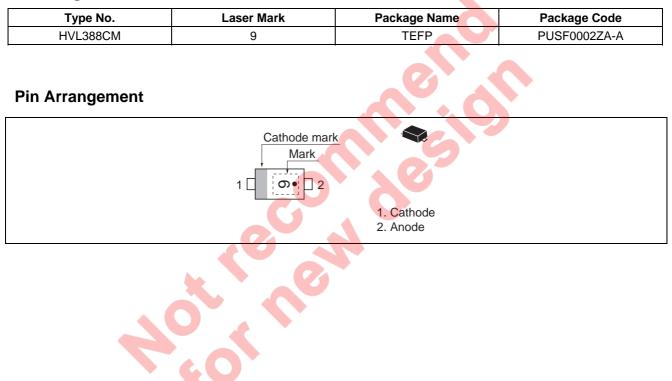
Variable Capacitance Diode for VCO

REJ03G0229-0200 Rev.2.00 Mar 15, 2006

Features

- High capacitance ratio. (n = 1.880 min)
- Low series resistance. (rs = 0.75Ω max)
- Thin Extremely small Flat Lead Package (TEFP) is suitable for surface mount design.

Ordering Information





Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	۵°

Electrical Characteristics

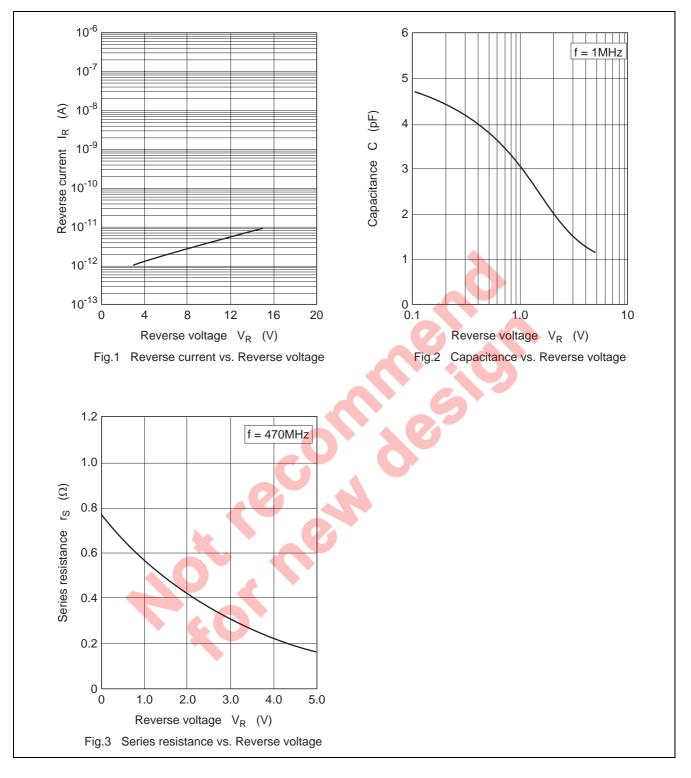
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	—	10	nA	V _R = 15 V
	I _{R2}	_	—	50		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	3.162	—	3.465	pF	$V_{R} = 1 V, f = 1 MHz$
	C ₃	1.570	—	1.720		V _R = 3 V, f = 1 MHz
Capacitance ratio	n	1.880	—	2.150		C ₁ / C ₃
Series resistance	r _s	_	—	0.75	Ω	V _R = 1 V, f = 470 MHz

Note: For TEFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

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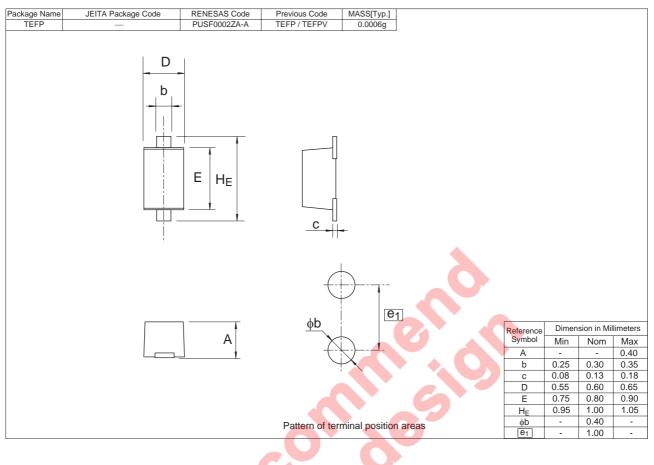


Main Characteristic





Package Dimensions





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