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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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HVD369B

Variable Capacitance Diode for VCO

REJ03G0502-0200
Rev.2.00
Jan 31, 2006

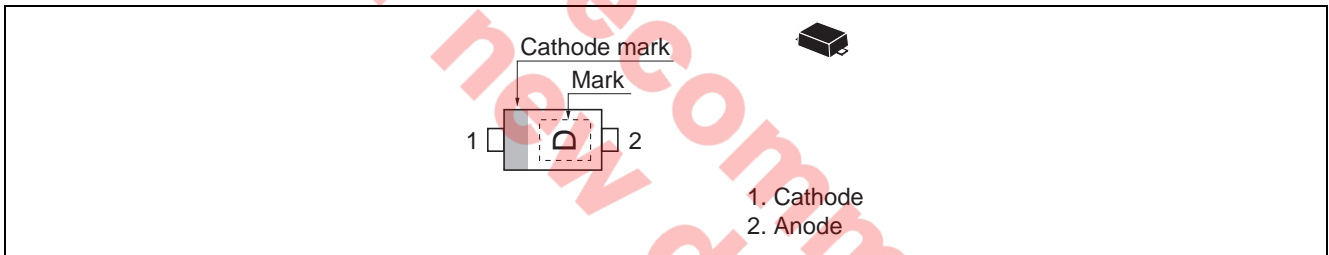
Features

- Low capacitance and to be usable at GHz.
- High capacitance ratio. ($n = 2.3$ min)
- Low series resistance. ($r_s = 0.5 \Omega$ max)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HVD369B	D	SFP	PUSF0002ZB-A

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	15	V
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	$V_R = 15\text{ V}$
	I_{R2}	—	—	100		$V_R = 15\text{ V}, T_a = 60^\circ\text{C}$
Capacitance	C_1	4.65	—	5.15	pF	$V_R = 1\text{ V}, f = 1\text{ MHz}$
	C_4	1.85	—	2.15		$V_R = 4\text{ V}, f = 1\text{ MHz}$
Capacitance ratio	n	2.30	—	—	—	C_1 / C_4
Series resistance	r_s	—	—	0.50	Ω	$V_R = 1\text{ V}, f = 470\text{ MHz}$

Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristics

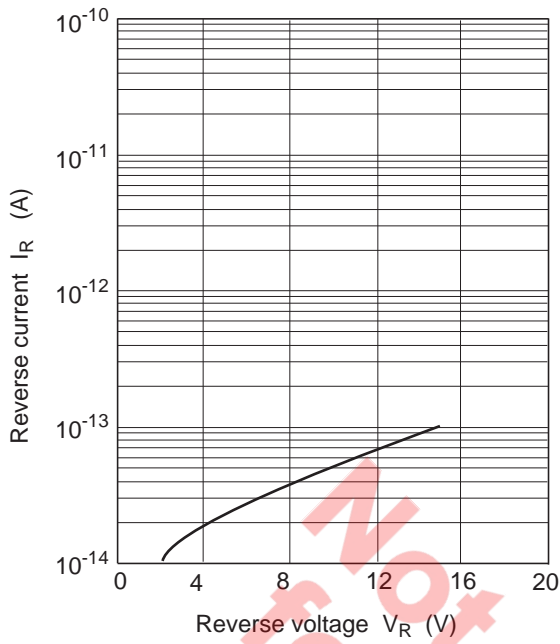


Fig.1 Reverse current vs. Reverse voltage

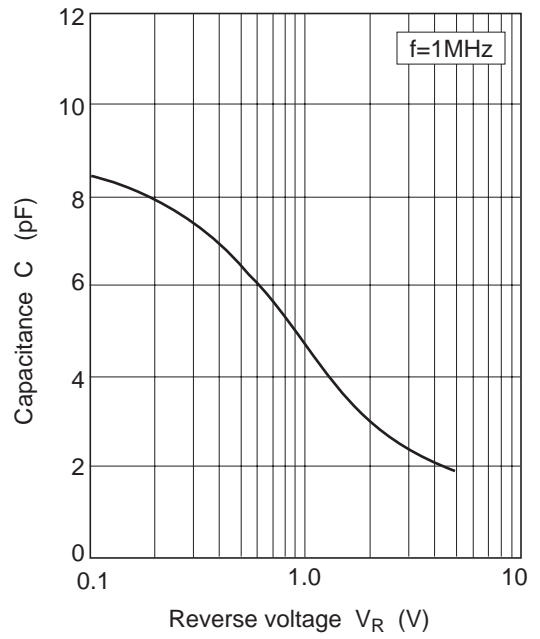


Fig.2 Capacitance vs. Reverse voltage

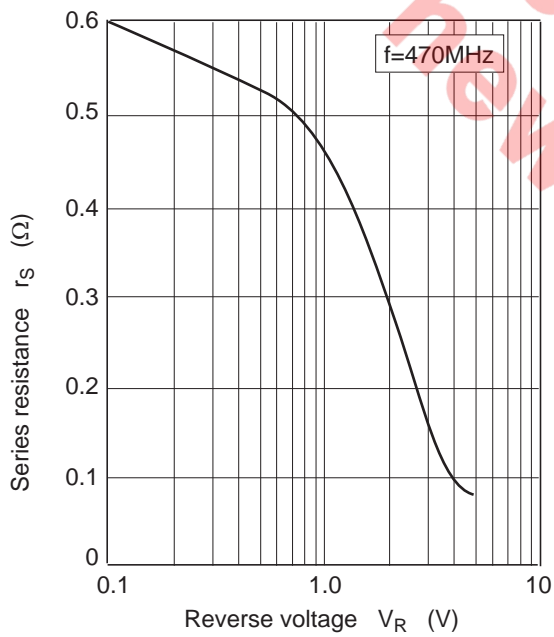
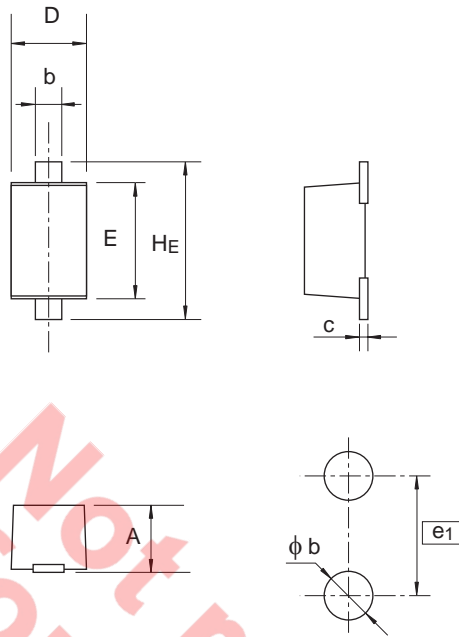


Fig.3 Series resistance vs. Reverse voltage

Package Dimensions

Package Name SFP	JEITA Package Code —	RENESAS Code PUSF0002ZB-A	Previous Code SFP / SFPV	MASS[Typ.] 0.0010g
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Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	—	0.55
b	0.25	0.30	0.35
c	0.08	0.13	0.18
D	0.55	0.60	0.65
E	0.90	1.00	1.10
H_E	1.30	1.40	1.50
ϕb	—	0.50	—
e_1	—	1.40	—

Not recommend for new design

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