

HSM2836C

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0554-0500
Rev.5.00
Dec 12, 2008

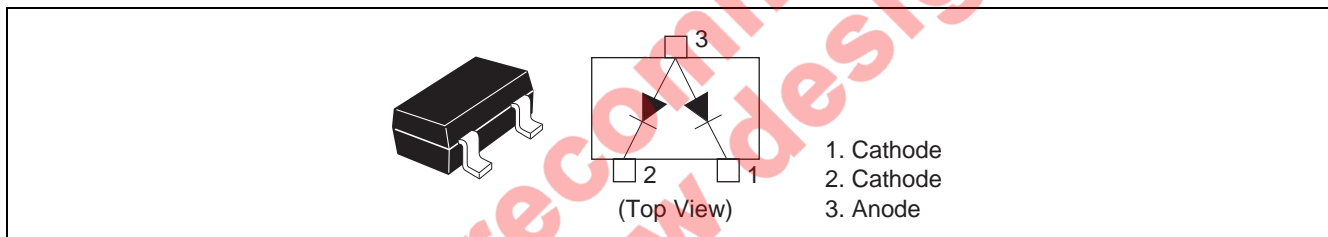
Features

- Fast recovery time. ($t_{rr} = 20\text{ns max}$)
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HSM2836CTL	A4	MPAK	PLSP0003ZC-A	TL (3,000 pcs / reel)
HSM2836CTR	A4	MPAK	PLSP0003ZC-A	TR (3,000 pcs / reel)

Pin Arrangement



Not recommended for new design

Absolute Maximum Ratings *¹

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM}	85	V
Reverse voltage	V _R	80	V
Peak forward current	I _{FM}	300	mA
Non-Repetitive peak forward surge current	I _{FSM} * ²	4	A
Average rectified current	I _o	100	mA
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. Per one device.

2. Within 1 μs forward surge current.

Electrical Characteristics *¹

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V _F	—	0.72	1.0	V	I _F = 10 mA
	V _F	—	0.83	1.0	V	I _F = 50 mA
	V _F	—	0.90	1.2	V	I _F = 100 mA
Reverse current	I _R	—	—	0.1	μA	V _R = 80 V
Capacitance	C	—	2.5	4.0	pF	V _R = 0 V, f = 1 MHz
Reverse recovery time	t _{rr}	—	—	20	ns	I _F = 10 mA, V _R = 6 V, R _L = 50 Ω

Note: 1. Per one device.

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Main Characteristics

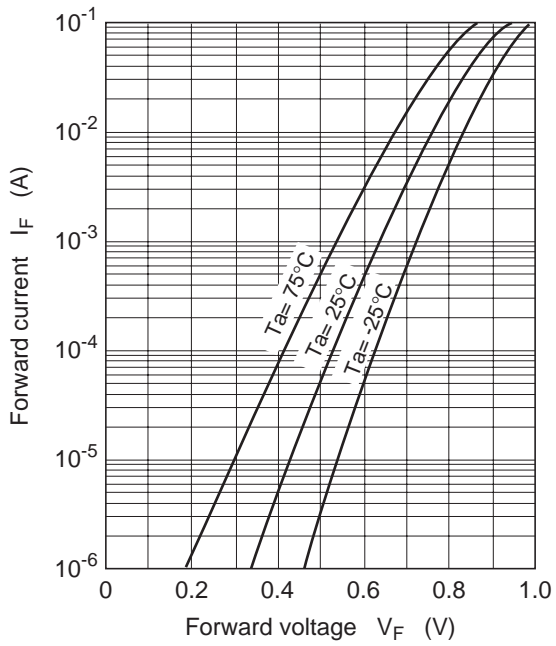


Fig.1 Forward current vs. Forward voltage

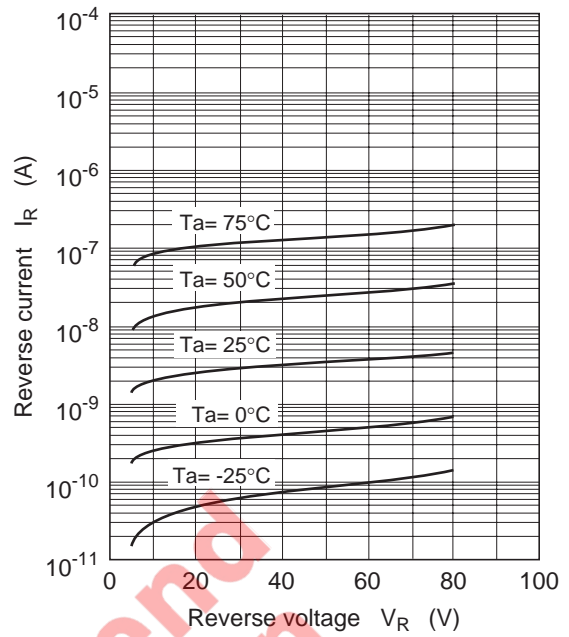


Fig.2 Reverse current vs. Reverse voltage

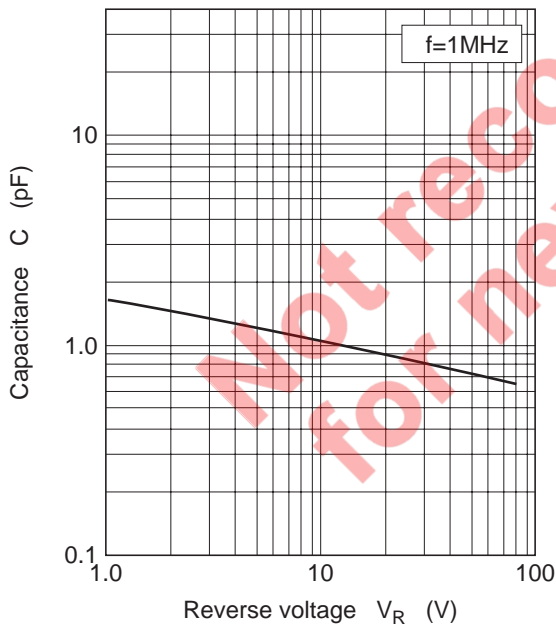
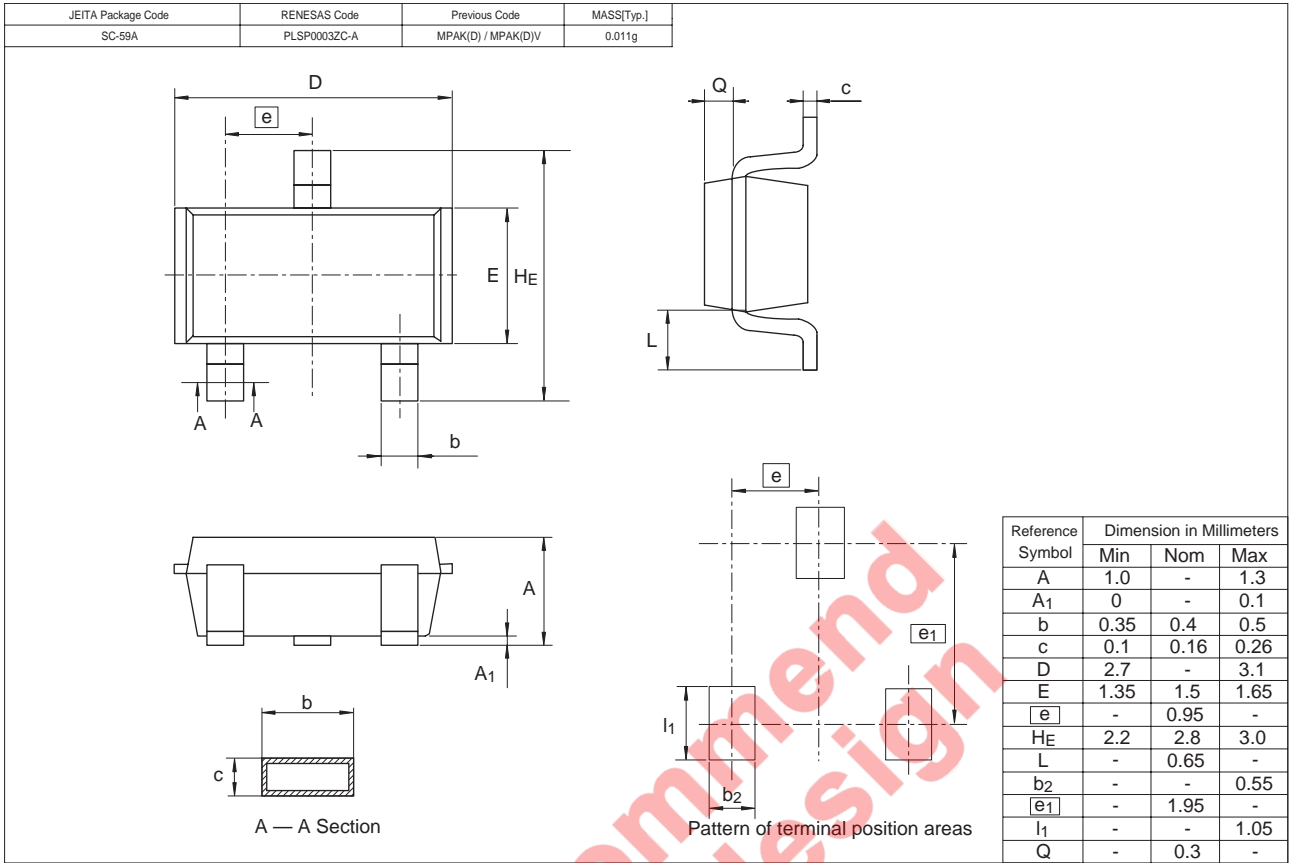


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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April 1st, 2010
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