

HSM276ASR

Silicon Schottky Barrier Diode for Balanced Mixer

REJ03G0609-0100
(Previous: ADE-208-840)
Rev.1.00
Apr 25, 2005

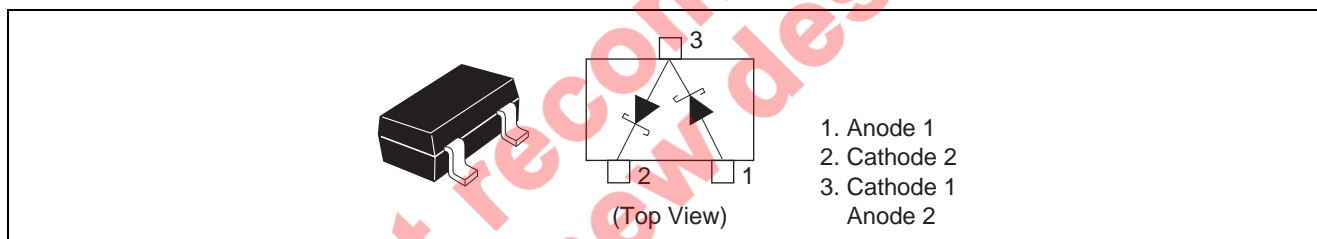
Features

- High forward current, Low capacitance.
- HSM276ASR which is interconnected in series configuration is designed for balanced mixer use.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HSM276ASR	S20	MPAK	PLSP0003ZC-A (MPAK)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	5	V
Reverse voltage	V_R	3	V
Average rectified current	I_O^{*1}	30	mA
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Note: 1. Per one device

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V_R	3	—	—	V	$I_R = 1 \text{ mA}$
Reverse current	I_R	—	—	50	μA	$V_R = 0.5 \text{ V}$
Forward current	I_F	35	—	—	mA	$V_F = 0.5 \text{ V}$
Capacitance	C	—	—	0.90	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
Capacitance deviation	ΔC	—	—	0.10	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
ESD-Capability *2	—	30	—	—	V	C = 200 pF, R = 0 Ω, Both forward and reverse direction 1 pulse.

Notes: 1. Per one device

2. Failure criterion ; $I_R \geq 100 \text{ μA}$ at $V_R = 0.5 \text{ V}$

Main Characteristic

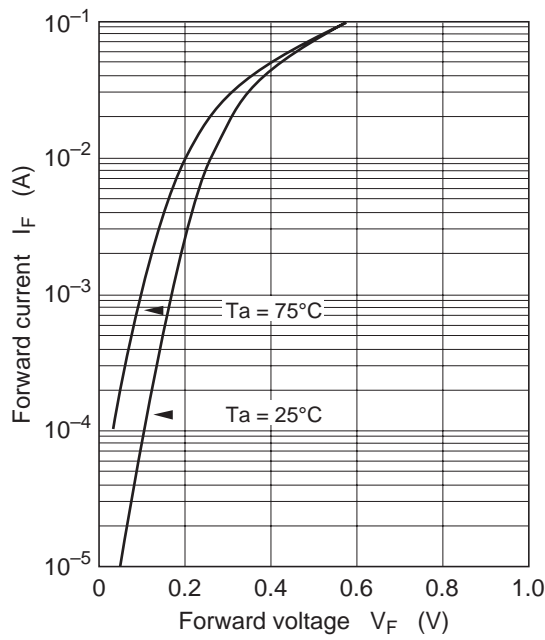


Fig.1 Forward current vs. Forward voltage

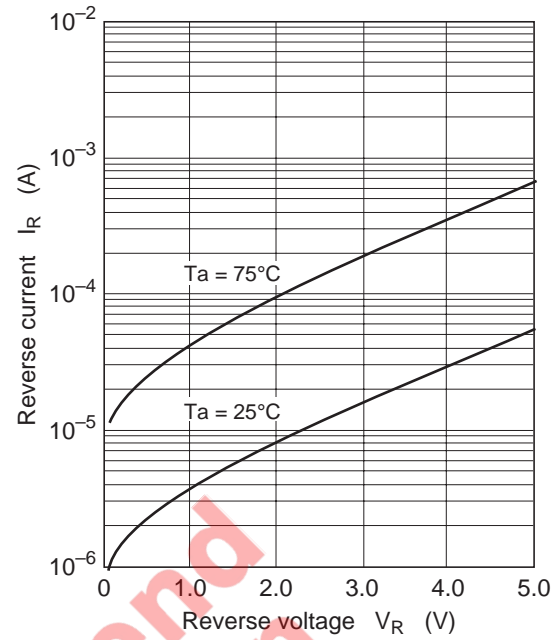


Fig.2 Reverse current vs. Reverse voltage

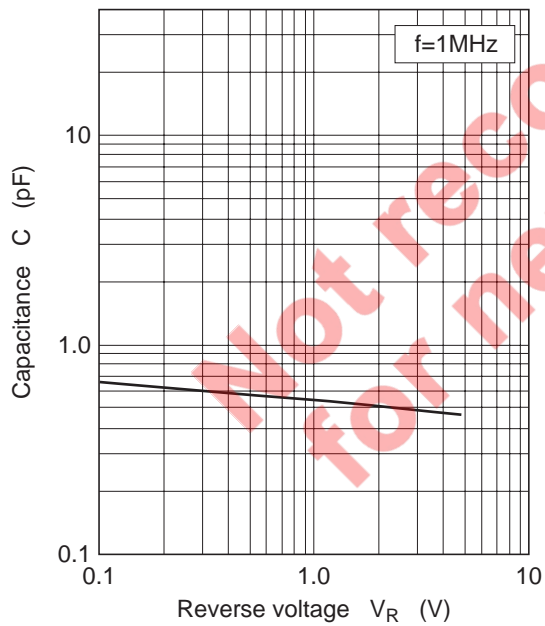
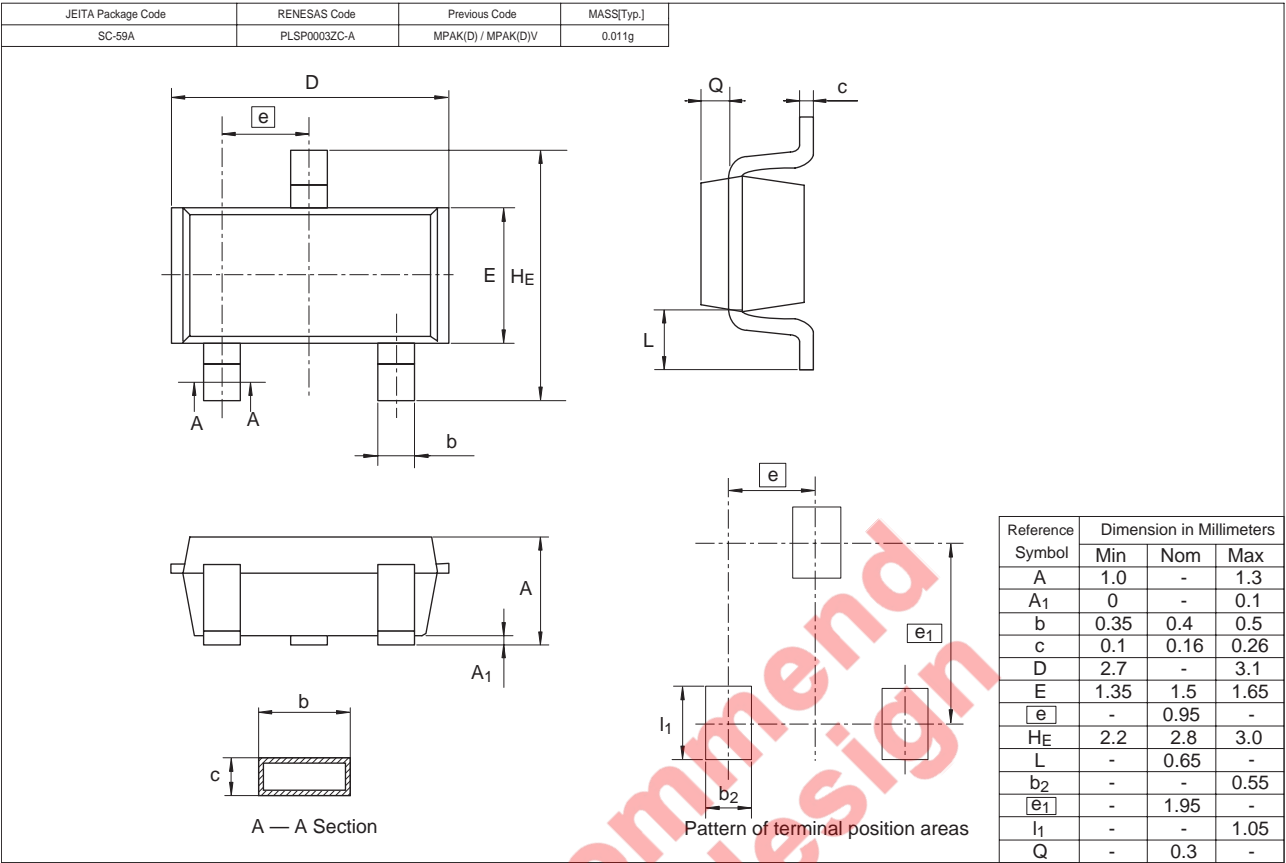


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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Tel: <852> 2265-6688, Fax: <852> 2730-6071

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10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

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April 1st, 2010
Renesas Electronics Corporation

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