

HRB0103B

Silicon Schottky Barrier Diode for Low Voltage High Speed Switching , Rectifying

REJ03G0617-0200
(Previous: ADE-208-491A)
Rev.2.00
May 10, 2005

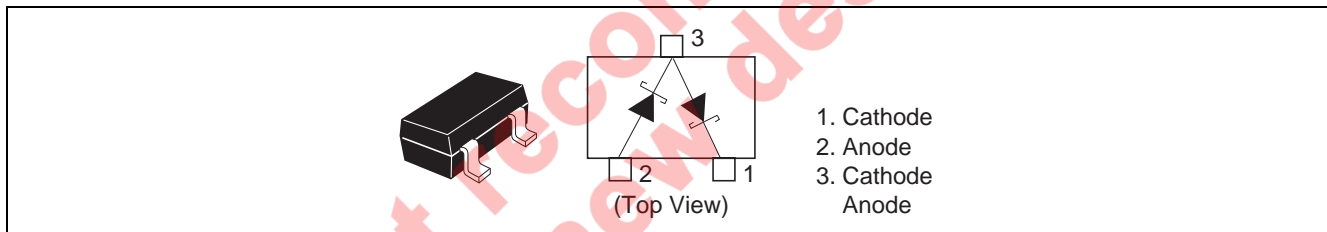
Features

- Low forward voltage drop and suitable for high efficiency forward current.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HRB0103B	E2	CMPAK	PTSP0003ZB-A (CMPAK)

Pin Arrangement



Absolute Maximum Ratings *1

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average rectified current	I_O^{*2}	100	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*3}	3	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Notes: 1. Per one device.
 2. See Fig.5, Two device total.
 3. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.44	V	$I_F = 100 \text{ mA}$
Reverse current	I_R	—	—	50	μA	$V_R = 30 \text{ V}$

Not recommended
for new design

Main Characteristic

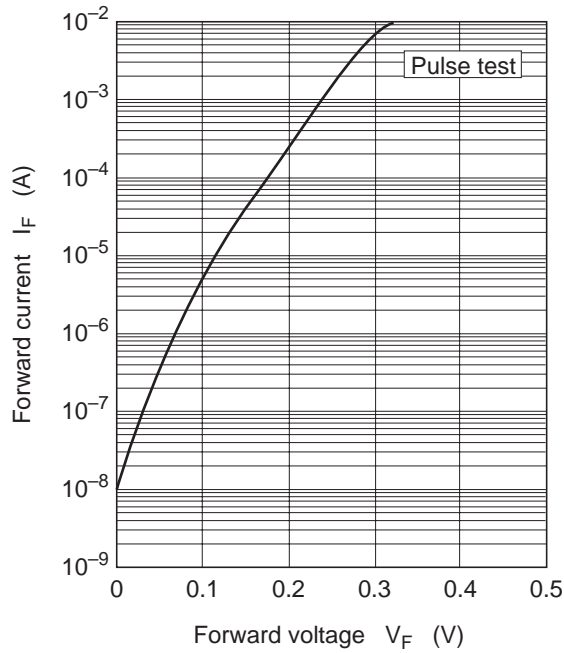


Fig.1 Forward current vs. Forward voltage

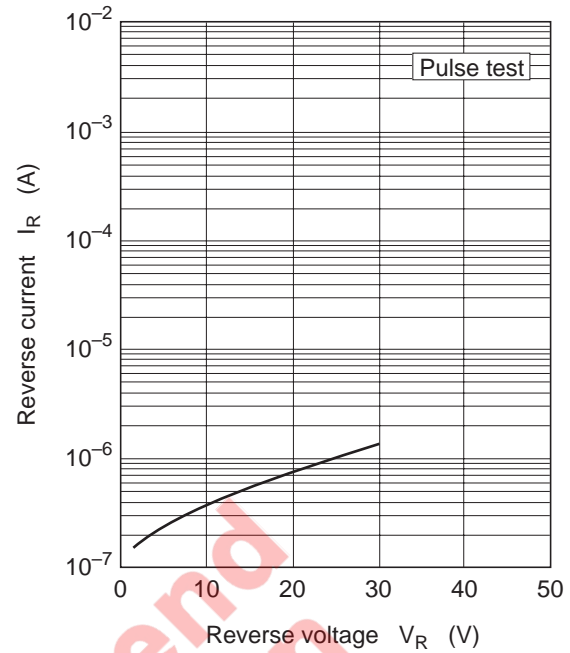


Fig.2 Reverse current vs. Reverse voltage

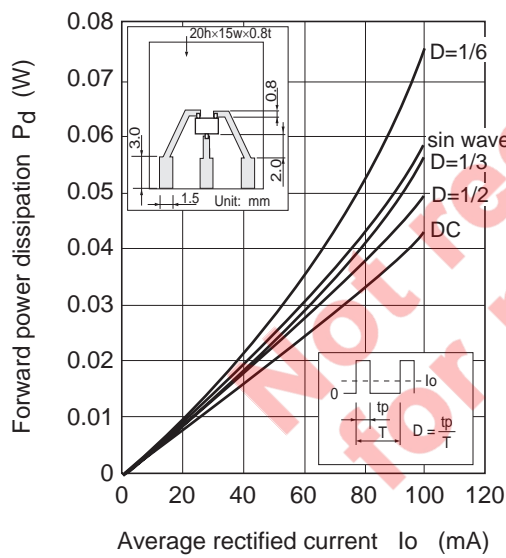


Fig3. Forward power dissipation vs. Average rectified current

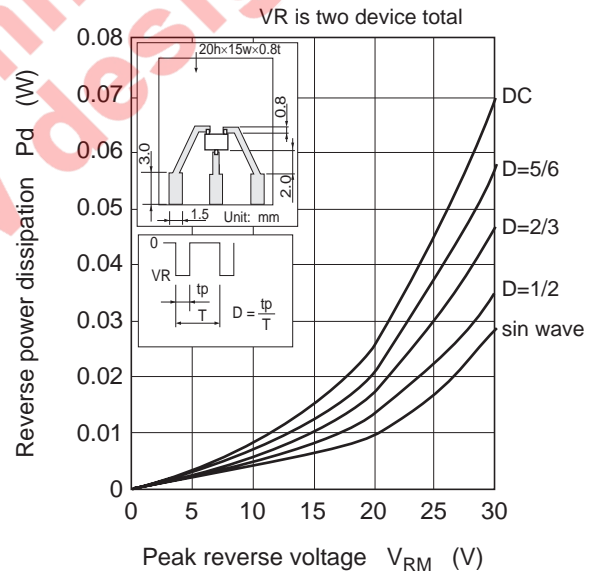


Fig4. Forward power dissipation vs. Peak reverse voltage

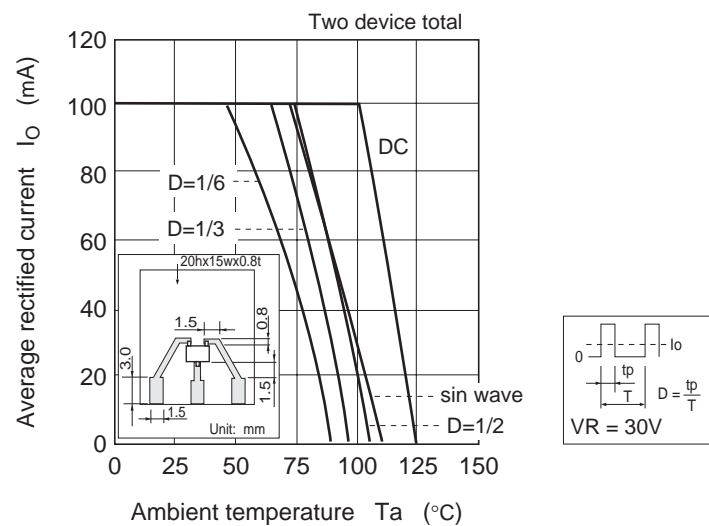
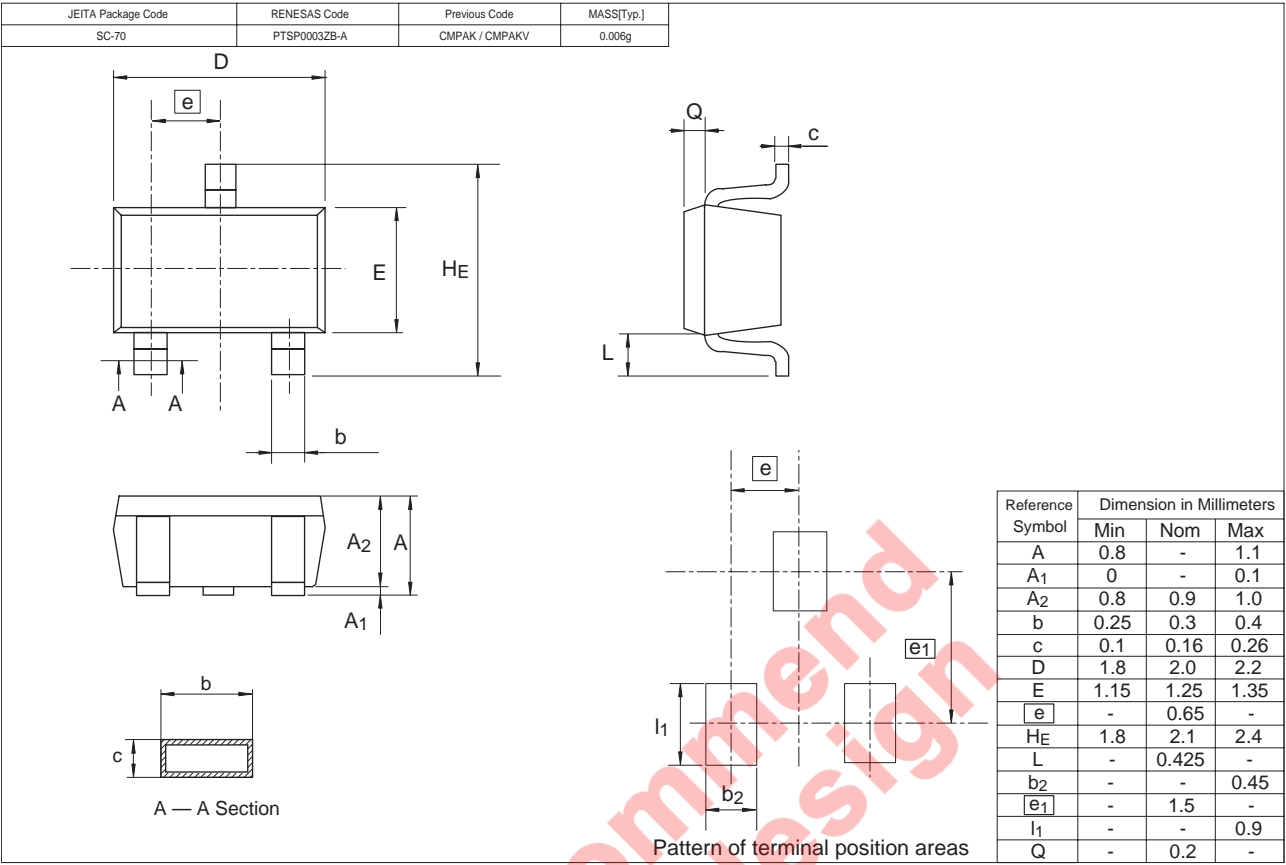


Fig.5 Average rectified current vs. Ambient temperature

Not recommended
for new design

Package Dimensions



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April 1st, 2010
Renesas Electronics Corporation

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