

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.
2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.
4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.
6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.
 - “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.
 - “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.
 - “Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.
8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

HD74LV2GT123A

Retriggerable Monostable Multivibrator / CMOS Logic Level Shifter

REJ03D0004-0300Z

Rev.3.00

Oct.22.2003

Description

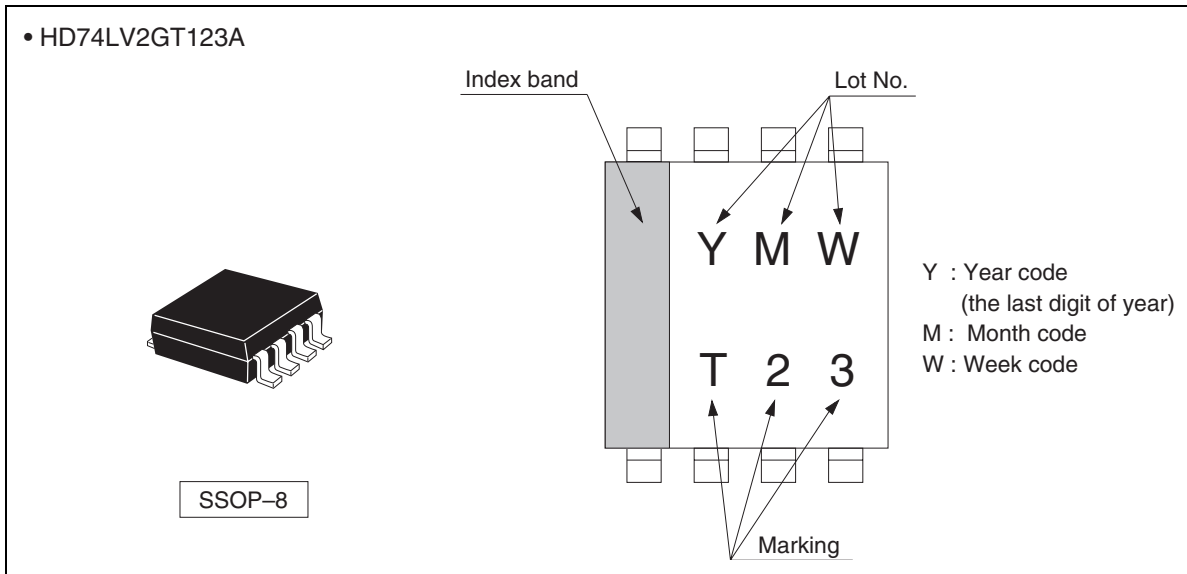
The HD74LV2GT123A features output pulse duration control by three methods. In the first method, the \overline{A} input is low and the B input goes high. In the second method, the B input is high and the \overline{A} input goes low. In the third method, the \overline{A} input is low, the B input is high, and the clear (\overline{CLR}) input goes high. The basic pulse duration is programmed by selecting external resistance and capacitance values. The external timing capacitor must be connected between Cext and Rext/Cext (positive) and an external resistor connected between Rext/Cext and V_{CC} . To obtain variable pulse durations, connect an external variable resistance between Rext/Cext and V_{CC} . Once triggered, the basic pulse duration can be extended by retriggering the gated low level active (\overline{A}) or high level active (B) input. Pulse duration can be reduced by taking \overline{CLR} low. The output pulse equation is simply : $t_{WQ} = Cext \bullet Rext$. The input protection circuitry on this device allows over voltage tolerance on the input, allowing the device to be used as a logic-level translator from 3.0 V CMOS Logic to 5.0 V CMOS Logic or from 1.8 V CMOS logic to 3.0 V CMOS Logic while operating at the high-voltage power supply. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Control input is TTL compatible input level.
Supply voltage range : 3.0 to 5.5 V
Operating temperature range : -40 to +85°C
- Logic-level translate function
3.0 V CMOS logic → 5.0 V CMOS logic (@ $V_{CC} = 5.0$ V)
1.8 V or 2.5 V CMOS logic → 3.3 V CMOS logic (@ $V_{CC} = 3.3$ V)
- All inputs V_{IH} (Max.) = 5.5 V (@ $V_{CC} = 0$ V to 5.5 V)
All outputs V_O (Max.) = 5.5 V (@ $V_{CC} = 0$ V)
- Output current ± 6 mA (@ $V_{CC} = 3.0$ V to 3.6 V), ± 12 mA (@ $V_{CC} = 4.5$ V to 5.5 V)
- All the logical inputs have hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV2GT123AUSE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs / Reel)

Outline and Article Indication



Function Table

Inputs

CLR	\bar{A}	B	Output Q
L	X	X	L
H	H	X	L
H	X	L	L
H	L	↑	⎓
H	↓	H	⎓
↑	L	H	⎓

H : High level

L : Low level

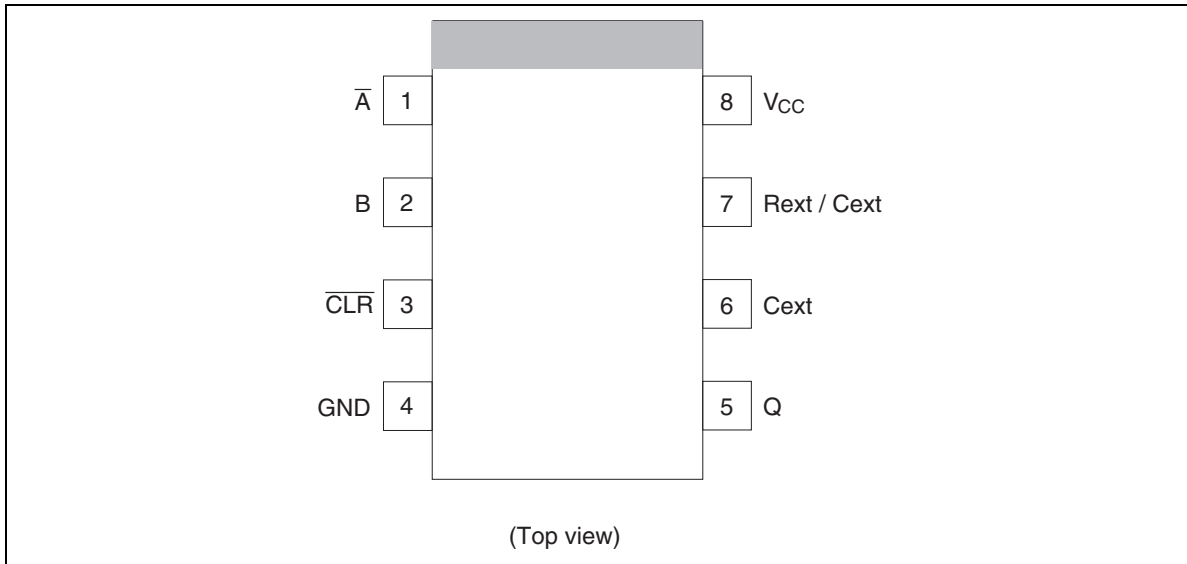
X : Immaterial

↑ : Low to high transition

↓ : High to low transition

⎓ : High level pulse

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range ^{*1}	V_I	-0.5 to 7.0	V	
Output voltage range ^{*1,2}	V_O	-0.5 to $V_{CC} + 0.5$ -0.5 to 7.0	V	Output : H or L V_{CC} : OFF
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	± 50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	± 25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 50	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) ^{*3}	P_T	200	mW	
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$	

- Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
 2. This value is limited to 5.5 V maximum.
 3. The maximum package power dissipation was calculated using a junction temperature of 150 $^\circ\text{C}$.

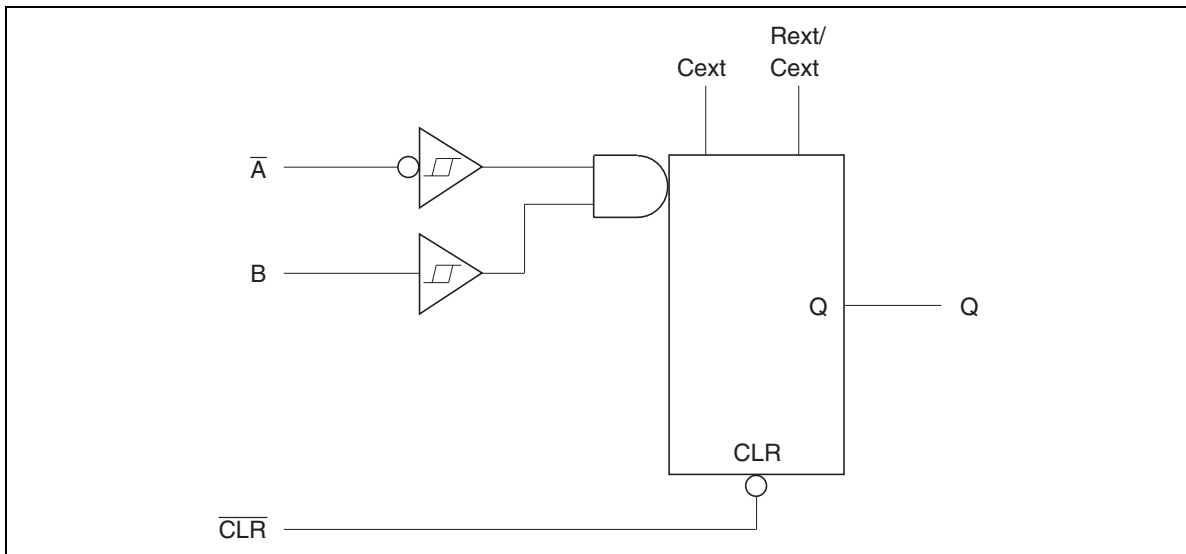
HD74LV2GT123A

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit	Conditions
Supply voltage range	V_{CC}	3.0	—	5.5	V	
Input voltage range	V_I	0	—	5.5	V	
Output voltage range	V_O	0	—	V_{CC}	V	
Output current	I_{OH}	—	—	-6	mA	$V_{CC} = 3.0$ to 3.6 V
		—	—	-12		$V_{CC} = 4.5$ to 5.5 V
	I_{OL}	—	—	6		$V_{CC} = 3.0$ to 3.6 V
		—	—	12		$V_{CC} = 4.5$ to 5.5 V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	—	100	ns / V	$V_{CC} = 3.0$ to 3.6 V
		0	—	20		$V_{CC} = 4.5$ to 5.5 V
External timing resistance	R_{ext}	1	—	—	k Ω	$V_{CC} = 4.5$ to 5.5 V
External capacitance	C_{ext}	—	Unlimited	—	F	
Supply transition rise rate	$\Delta t / \Delta V_{CC}$	1	—	—	ms / V	
Operating free-air temperature	T_a	-40	—	85	$^{\circ}\text{C}$	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



Electrical Characteristic

- $T_a = -40$ to 85°C

Item	Symbol	V_{CC} (V) *	Min	Typ	Max	Unit	Test condition
Input voltage	V_{IH}	3.0 to 3.6	1.5	—	—	V	
		4.5 to 5.5	2.0	—	—		
	V_{IL}	3.0 to 3.6	—	—	0.6		
		4.5 to 5.5	—	—	0.8		
Hysteresis voltage	V_H	3.3	—	0.10	—	V	$V_{T^+} - V_{T^-}$
		5.0	—	0.15	—		
Output voltage	V_{OH}	Min to Max	$V_{CC}-0.1$	—	—	V	$I_{OH} = -50 \mu\text{A}$
		3.0	2.48	—	—		$I_{OH} = -6 \text{ mA}$
		4.5	3.8	—	—		$I_{OH} = -12 \text{ mA}$
	V_{OL}	Min to Max	—	—	0.1		$I_{OL} = 50 \mu\text{A}$
		3.0	—	—	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	—	0.55		$I_{OL} = 12 \text{ mA}$
Input current	I_{IN}	0 to 5.5	—	—	± 1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Input current Rext / Cext	I_{IN}	5.5	—	—	± 2.5	μA	$V_{IN} = V_{CC} \text{ or GND}$
Quiescent supply current	I_{CC}	5.5	—	—	10	μA	$V_{IN} = V_{CC} \text{ or GND,}$ $I_O = 0$
	I_{CC-T}	5.5	—	—	1.5	mA	One input $V_{IN} = 3.4 \text{ V,}$ other input $V_{CC} \text{ or}$ GND
Active state supply current	ΔI_{CC}	4.5	—	—	650	μA	$V_{IN} = V_{CC} \text{ or GND}$ Rext / Cext = $0.5V_{CC}$
		5.5	—	—	975		
Output leakage current	I_{OFF}	0	—	—	5	μA	$V_{IN} \text{ or } V_O = 0 \text{ to } 5.5 \text{ V}$
Input capacitance	C_{IN}	5.0	—	3.0	—	pF	$V_{IN} = V_{CC} \text{ or GND}$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

HD74LV2GT123A

Switching Characteristics

- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	10.0	21.0	1.0	24.0	ns	$C_L = 15 \text{ pF}$	\bar{A} or B	Q
		—	11.5	24.5	1.0	27.5		$C_L = 50 \text{ pF}$		
	t_{PHL}	—	8.0	16.0	1.0	18.5	ns	$C_L = 15 \text{ pF}$	$\overline{\text{CLR}}$	Q
		—	9.5	19.5	1.0	22.0		$C_L = 50 \text{ pF}$		
	t_{PLH}	—	10.0	22.5	1.0	26.0	ns	$C_L = 15 \text{ pF}$	$\overline{\text{CLR}}$	Q
		—	11.5	26.0	1.0	29.5		$C_L = 50 \text{ pF}$	(Trigger)	
Output pulse width	t_{wQ}	—	150	240	—	300	ns	$C_L = 50 \text{ pF}$, $C_{ext} = 28 \text{ pF}$, $R_{ext} = 2 \text{ k}\Omega$		
		90	100	110	90	110	μs	$C_L = 50 \text{ pF}$, $C_{ext} = 0.01 \text{ }\mu\text{F}$, $R_{ext} = 10 \text{ k}\Omega$		
		0.9	1.0	1.1	0.9	1.1	ms	$C_L = 50 \text{ pF}$, $C_{ext} = 0.1 \text{ }\mu\text{F}$, $R_{ext} = 10 \text{ k}\Omega$		
Pulse width	t_w	5.0	—	—	5.0	—	ns	\bar{A} , B or $\overline{\text{CLR}}$		
Retrigger time	t_{rr}	—	30	—	—	—	ns	\bar{A} or B ($R_{ext} = 1 \text{ k}\Omega$, $C_{ext} = 100 \text{ pF}$)		
		—	1.2	—	—	—	μs	\bar{A} or B ($R_{ext} = 1 \text{ k}\Omega$, $C_{ext} = 0.01 \text{ }\mu\text{F}$)		

- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

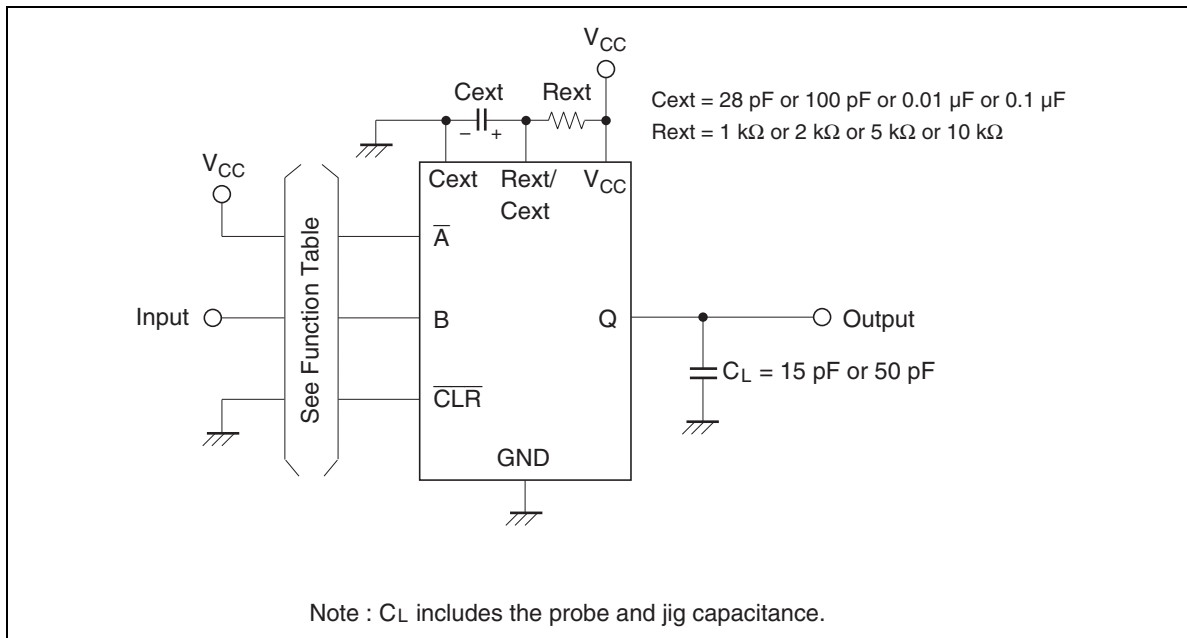
Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	7.3	12.0	1.0	14.0	ns	$C_L = 15 \text{ pF}$	\bar{A} or B	Q
		—	8.5	14.0	1.0	16.0		$C_L = 50 \text{ pF}$		
	t_{PHL}	—	5.9	9.4	1.0	11.0	ns	$C_L = 15 \text{ pF}$	$\overline{\text{CLR}}$	Q
		—	7.5	11.4	1.0	13.0		$C_L = 50 \text{ pF}$		
	t_{PLH}	—	7.3	12.9	1.0	15.0	ns	$C_L = 15 \text{ pF}$	$\overline{\text{CLR}}$	Q
		—	8.7	14.9	1.0	17.0		$C_L = 50 \text{ pF}$	(Trigger)	
Output pulse width	t_{wQ}	—	140	200	—	240	ns	$C_L = 50 \text{ pF}$, $C_{ext} = 28 \text{ pF}$, $R_{ext} = 2 \text{ k}\Omega$		
		90	100	110	90	110	μs	$C_L = 50 \text{ pF}$, $C_{ext} = 0.01 \text{ }\mu\text{F}$, $R_{ext} = 10 \text{ k}\Omega$		
		0.9	1.0	1.1	0.9	1.1	ms	$C_L = 50 \text{ pF}$, $C_{ext} = 0.1 \text{ }\mu\text{F}$, $R_{ext} = 10 \text{ k}\Omega$		
Pulse width	t_w	5.0	—	—	5.0	—	ns	\bar{A} , B or $\overline{\text{CLR}}$		
Retrigger time	t_{rr}	—	20	—	—	—	ns	\bar{A} or B ($R_{ext} = 1 \text{ k}\Omega$, $C_{ext} = 100 \text{ pF}$)		
		—	0.95	—	—	—	μs	\bar{A} or B ($R_{ext} = 1 \text{ k}\Omega$, $C_{ext} = 0.01 \text{ }\mu\text{F}$)		

Operating Characteristics

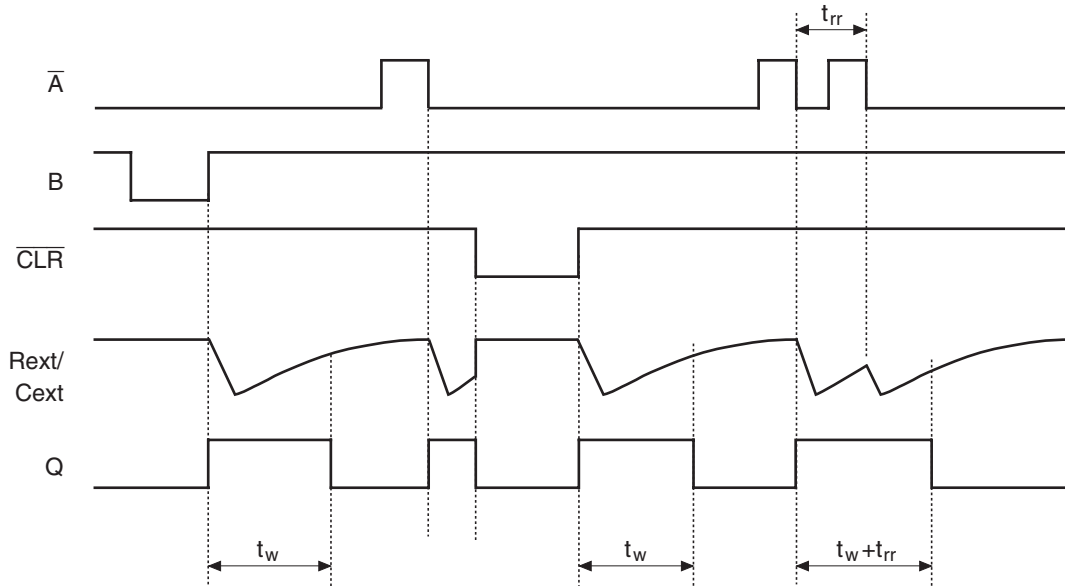
- $C_L = 50 \text{ pF}$

Item	Symbol	$V_{CC} \text{ (V)}$	$T_a = 25^\circ\text{C}$			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C_{PD}	5.0	—	31.0	—	pF	$f = 10 \text{ MHz}$

Test Circuit



Timing Diagram



Caution in use

In order to prevent any malfunctions due to noise, connect a high frequency performance capacitor between Vcc and GND, and keep the wiring between the External components and Cext, Rext/Cext pins as short as possible.

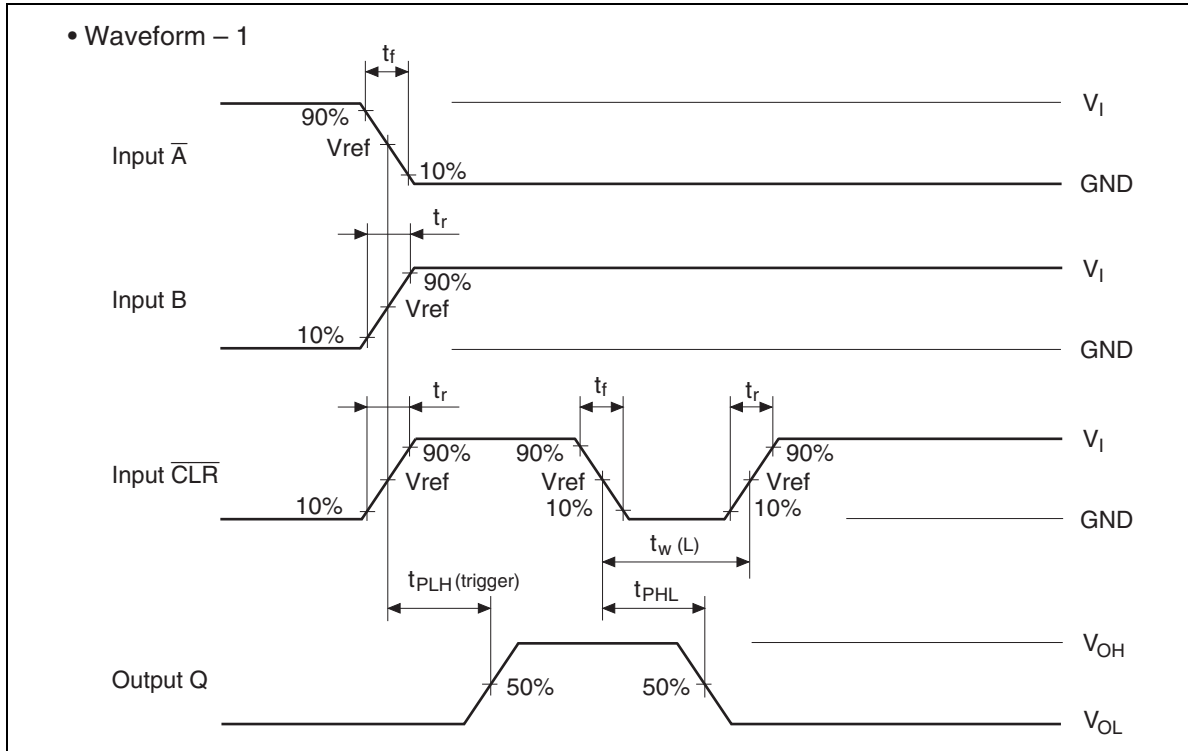
Large values of Cext may cause problems when powering down the HD74LV2GT123A because of the amount of energy stored in the capacitor. When a system containing this device is powered down, the capacitor may discharge from Vcc through the protection diodes at pin 7 pin.

Current through the input protection diodes must be limited to 20 mA; therefore, the turn-off time of the Vcc power supply must not be faster than $t = V_{cc} \cdot C_{ext} / (20 \text{ mA})$. For example, if $V_{cc} = 5 \text{ V}$ and $C_{ext} = 22 \mu\text{F}$, the Vcc supply must turn off no faster than $t = (5 \text{ V}) \cdot (22 \mu\text{F}) / 20 \text{ mA} = 5.5 \text{ ms}$. This is usually not a problem because power supplies are heavily filtered and cannot discharge at this rate.

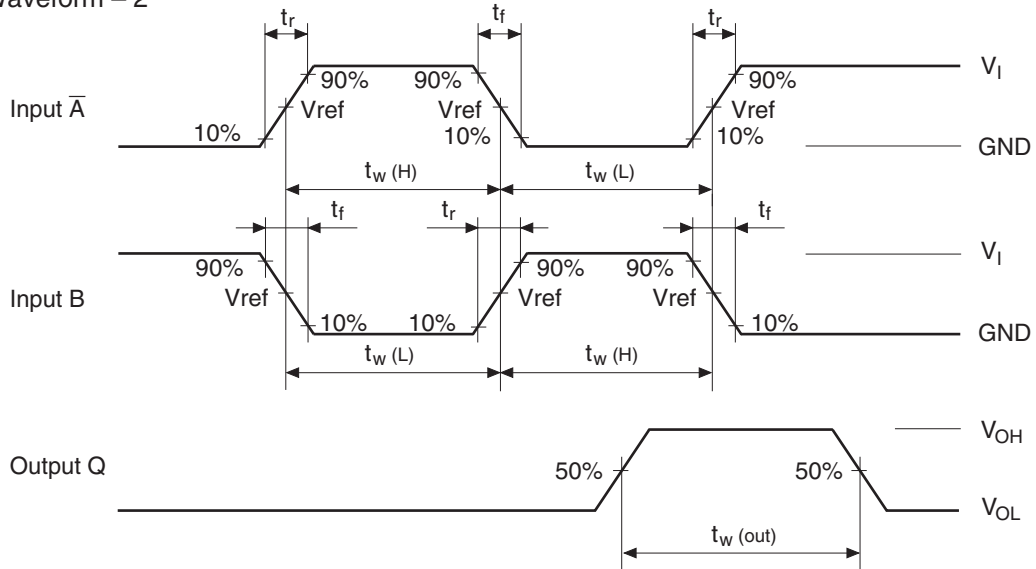
When a more rapid decrease of Vcc to zero volts occurs, the HD74LV2GT123A may sustain damage.

To avoid this possibility, use an external clamping diode.

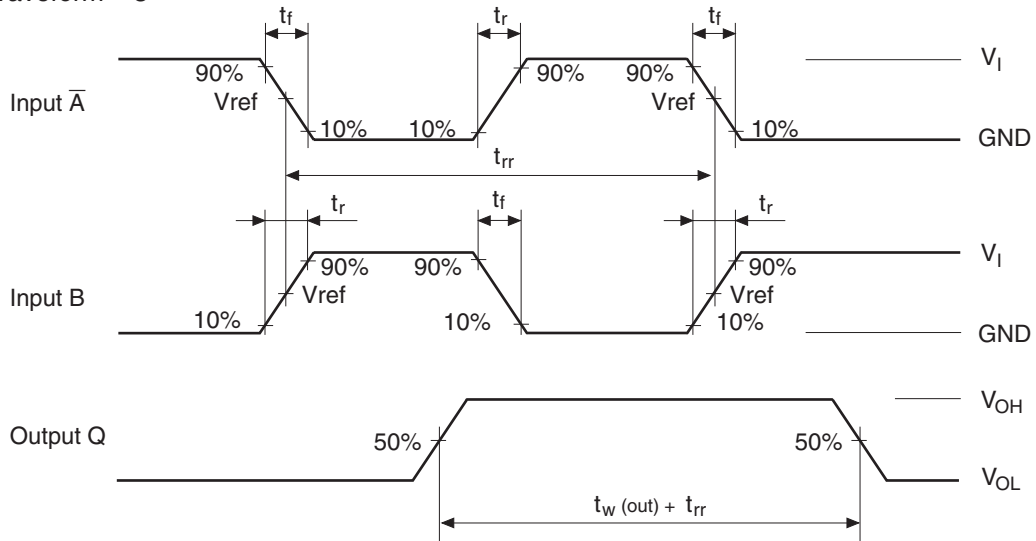
The input pins for unused circuit should be used under conditions to fix the outputs to avoid malfunction caused by noises. Also, it's recommended that Rext / Cext terminals are open and external parts are not connected to.



• Waveform – 2



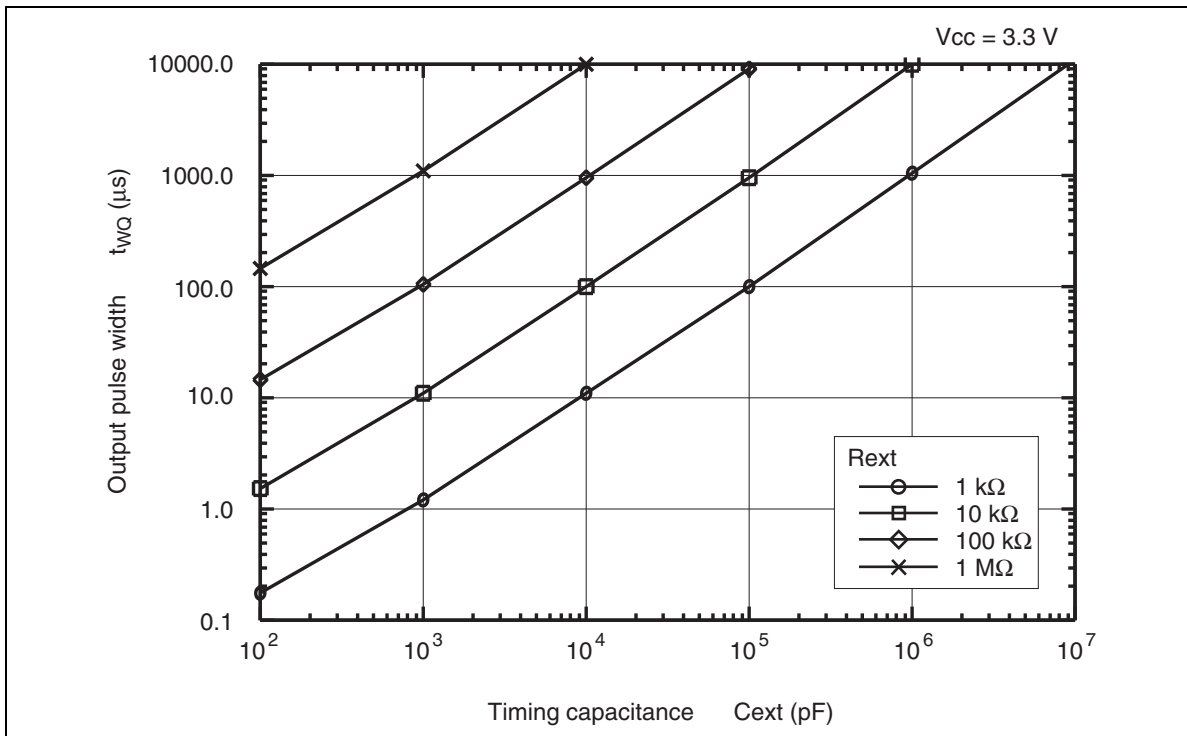
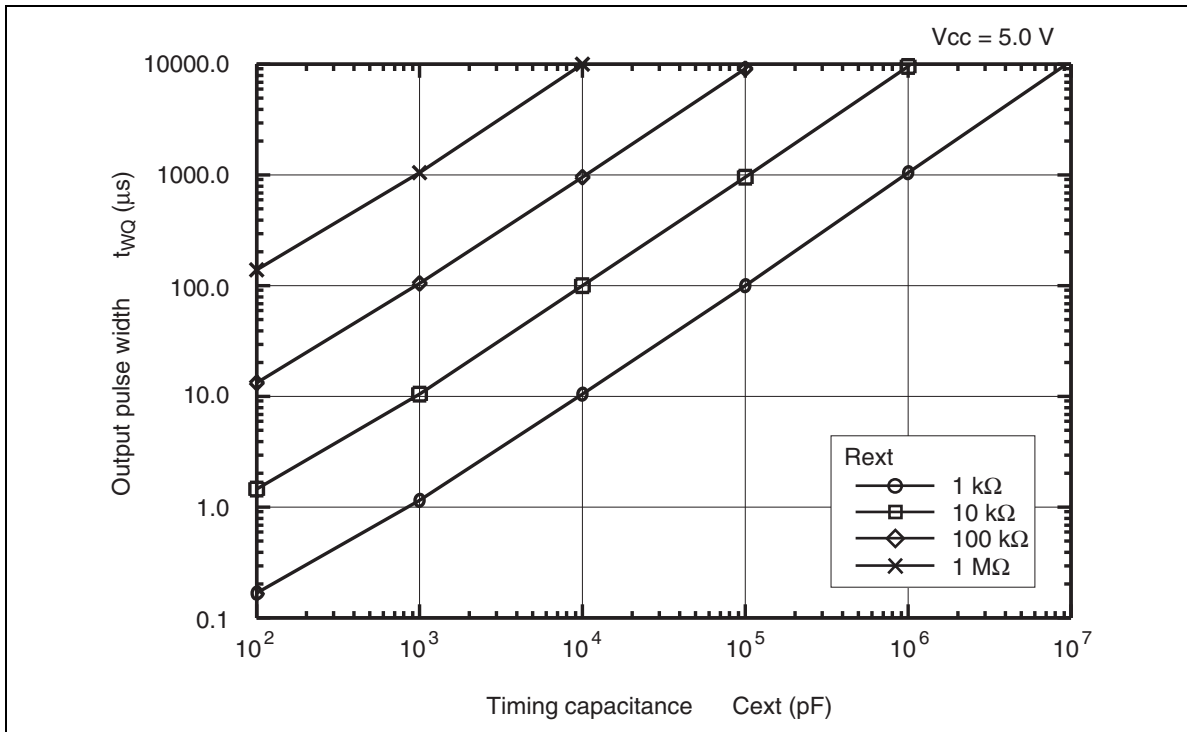
• Waveform – 3

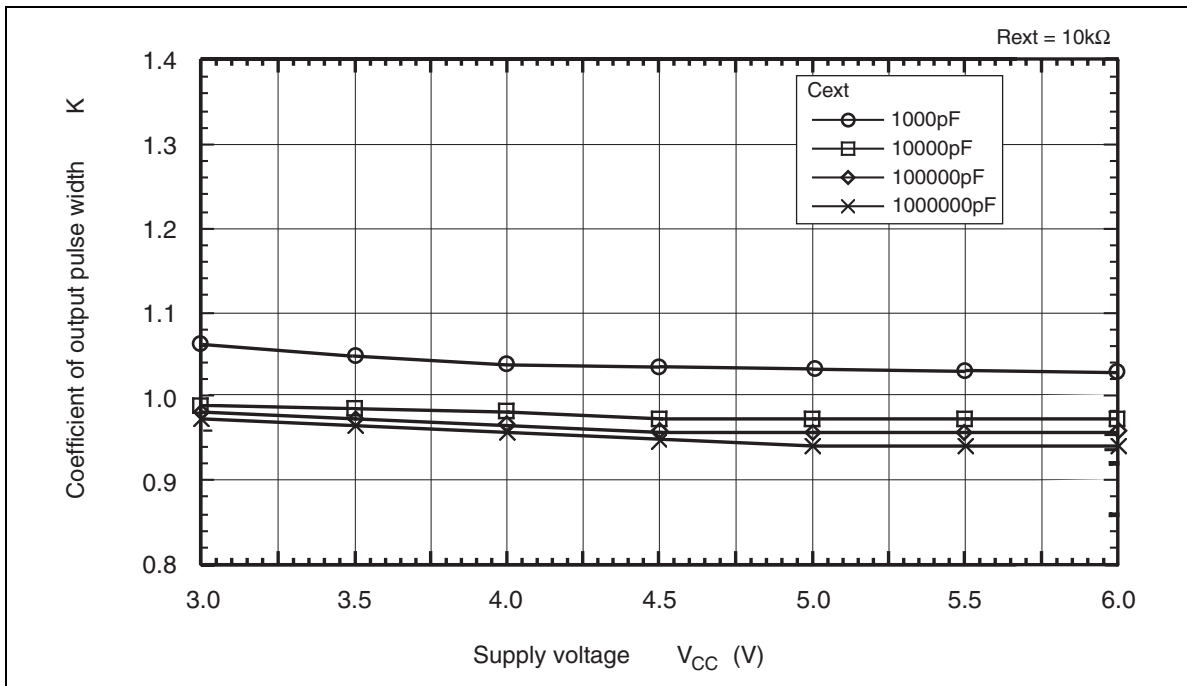
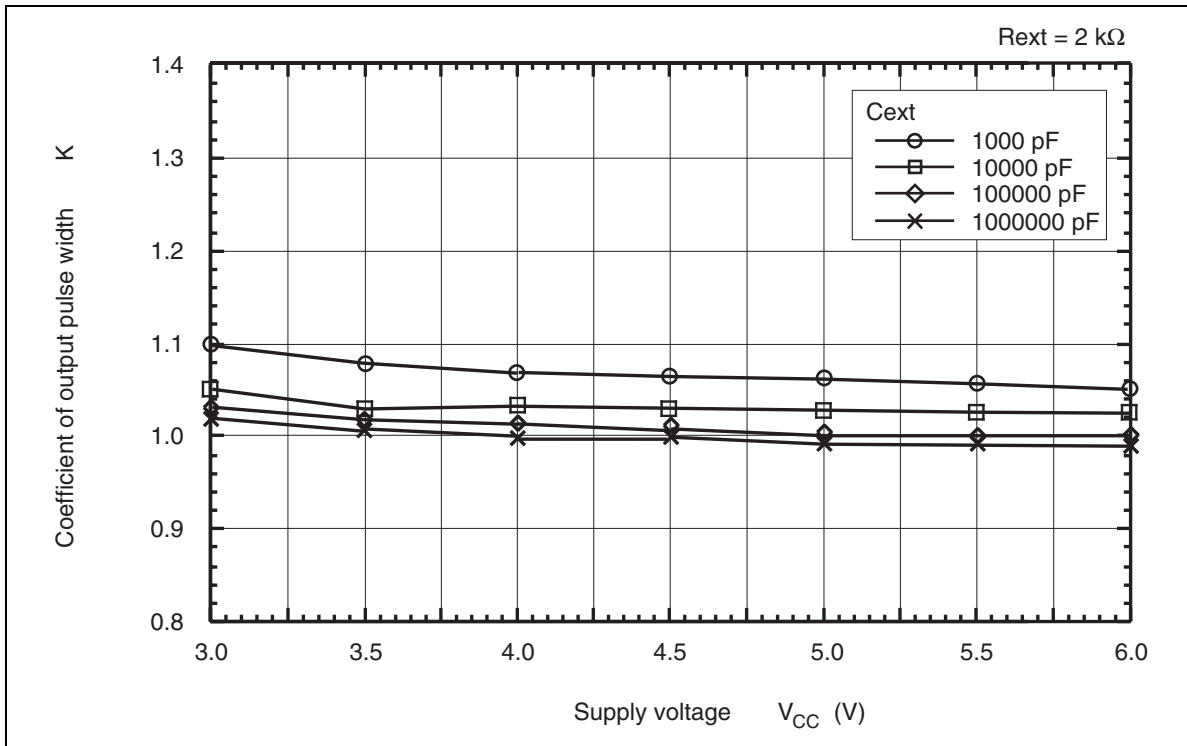


V_{CC} (V)	INPUTS		Vref
	V_I	t_r / t_f	
3.3±0.3	2.5 V	≤ 3.0 ns	50%
5.0±0.5	3 V	≤ 3.0 ns	1.5 V

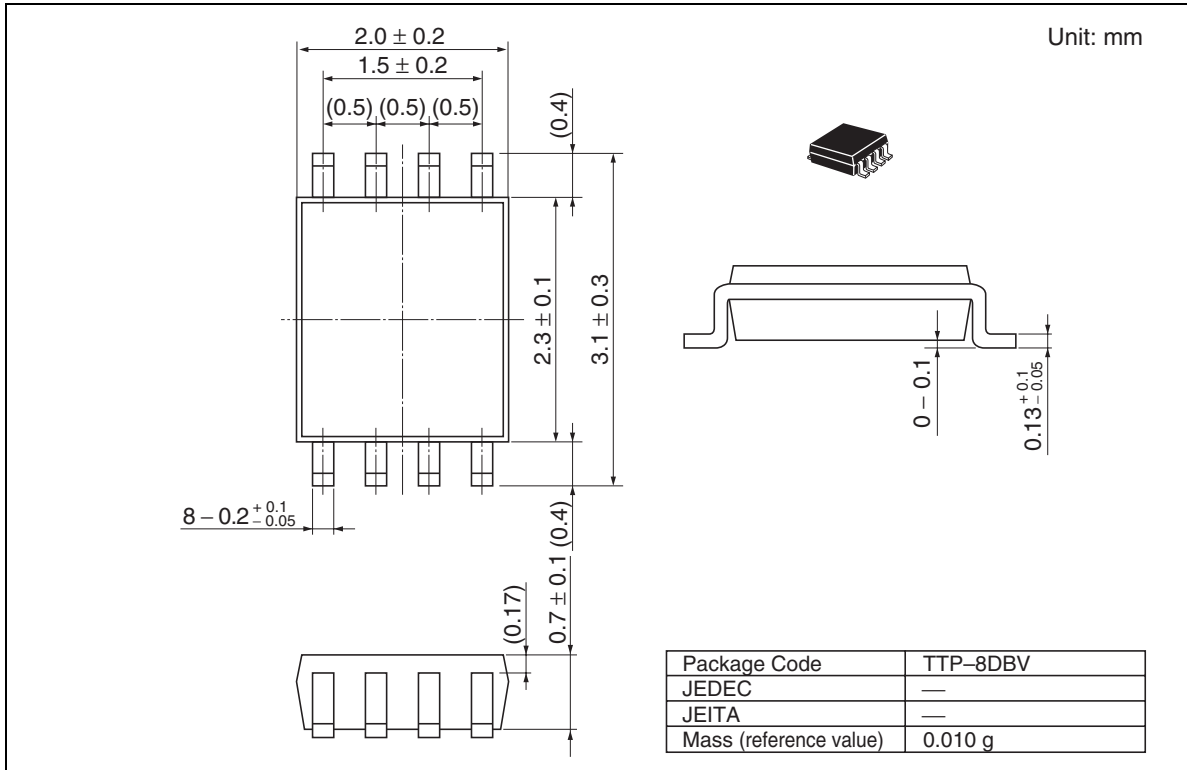
- Notes: 1. Input waveform: PRR ≤ 1 MHz, $Z_o = 50 \Omega$.
 2. The output are measured one at a time with one transition per measurement.

Application Data





Package Dimensions



RENESAS Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
 2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
 3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
The information described here may contain technical inaccuracies or typographical errors.
Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (<http://www.renesas.com>).
 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
 5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.
-



RENESAS SALES OFFICES

<http://www.renesas.com>

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom
Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH
Dornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd.
7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd.
FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001